A Report on Three-day Workshop
on

"IPR- Patents & Designs filing Process"

Organized by Department of Electrical and Electronics Engineering

in association with Rajiv Gandhi National Institute of Intellectual Property Management (RGNIIPM),

IQAC-SVEC, IPR Cell- SVEC

under National Intellectual Property Awareness Mission (NIPAM)

from 7th to 09th Dec 2021

The Three-Day National Level Workshop on **"IPR- Patents & Designs filing Process"** was organized by the Department of Electrical & Electronics Engineering in association with Rajiv Gandhi National Institute of Intellectual Property Management (RGNIIPM), IQAC-SVEC, IPR Cell- SVEC under National Intellectual Property Awareness Mission (NIPAM) from 07 to 09 Dec 2021.

The Workshop received an overwhelming response with 90 participants from in and around engineering colleges of the country.

The endeavours were successful in creating a learning environment for the faculty members in filing a patent. The FDP has also provided a forum for interacting and sharing ideas among the faculty and opened new avenues for academic collaborative activities.

The proceeding of the National level Workshop was inaugurated on 06th Dec 2021.

1. Pankaj P Borkar, Dy. Controller of Patents and Design, Rajiv Gandhi National Institute of Intellectual Property Management, Nagpur. O/o CGPDTM, DPIIT, Ministry of Commerce & Industry - Govt. of India.
2. Dr. B. M. Sathish, Principal, Sree Vidyanikethan Engineering College.
3. Dr. M. S. Sujatha, Professor & HOD EEE, Sree Vidyanikethan Engineering College.
4. Dr. S. Farook, Professor & BoS Chairman EEE, Sree Vidyanikethan Engineering College and Convener of the Program.
5. Dr. D. Suresh Babu, Associate Professor EEE, Sree Vidyanikethan Engineering College and Co-Convener of the Program.

Day 1: Inaugural session, 07-12-2021, 11:30 A.M to 12:30 P.M.

Dr. S. Farook, Convener has welcomed the participants and brought out the importance and objectives of the FDP and requested the participants to learn more and disseminate the knowledge gained among the students and peer group as well.
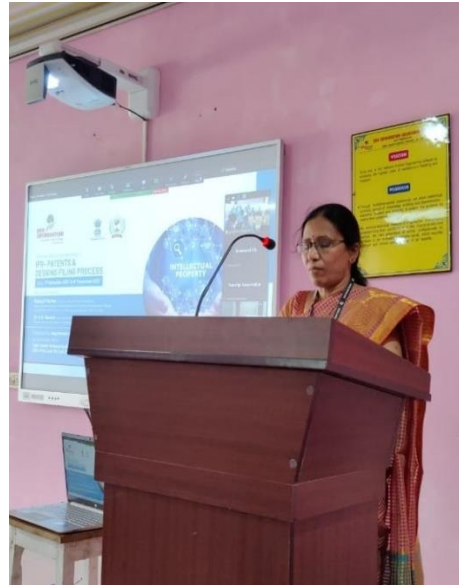
Dr. B. M. Sathish, Principal, SVEC congratulated the organizer to initiate the event and planning for sessions. He also emphasized the participants to utilize the sessions for their individual and team growth.

Dr. M.S. Sujatha, Professor & HOD EEE, Sree Vidyanikethan Engineering College in her welcome address, brought out the importance of these kinds of FDPs organized.

Dr. D. Suresh Babu, Co-convener mentioned as it is the first time the department is organizing a hybrid model of the workshop both online and offline. So, he requested all the participants to make use of the sessions to get some idea for their individual research problems.
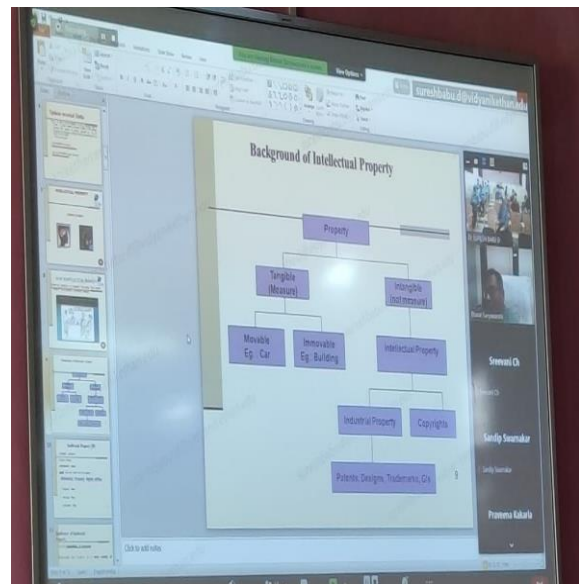
.

Dr. B. M. Sathish, Principal, SVEC delivered the welcome speech and congratulated the organizer to initiate the event and planning for sessions.

Dr. M.S. Sujatha, HOD EEE**,** appreciates the organizers and participants taking part in the learning process of IPR.

Dr. S. Farook, Professor, and chairman BoS EEE and Convener of the program appreciate the participants taking part in the learning process of Blockchain technology.

.

Participants in the inaugural function

Day 1: Technical Session: 07-12-2021, 11:45 A.M to 12:45 P.M.

Speaker: Dr. Bharath N. Suryawanshi, Assistant Controller of Patents & Design, Rajiv Gandhi National Institute of Intellectual Property Management, Nagpur. O/o CGPDTM, DPIIT, Ministry of Commerce & Industry - Govt. of India.

Introduction to Intellectual Property

The session started with the introduction of the importance of Blockchain technology and various platforms used for implementing the technology. Various applications were made understood to different groups of people for their research areas. The hands-on session was carried out further for different algorithms to analyze data.

Day 2 & Day 3: Technical Session: 08-12-2021 & 09-12-2021

Speaker: Dr. K. K. Basheer, Associate Professor, Department of IT.

.

Few Photographs of the session

Dr. D. Suresh Babu

(Convener)