

**2.2.1: The institution assesses the learning levels of the students and organises special Programmes for advanced learners and slow learners.**

SVEC has a robust mechanism for continuous monitoring and evaluation of the students. The students are from different domicile status and scholastic abilities. Based on the performance of the students in diagnostic tests, Formative tests, & Continuous Internal Evaluation and performance in the classes, the students are categorized as advanced and slow learners at the beginning of the class work. This helps to reach out to both the categories of students to learn at their own pace and become competitive.

**Special Programmes for advanced learners:** Advanced learners are identified based on the academic performance, interaction in the theory and laboratory classes. These advanced learners are encouraged to become members of Professional bodies, to take up mini projects for inculcating a research culture, to participate/organize inter-collegiate technical association activities.

Students are encouraged to take up internships at reputed organizations and also to enrol and learn through MOOC programs offered by premiere institutions. They are motivated to participate in Workshops, SDPs, Paper presentations and others. In-house competitions such as Technical quiz, Debate, Group discussion, Problem solving and decision making under Technical associations are conducted. Campus recruitment training is provided. They are also encouraged to participate in Spoken Tutorials offered by IIT, Bombay.

**Special Programmes for slow learners:** SVEC takes special care of slow learners. Mentors monitor academic performance and interact frequently to understand the issues that affect their ability to learn or


impede their academic success. The institution has a robust system to regularly communicate the performance and attendance of the students to parents. Departments conduct remedial classes; provide course material to slow learners as well as for those who are on the verge of dropping out due to arrear subjects. These students are given reinforcement classes in order to improve their performance in the supplementary semester end examinations. The Faculty members also revise the topics as requested by the students, provide question bank and also train in answering the questions to score good marks.

English Language Learning classes through Pearson Me-Pro platform are provided for the benefit of students hailing from vernacular medium and also those whose proficiency in English is weak. Personality Development programs are conducted for students who have good technical skills but unable to express their knowledge.

**Strategies adopted for student improvement:** Departments organize remedial classes to clarify the doubts of the students, provide course contents, question bank and solutions for the difficult numerical problems. Faculty members teach content beyond the syllabus after the completion of the syllabus to enrich the knowledge of the students in the advanced topics. Additional laboratory experiments are also conducted after the completion of the regular laboratory classes. The institution has signed MoUs with many organizations by which students are benefitted with expert lectures from industry experts and also to undertake industrial visits. Students are encouraged to attend industrial visits also. To bridge the knowledge gap of the students, various Add-on/Value added courses are regularly conducted by the departments. In-plant training is also provided to the students to fill their knowledge gap.

**Supporting evidences for our justification is attached herewith for your kind perusal.**



  
**(B.M. SATISH)**  
PRINCIPAL  
**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
(AUTONOMOUS)  
Sree Sainath Nagar, A. RANGAMPET  
Chittoor (Dist.) - 517 102, A.P., INDIA.

**2.2.1: The institution assesses the learning levels of the students and organizes special Programmes for advanced learners and slow learners (15)**

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# Sample Copy Diagnostic test Schedule

## SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous) Sree Sainath Nagar, A.Rangampet-517 102

Department of Electronics and Communication Engineering


24-06-2019

### Academic Year - (2019-20)

#### Circular

Faculty members are requested to conduct diagnostic test as per the schedule mentioned in Academic calendar. The question paper may be fill in the blanks or one mark question etc., After evaluation, submit the marks statement along with summary sheet and weak students list to the Department.

  
HOD, ECE

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS) SREE SAINATH NAGAR, TIRUPATI-517 102 (Approved by AICTE; Programs Accredited by NBA; NAAC with 'A' Grade)		
<b>Academic Calendar for the Year 2019-20</b> For <b>II B. Tech - I Semester</b>		
<i>I Spell of Instruction</i>	24.06.2019 to 17.08.2019	8 weeks
Diagnostic Tests	From 04.07.2019 (3 working days)	
<b>I Mid-term Examinations</b>	19.08.2019 to 26.08.2019	1 week
<i>II Spell of Instructions</i>	27.08.2019 to 26.10.2019	9 weeks
Remedial Classes	From 03.09.2019 (12 working days)	
<b>II Mid-term Examinations</b>	28.10.2019 to 02.11.2019	1 week
Preparation & Practical Examinations	04.11.2019 to 16.11.2019	2 weeks
Semester-End Examinations	18.11.2019 to 30.11.2019	2 weeks
Semester-Break	01.12.2019 to 15.12.2019	2 weeks
Commencement of Class work for II B.Tech II- Semester	16.12.2019	-
<b>II B. Tech - II Semester</b>		
<i>I Spell of Instruction</i>	16.12.2019 to 08.02.2020	8 weeks
Diagnostic Tests	From 26.12.2019 (3 working days)	
<b>I Mid-term Examinations</b>	10.02.2020 to 15.02.2020	1 week
<i>II Spell of Instruction</i>	17.02.2020 to 18.04.2020	9 weeks
Remedial Classes	From 24.02.2020 (12 working days)	
<b>II Mid-term Examinations</b>	20.04.2020 to 25.04.2020	1 week
Preparation & Practical Examinations	27.04.2020 to 09.05.2020	2 weeks
Semester-End Examinations	11.05.2020 to 23.05.2020	2 weeks
Summer Vacation	24.05.2020 to 21.06.2020	4 weeks
Commencement of Class work for III B. Tech. I Semester	22.06.2020	-
Date: 13-05-2019		
 PRINCIPAL		

## Sample Copy Formative test Schedule



SREE VIDYANIKETHAN ENGINEERING COLLEGE  
(Autonomous)  
Sree Sainath Nagar, Tirupati- 517 102

Department of Electronics and Communication Engineering

24.06.2019

### **Academic Year – (2019-20)** **Schedule for submitting Formative Test Question Papers**

The faculty members who are handling subjects for **II, III & IV B. Tech., I-semester** are requested to submit the soft & Hard copy of formative tests Question paper to the Department to get multiple copies as per the following schedule.

Sl. No.	TEST	Date
1.	<b>Formative Test - I (Unit - 1)</b>	29-07-2019
2.	<b>Formative Test - II (Unit - 2)</b>	16-08-2019
3.	<b>Formative Test - III (Unit - 3)</b>	18-09-2019
4.	<b>Formative Test - IV (Unit - 4)</b>	04-10-2019
5.	<b>Formative Test - V (Unit -5)</b>	23-10-2019

  
HOD, ECE

Cc: 1. HOD – ECE.  
2. Faculty circulation.

Department of Computer Science and Engineering

Dt: 02-02-2019

**II B.Tech (CSE) – II Semester 2018-19**

**SUB: TIME TABLE FOR FORMATIVE TEST-II**

Following is the schedule of Formative Test-II for II B.Tech (CSE) students. The tests will be conducted in respective classrooms for 20 minutes duration.

Date & Time	CSE-A	CSE-B	CSE-C	CSE-D
	Faculty	Faculty	Faculty	Faculty
06/2/2019 Wednesday	Environmental Studies Dr. Y. Tharakeswar Rao	Environmental Studies Dr. Y. Tharakeswar Rao	Environmental Studies Mr. V. Raju	Environmental Studies Mr. V. Raju
07/2/2019 Thursday	Computer Graphics Dr. K. Reddy Madhavi	Computer Graphics Dr. V.V. Rama Prasad	Computer Graphics Dr. K.G. Suma	Computer Graphics Dr. K.S. Kannan
08/2/2019 Friday	Database Management Systems Mr. R. Nagaraju	Database Management Systems Mr. B. Gurunadha Rao	Database Management Systems Mr. R. Nagaraju	Database Management Systems Mr. P. Jayaram
06/2/2019 Wednesday	Design and Analysis of Algorithms Prof. C. Madhusudhana Rao	Design and Analysis of Algorithms Ms. V. Bhargavi	Design and Analysis of Algorithms Dr. K. Reddy Madhavi	Design and Analysis of Algorithms Prof. C. Madhusudhana Rao
07/2/2019 Thursday	Java Programming Mr. S. Bavaji	Java Programming Mr. I. Reddy Sekhar Reddy	Java Programming Mr. K. Siva Krishna Rao	Java Programming Mr. Shaik Salam
08/2/2019 Friday	Software Engineering Mr. E.S. Phalguna Krishna	Software Engineering Mr. K. Jeevan Pradeep	Software Engineering Mr. D. Ganesh	Software Engineering Mr. T.M. Jaya Krishna

**Note:** Faculty members of concerned subject are requested to prepare 20 questions (fill in the blanks type/one word answers) Softcopy & Hardcopy 3 sets Question paper should be submitted to HOD, CSE on or before 05.02.2019.

  
HOD, CSE

# Special Programmes for slow learner

## i. Sample Copy Remedial Classes Schedule



SREEVIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)  
Sree Sainath Nagar, Tirupati – 517 102, A.P.  
DEPARTMENT OF CIVIL ENGINEERING

No. SVEC/CE/Remedial Classes/2019-20/03

09 September 2019

### CIRCULAR

This is to inform that the Remedial Classes will be held from 12-09-2019 for the benefit of III B.Tech I Semester Civil Engineering students so as to enable them to understand the subjects as per the following schedule (2Hrs for each subject).

Day	Time	Subject	Name of the Faculty	Signature
Monday	2.15 pm – 3.30 pm	Reinforced Cement Concrete Structures	Mr. V. Mahesh / Mr. M. P. Charan Sai	
Tuesday	2.15 pm – 3.30 pm	Soil Mechanics	Mrs. G. Gnana Prasana / Dr. P. Ramesh	
Wednesday	2.15 pm – 3.30 pm	Structural Analysis – II	Mr. D. V. Purushotham / Mr. M. Gokulnath	
Thursday	2.15 pm – 3.30 pm	Wastewater Technology	Dr. Hemadri Prasad Raju / Mr. B. Sudhakar	

Students who are in the list must attend to the classes without fail. Other students can also attend the classes

Copy to: The Principal  
Dean (Academics)

Head of the Dept.  
Civil Engineering  
Sree Vidyanikethan Engineering College  
(Autonomous)  
Sree Sainath Nagar, Tirupati  
Chittoor Dt. - 517 102, (A.P.)



SREE VIDYANIKETHAN ENGINEERING COLLEGE  
(Autonomous)  
Sree Sainath Nagar, Tirupati – 517 102  
Department of EEE

SVEC/EEE/2019-20/B13

05<sup>th</sup> September, 2019

### CIRCULAR

Students of II B.Tech EEE whose marks are less than 12 in MID-I examinations are requested to attend Remedial Classes Scheduled from 07.09.2019 to 23.09.2019 for the following courses in Room No. 324.

<b>ES</b>	:	Environmental Studies
<b>SFCA</b>	:	Special Functions and Complex Analysis
<b>DCM</b>	:	DC Machines
<b>EMF</b>	:	Electromagnetic Fields
<b>SS&amp;N</b>	:	Signals, Systems and Networks
<b>AEC</b>	:	Analog Electronic Circuits

Students are requested to attend the classes regularly failing which disciplinary action will be taken.

Encl.: Remedial Class Time Table.

HOD, EEE

## ii. Sample Copy Reinforcement Classes Schedule



DEPARTMENT OF CIVIL ENGINEERING  
SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)  
Sree Sainath Nagar, Tirupati - 517 102 A.P.

No. SVEC/CE/Reinforcement Classes/2019-20/01

26 July 2019

### CIRCULAR

This is to inform that the Reinforcement Classes will be held from 01-08-2019 (6 periods for each subject) onwards for the benefit of III B.Tech. I Semester Civil Engineering students (A&B Sections) so as to enable them to understand the subjects and to fare well in the university exams as per the following schedule.

Date	Time	Subject	Name of the Faculty	Signature
Monday	03:00 pm to 04:45 p.m	Engineering Geology	Mr.P.Anil Kumar	
Tuesday	03:00 pm to 04:45 p.m	Concrete Technology	Mr. D V Purushotham	
Wednesday	03:00 pm to 04:45 p.m	Engineering Hydrology	Dr. D Sreenivasulu	
Thursday	03:00 pm to 04:45 p.m	Environmental Studies	Mr.M.Tharun Kumar	
Friday	03:00 pm to 04:45 p.m	Structural Analysis-I	Mr. M Gokulnath	
Saturday	03:00 pm to 04:45 p.m	Water Supply Engineering	Dr. Hemadri Prasad Raju	

All the students shall attend the classes. Student(s) absconding the above classes will be viewed very seriously.

III-I [A] [B] [C] [D] [E] [F] [G] [H] [I] [J] [K] [L] [M] [N] [O] [P] [Q] [R] [S] [T] [U] [V] [W] [X] [Y] [Z] [AA] [AB] [AC] [AD] [AE] [AF] [AG] [AH] [AI] [AJ] [AK] [AL] [AM] [AN] [AO] [AP] [AQ] [AR] [AS] [AT] [AU] [AV] [AW] [AX] [AY] [AZ] [BA] [BB] [BC] [BD] [BE] [BF] [BG] [BH] [BI] [BJ] [BK] [BL] [BM] [BN] [BO] [BP] [BQ] [BR] [BS] [BT] [BU] [BV] [BW] [BX] [BY] [BZ] [CA] [CB] [CC] [CD] [CE] [CF] [CG] [CH] [CI] [CJ] [CK] [CL] [CM] [CN] [CO] [CP] [CQ] [CR] [CS] [CT] [CU] [CV] [CW] [CX] [CY] [CZ] [DA] [DB] [DC] [DD] [DE] [DF] [DG] [DH] [DI] [DJ] [DK] [DL] [DM] [DN] [DO] [DP] [DQ] [DR] [DS] [DT] [DU] [DV] [DW] [DX] [DY] [DZ] [EA] [EB] [EC] [ED] [EE] [EF] [EG] [EH] [EI] [EJ] [EK] [EL] [EM] [EN] [EO] [EP] [EQ] [ER] [ES] [ET] [EU] [EV] [EW] [EX] [EY] [EZ] [FA] [FB] [FC] [FD] [FE] [FF] [FG] [FH] [FI] [FJ] [FK] [FL] [FM] [FN] [FO] [FP] [FQ] [FR] [FS] [FT] [FU] [FV] [FW] [FX] [FY] [FZ] [GA] [GB] [GC] [GD] [GE] [GF] [GG] [GH] [GI] [GJ] [GK] [GL] [GM] [GN] [GO] [GP] [GQ] [GR] [GS] [GT] [GU] [GV] [GW] [GX] [GY] [GZ] [HA] [HB] [HC] [HD] [HE] [HF] [HG] [HH] [HI] [HJ] [HK] [HL] [HM] [HN] [HO] [HP] [HQ] [HR] [HS] [HT] [HU] [HV] [HW] [HX] [HY] [HZ] [IA] [IB] [IC] [ID] [IE] [IF] [IG] [IH] [II] [IJ] [IK] [IL] [IM] [IN] [IO] [IP] [IQ] [IR] [IS] [IT] [IU] [IV] [IW] [IX] [IY] [IZ] [JA] [JB] [JC] [JD] [JE] [JF] [JG] [JH] [JI] [JJ] [JK] [JL] [JM] [JN] [JO] [JP] [JQ] [JR] [JS] [JT] [JU] [JV] [JW] [JX] [JY] [JZ] [KA] [KB] [KC] [KD] [KE] [KF] [KG] [KH] [KI] [KJ] [KL] [KM] [KN] [KO] [KP] [KQ] [KR] [KS] [KT] [KU] [KV] [KW] [KX] [KY] [KZ] [LA] [LB] [LC] [LD] [LE] [LF] [LG] [LH] [LI] [LJ] [LK] [LL] [LM] [LN] [LO] [LP] [LQ] [LR] [LS] [LT] [LU] [LV] [LW] [LX] [LY] [LZ] [MA] [MB] [MC] [MD] [ME] [MF] [MG] [MH] [MI] [MJ] [MK] [ML] [MM] [MN] [MO] [MP] [MQ] [MR] [MS] [MT] [MU] [MV] [MW] [MX] [MY] [MZ] [NA] [NB] [NC] [ND] [NE] [NF] [NG] [NH] [NI] [NJ] [NK] [NL] [NM] [NN] [NO] [NP] [NQ] [NR] [NS] [NT] [NU] [NV] [NW] [NX] [NY] [NZ] [OA] [OB] [OC] [OD] [OE] [OF] [OG] [OH] [OI] [OJ] [OK] [OL] [OM] [ON] [OO] [OP] [OQ] [OR] [OS] [OT] [OU] [OV] [OW] [OX] [OY] [OZ] [PA] [PB] [PC] [PD] [PE] [PF] [PG] [PH] [PI] [PJ] [PK] [PL] [PM] [PN] [PO] [PP] [PQ] [PR] [PS] [PT] [PU] [PV] [PW] [PX] [PY] [PZ] [QA] [QB] [QC] [QD] [QE] [QF] [QG] [QH] [QI] [QJ] [QK] [QL] [QM] [QN] [QO] [QP] [QQ] [QR] [QS] [QT] [QU] [QV] [QW] [QX] [QY] [QZ] [RA] [RB] [RC] [RD] [RE] [RF] [RG] [RH] [RI] [RJ] [RK] [RL] [RM] [RN] [RO] [RP] [RQ] [RR] [RS] [RT] [RU] [RV] [RW] [RX] [RY] [RZ] [SA] [SB] [SC] [SD] [SE] [SF] [SG] [SH] [SI] [SJ] [SK] [SL] [SM] [SN] [SO] [SP] [SQ] [SR] [SS] [ST] [SU] [SV] [SW] [SX] [SY] [SZ] [TA] [TB] [TC] [TD] [TE] [TF] [TG] [TH] [TI] [TJ] [TK] [TL] [TM] [TN] [TO] [TP] [TQ] [TR] [TS] [TT] [TU] [TV] [TW] [TX] [TY] [TZ] [UA] [UB] [UC] [UD] [UE] [UF] [UG] [UH] [UI] [UJ] [UK] [UL] [UM] [UN] [UO] [UP] [UQ] [UR] [US] [UT] [UU] [UV] [UW] [UX] [UY] [UZ] [VA] [VB] [VC] [VD] [VE] [VF] [VG] [VH] [VI] [VJ] [VK] [VL] [VM] [VN] [VO] [VP] [VQ] [VR] [VS] [VT] [VU] [VV] [VW] [VX] [VY] [VZ] [WA] [WB] [WC] [WD] [WE] [WF] [WG] [WH] [WI] [WJ] [WK] [WL] [WM] [WN] [WO] [WP] [WQ] [WR] [WS] [WT] [WU] [WV] [WW] [WX] [WY] [WZ] [XA] [XB] [XC] [XD] [XE] [XF] [XG] [XH] [XI] [XJ] [XK] [XL] [XM] [XN] [XO] [XP] [XQ] [XR] [XS] [XT] [XU] [XV] [XW] [XX] [XY] [XZ] [YA] [YB] [YC] [YD] [YE] [YF] [YG] [YH] [YI] [YJ] [YK] [YL] [YM] [YN] [YO] [YP] [YQ] [YR] [YS] [YT] [YU] [YV] [YW] [YX] [YY] [YZ] [ZA] [ZB] [ZC] [ZD] [ZE] [ZF] [ZG] [ZH] [ZI] [ZJ] [ZK] [ZL] [ZM] [ZN] [ZO] [ZP] [ZQ] [ZR] [ZS] [ZT] [ZU] [ZV] [ZW] [ZX] [ZY] [ZZ]

Head of the Dept.

HOD, Dept. of Civil Engineering  
SREE VIDYANIKETHAN ENGINEERING COLLEGE  
(AUTONOMOUS)  
Sree Sainath Nagar, TIRUPATI,  
Chittoor Dt.-517 102. (A.P.).



SREE VIDYANIKETHAN ENGINEERING COLLEGE  
(Autonomous)  
Sree Sainath Nagar, A.Rangampet - 517102

### Department of Electronics and Instrumentation Engineering

SVEC/EIE/ Reinforcement Classes/2019-20

08-08-2019

### Notice

Sub: Reinforcement for II B.Tech. II Semester subjects- reg.

Timing: 2:30pm to 4:00pm

Subject	Date	Name of the Faculty	Signature of the Faculty
Linear and Digital ICs	19-10-2019	Dr .P. Srinivasa Rao EEE	
	21-10-2019		
	22-10-2019		
	23-10-2019		
Pulse and Digital Circuits	07-10-2019	Mr. K. Ayyappa Swamy	
	14-10-2019		
	15-10-2019		
Electronic Circuit Analysis and Design	16-10-2019	Ms. N. Harathi	
	17-10-2019		
	18-10-2019		

HOD, EIE



### iii. Sample Copy Make-up/Extra Classes Schedule

#42

**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
(AUTONOMOUS)  
Sree Sainath Nagar, Tirupati- 517 102  
Department of Electronics and Communication Engineering

SVEC/ECE/2018-19 17-09-2018

**Extra classes Timetable for III B.Tech**

Room No: 321-A Sec., 201-B Sec., 301-C Sec., 302-D Sec.

Date \ Hour	SECTION-A			SECTION-B			SECTION-C			SECTION-D		
	2:30PM - 3:20PM	3:20PM - 4:10PM	4:10PM - 5:00PM	2:30PM - 3:20PM	3:20PM - 4:10PM	4:10PM - 5:00PM	2:30PM - 3:20PM	3:20PM - 4:10PM	4:10PM - 5:00PM	2:30PM - 3:20PM	3:20PM - 4:10PM	4:10PM - 5:00PM
20/09/2018	DC	PE-II	CS	MEPA	PE-II	DC	MPMC	PE-II	DC	DC	PE-II	MEPA
22/09/2018	LDICA/MPMC Lab			MPMC	CS	MEPA	MEPA	DC	CS	MPMC	CS	VLSID
24/09/2018	CS	MEPA	MPMC	DC	VLSID	MEPA	VLSID	CS	MPMC	LDICA/MPMC Lab		
25/09/2018	DC	PE-II	PE-II	CS	PE-II	PE-II	MEPA	PE-II	PE-II	DC	PE-II	PE-II
26/09/2018	MPMC	MEPA	VLSID	DC	VLSID	MEPA	DC	VLSID	MEPA	MEPA	CS	MPMC
27/09/2018	DC	PE-II	CS	MEPA	PE-II	DC	MPMC	PE-II	DC	DC	PE-II	MEPA
28/09/2018	MEPA	DC	VLSID	MEPA	MPMC	DC	LDICA/MPMC Lab			DC	MPMC	MEPA
29/09/2018	MPMC	CS	DC	MPMC	VLSID	CS	MPMC	DC	MEPA	MPMC	VLSID	CS
03/10/2018	MEPA	MPMC	PE-II	MPMC	CS	PE-II	CS	VLSID	PE-II	CS	DC	PE-II
04/10/2018	PE-II	CS	DC	PE-II	MPMC	VLSID	PE-II	CS	DC	PE-II	MEPA	DC
05/10/2018	DC	VLSID	MEPA	DC	MEPA	MPMC	MEPA	CS	MPMC	MPMC	VLSID	DC
08/10/2018	CS	MEPA	VLSID	DC	VLSID	CS	LDICA/MPMC Lab			VLSID	CS	MPMC
09/10/2018	MPMC	PE-II	CS	CS	PE-II	VLSID	VLSID	PE-II	MPMC	MPMC	PE-II	VLSID
10/10/2018	VLSID	MEPA	DC	LDICA/MPMC Lab			CS	VLSID	DC	CS	VLSID	MEPA
11/10/2018	VLSID	CS	MPMC	LDICA/MPMC Lab			CS	DC	VLSID	DC	MEPA	CS
12/10/2018	LDICA/MPMC Lab			MPMC	DC	VLSID	MEPA	MPMC	VLSID	MEPA	MPMC	VLSID
13/10/2018	MPMC	VLSID	PE-II	CS	VLSID	PE-II	MEPA	CS	PE-II	CS	VLSID	PE-II
15/10/2018	MEPA	VLSID	MPMC	MEPA	CS	MPMC	MPMC	VLSID	MEPA	LDICA/MPMC Lab		

HOD, ECE  
CC: Principal, Dean-Academics, NVA Coordinator-ECE, Examination Section, HODs- CSE, GEBN & Notice Board

Principal



**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
(AUTONOMOUS)  
SREE SAINATH NAGAR, TIRUPATI-517 102

SVEC/EEE/2018-19/D46

04-08-2018

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

### II EEE – D Time Table for Extra Hours

From 06-08-2018 to 18-08-2018

Day\Time	8:00 AM to 9:00 AM	9:00 AM to 10: 00 AM
Monday	SS&N	SS&N
Tuesday	DCM	EMF
Wednesday	ES	SFCA
Thursday	SFCA	AEC
Friday	EMF	DCM
Saturday	AEC	ES

From 20-08-2018 to 25-08-2018

Day\Time	9:40 AM to 10: 30 AM
Monday	SS&N
Tuesday	EMF
Wednesday	SFCA
Thursday	AEC
Friday	DCM
Saturday	ES

T.N. Prasad  
HOD, EEE

# Special Programmes for advanced learner

## i. Sample Copy Mini-project

### Java Mini Project on **ONLINE EXAMINATION SYSTEM**

Submitted by

**BATCH - #15**

Name of the student	Roll Number
A K S Charan	17121a1202
B Harsha vardhan	17121a1205
B Sonia	17121a1206
K Sushma	17121a1247

Department of Information Technology



**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
(AUTONOMOUS)

(Affiliated to JNTUA, Ananthapuramu, Approved by AICTE, Accredited by NBA & NAAC)  
Sree Sainath Nagar, Tirupati – 517 102, A.P., INDIA  
2018 -2019

A MINI PROJECT ON  
AGE CALCULATOR APPLICATION IN ANDROID STUDIO

Submitted by  
BATCH NO: 02

N.SOWMYA SREE	16121A1272
P.DIVYA	16121A1285
V.TEJASREE	16121A12B2
K.B.HARI KRISHNA	15121A1243

*Under the Supervision of*

**A Ramprakash Reddy, M.Tech., (Ph.D)**  
Assistant Professor  
Department of Information Technology



**INFORMATION TECHNOLOGY**

**Name of the lab: MOBLIE APPLICATION DEVELOPMENT**

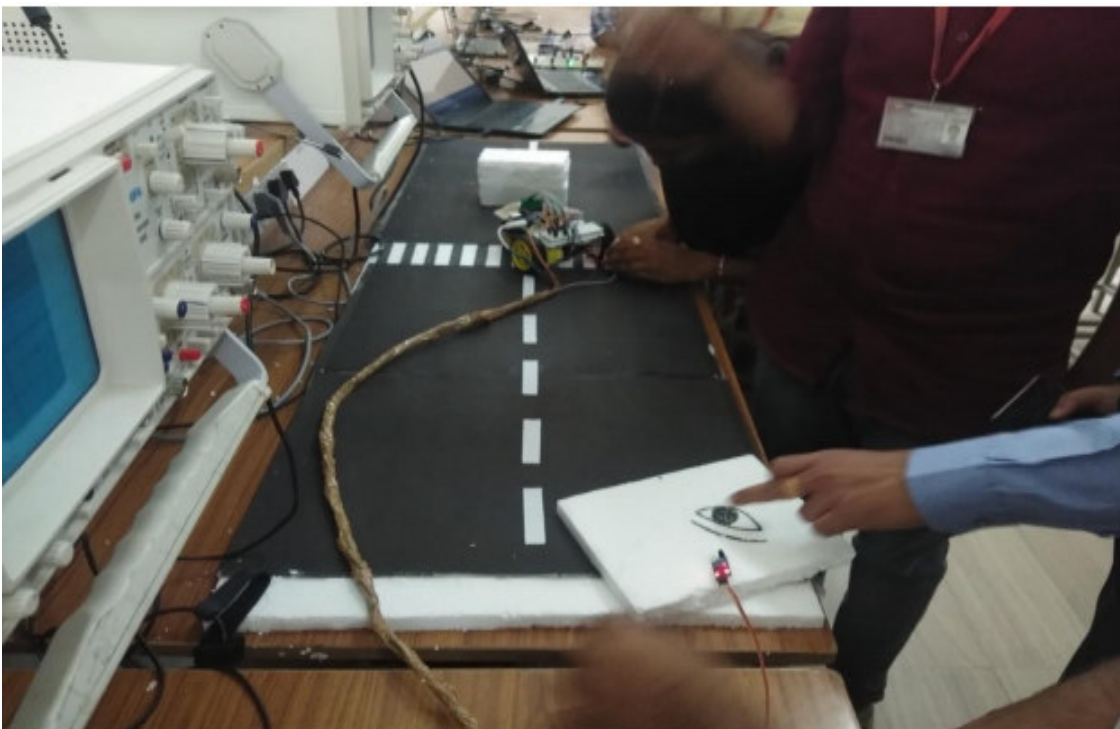
**SREE VIDYANIKETHAN ENGINEERING COLLEGE  
(AUTONOMOUS)**

(Affiliated to JNTUA, Ananthapuramu, Approved by AICTE, and Accredited by NBA & NAAC)  
Sree Sainath Nagar, Tirupati – 517 102, A.P., INDIA

**ii. Sample Copy Hardware models**



Students Demonstrating the Trainable Surgical Robotic Arm Hardware Model to Principal, SVEC



Students Demonstrating a Hardware Model

### iii. Sample Copy of Paper/Poster Presentation



Student Demonstrating a Presentation at SVEC



Finalist Certification from L&T TECHgium on successful demonstration of the proof concept on "REAL TIME PEOPLE MOVEMENT TRACKING"



Meritorious Certification from NIT, Warangal at Technozion'17



Students Explaining a Technical Poster Presentation at SVEC

**IV. Sample Copy of Student participation in competition**



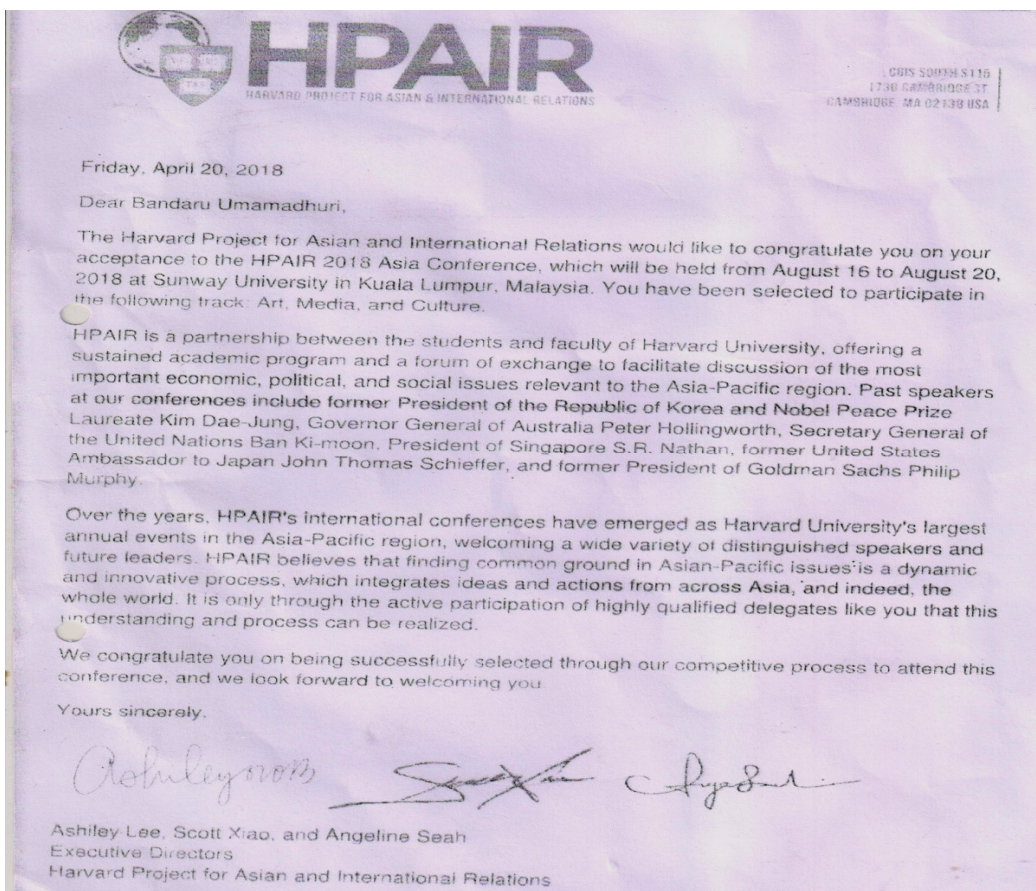
Meritorous Certification from InnovationCell IIT, Bombay at Robotic Workshop



Best Performer Certification in International Quiz League from Jointlook



Meritorous Certification from Shri RamaChandraMission at All India Essay Writing Event 2018



Letter of Invitation from HPAIR 2018 for participation in Track of Art, Media and Culture International conference held at Kuala Lumpur, Malaysia



## V. Sample Copy of Student Internships



**Indian Institute of Information Technology  
Design and Manufacturing (IIITDM) Kancheepuram**  
Melakottaiyur, Off Vandalur – Kelambakkam Road  
Chennai – 600 048, India  
Fax: +91-044-2747 6301; <http://www.iiitdm.ac.in>

**Dr NOOR MAHAMMAD SK**  
Assistant Professor  
Computer Science and Engineering  
Ph: +91-044-2747 6349  
Email: noor@iiitdm.ac.in

**Dr Noor Mohammad Sk**  
Assistant Professor

1 June 2020

### CERTIFICATE

This is to certify that **Mr G DHARANI KUMAR (16121A0472)**, B. Tech., Electronics and Communication Engineering, student of Sree Vidyanikethan Engineering College, A Rangampet, Tirupati, has done his research internship from 10 December 2019 to 15 March 2020, at High Performance Reconfigurable Computing System Engineering group, in the department of Computer Science and Engineering, Indian Institute of Information Technology Design and Manufacturing (IIITDM) Kancheepuram under my supervision. During this period he got exposure to the concepts of Communication System Design using Software Defined Radio, RF Design techniques and Communication System Modeling and Testing. He has successfully completed the assigned work and submitted the report on "RF based drone detection system".

[Dr.Noor Mohammad Sk]

Research Internship Completion certification from IIITDM,Kancheepuram

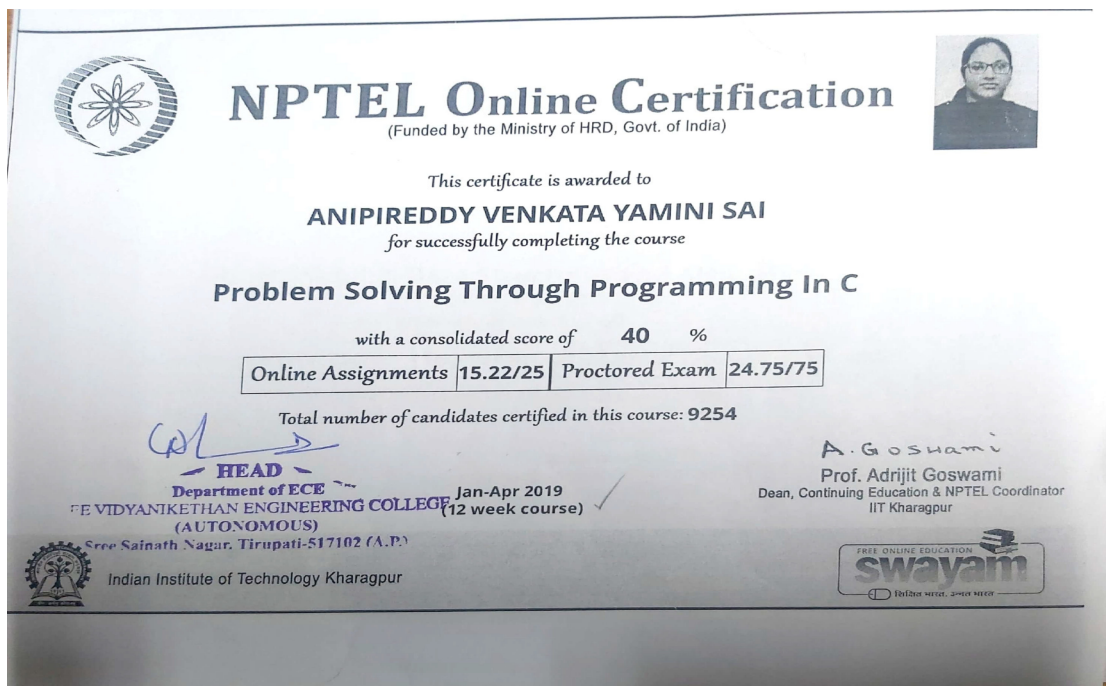


Internship Completion Certificate from INFOSYS Ltd.

**V. Sample Copy of Certificate courses**



Participation Certification from ICTACADEMY at NEW INDIA LEARNATHON2020



NPTEL SWAYAM Certification on Problem Solving through Programming In C from IIT, Kharagpur