



**SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)**

Sree Sainath Nagar, Tirupati - 517 102

AICTE – IDEA LAB TEAM, SVEC

Cordially invites you

**To
*Awareness Program***

**On
AICTE – IDEA LAB
FOR THE FACULTY OF EEE, ECE, EIE & ME**

Date : 22-Jan-2022
Time : 03:00 PM
Venue : Dasari Auditorium

Dr. P.Giridhara Reddy
Director (Academics &
Research), SVEC

Dr. B.M.Satish
Principal, SVEC

Dr. T. Nageswara Prasad
Vice-Principal, SVEC

Dr. P.V.Ramana
Dean Academics, SVEC

Dr. S. Hemachandra
Dean, Industry Relations, SVEC

Will grace the occasion



**AWARENESS PROGRAM ON
AICTE – IDEA Lab
(Idea Development Evaluation &
Application)**

(22-01-2022)

**SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)
SREE SAINATH NAGAR
TIRUPATI**



AICTE IDEA LAB, SVEC | Date: 22nd Jan, 2022

Report

Title of the Program : " Awareness Program on AICTE IDEA (Idea Development Evaluation & Application) LAB"
Organized Date(s) : 22 Jan 2022
Time : 3.00 P.M to 5.00 P.M
Organizing Department(s) : AICTE IDEA LAB, SVEC
Target Audience : Faculty of ECE, EEE, EIE & ME
Venue : Dasari Auditorium
Number of Participants : 66

Outcomes of the Program :

Awareness amongst the faculty on the following:

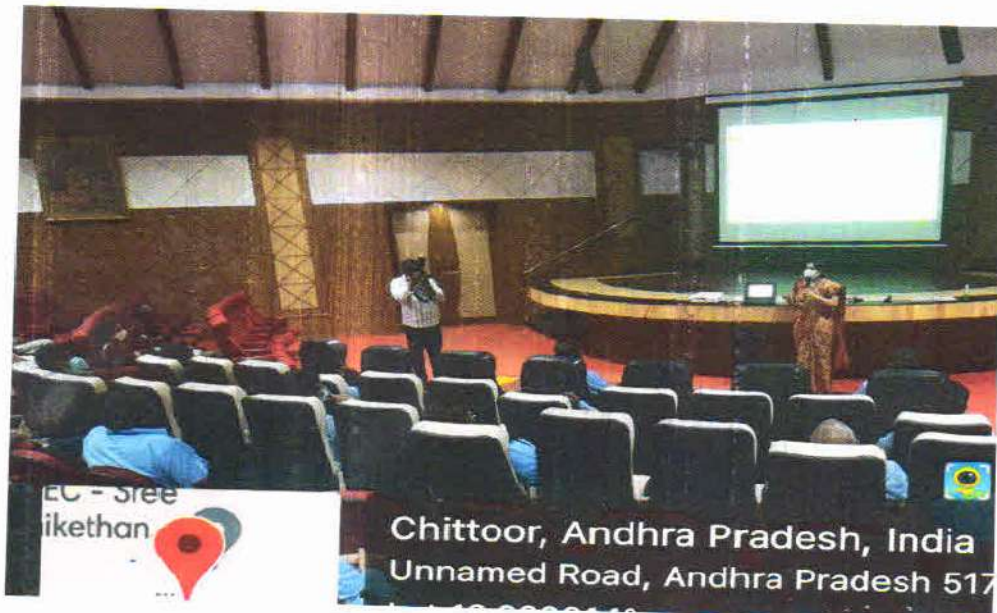
- The Vision, Mission and Objectives of the AICTE IDEA Lab.
- The IDEA Lab team members and their roles.
- Equipment and Facilities at IDEA lab.
- Activities/Events/training programs to be organized by IDEA Lab.

An awareness program on "**AICTE IDEA (Idea Development Evaluation & Application) Lab**" was organized by AICTE IDEA Lab team, Sree Vidyanikethan Engineering College on **22 Jan 2020**, from **3.00 p.m to 5.00 p.m in Dasari Auditorium**, for the faculty of Electronics and Communication Engineering (ECE), Electrical and Electronics Engineering (EEE), Electronics and Instrumentation Engineering (EIE) and Mechanical Engineering Departments of Sree Vidyanikethan Engineering college.

The program started with an introduction to AICTE IDEA Lab, its concept, theme and its objectives by Dr. B.M Satish, Principal, SVEC and Chief Mentor of IDEA Lab. He explained the importance and necessity of such a lab for faculty and students for realizing their projects into functional modules and products. He emphasized that each and every faculty and student have to utilize and contribute for the lab.



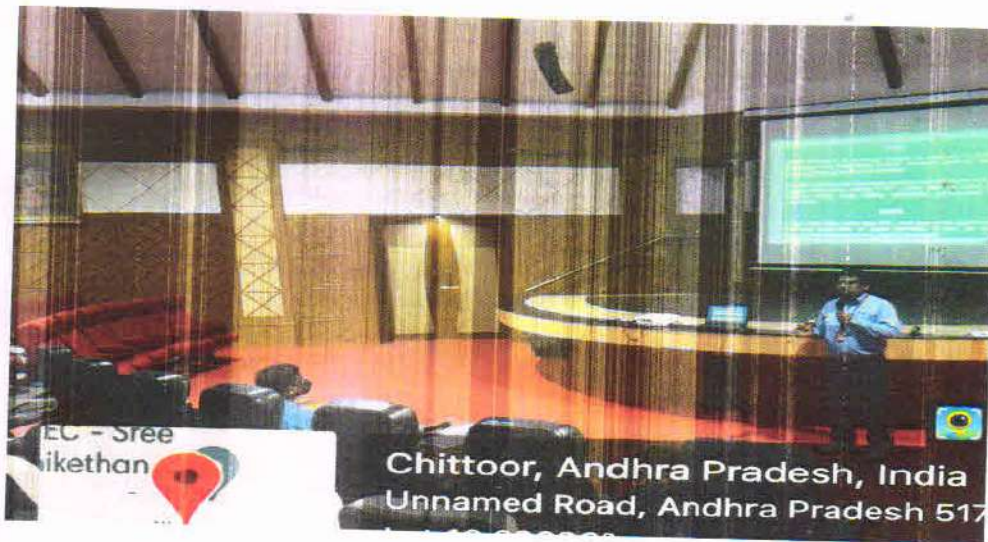
Dr. B.M. Satish, Principal, SVEC & Chief Mentor, AICTE IDEA Lab addressing the Gathering at Dasari Auditorium about AICTE IDEA lab, its significance and scope.



Dr. N Padmaja, Co-ordinator, AICTE IDEA Lab briefing about the Objectives of IDEA lab

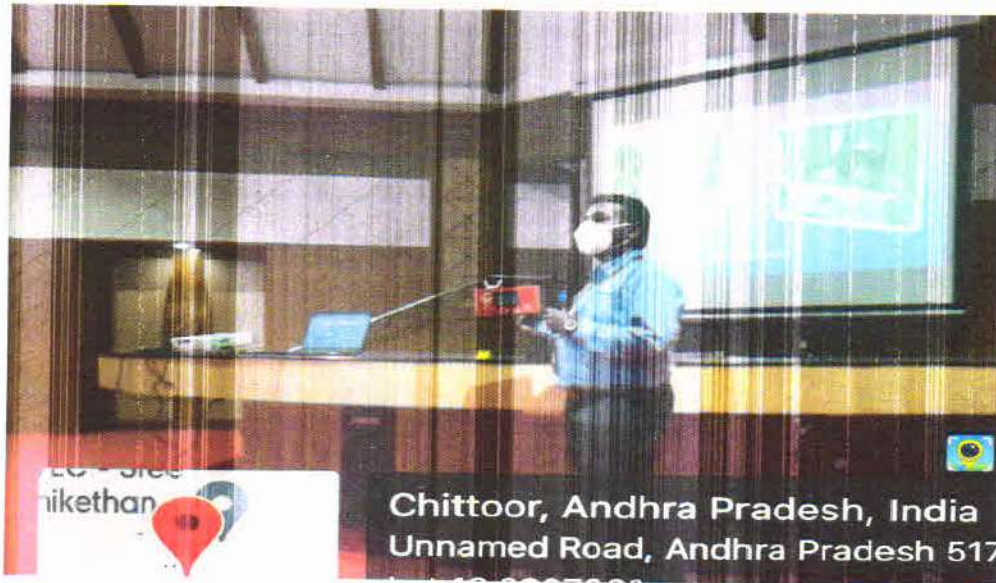
Dr. N Padmaja, Professor of ECE and co-ordinator of the IDEA Lab presented the Vision, Mission, Objectives and role of the departments of the IDEA lab. She also shared her experience and insights of the ATAL FDP program organized by AICTE attended by her at D.Y Patil International University, Pune during 23-28 Sept, 2021. She explained about the process of establishment of the lab and the expected outcomes of the lab.

Dr. A.K Damodaram, Professor of Mechanical Engg and Co-coordinator of the IDEA Lab presented the scope and role of the members of the IDEA Lab. He also presented the Action Plan and Yearly Calender of the IDEA Lab. Dr. Damodaram shared his experience and information gained by him during the ATAL FDP program organized by AICTE, at Netaji Subhas University, New Delhi during 9-14 Aug, 2021.

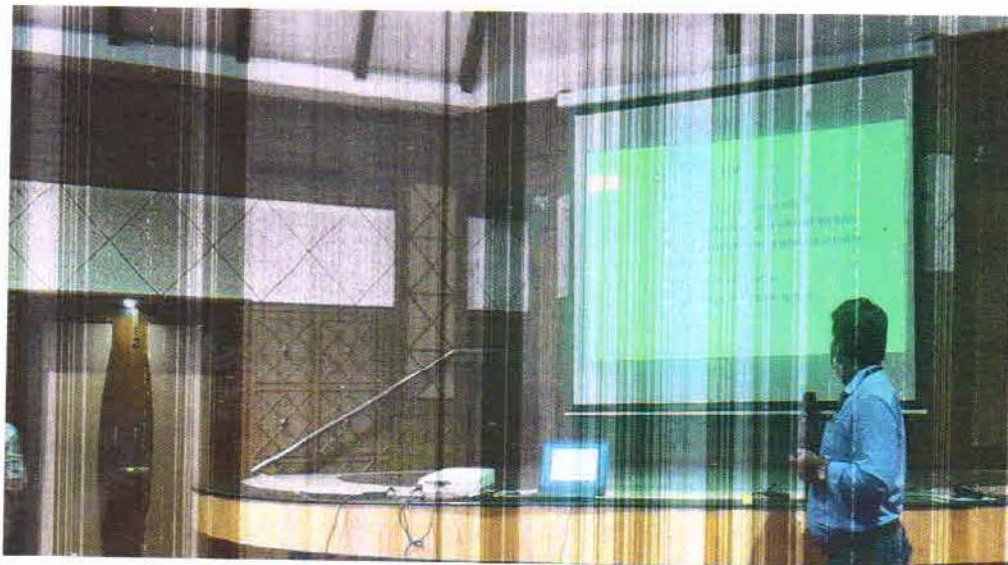


Dr. A. K Damodaram, Co-coordinator, AICTE IDEA Lab briefing about the IDEA lab scope and Plan of Action

Dr. Sachidananda K.B, Associate Prof of ME and Mr.D.Madhu Sudhan Reddy, Assistant Prof of ME are members of AICTE IDEA Lab team who are also called as "TECH GURUS " They demonstrated the model developed by them during the presentation. The presentation started with addressing the audience of describing the target project developed during the AICTE Advance IDEA Lab FDP on "Smart Manufacturing" held at AK Garg Engineering College, Ghaziabad during 01-07 December, 2021. The project titled " MINI Weather TV" which include various trades namely CNC Wood Router, Laser cutting and Engraving, 3D Printing, PCB design and fabricating. The description of software involved in design and fabricating the same in each trade was discussed briefly. Finally the presentation concluded by explaining the assembly of all the required components in making the assigned project.



Dr. Sachidananda K.B & Mr. D. Madhu Sudhan Reddy, TECH GURUs, AICTE IDEA Lab demonstrating their developed model.




Dr. T.V S Gowtham Prasad, TECH GURU, AICTE IDEA Lab presenting about PCB design and the various simulation tools.



Dr. Sophiya, Member, AICTE IDEA Lab proposing Vote of Thanks

Dr T V S Gowtham Prasad, Associate Professor, Dept of ECE, SVEC and one of the "TECH GURU" of IDEA Lab shared the experiences gained during AICTE IDEA Lab FDP on Electronic System Design at D. Y. Patil International University, Pune. He briefly explained the objective and outcomes of the FDP. Entire presentation focused on simulation tools such as Tinker CAD & MIT APP free available Cloud based software, Luban free open source software and also focused on devices such as NI My DAQ and NI My Rio portable devices and SnapMaker 3-in-1 3-D printer, laser engraver and laser carving machine.

A total of **66 faculty** members from ECE, EEE, EIE and MECH Engg attended the program and were benefitted by the awareness program. There was an interactive question and answer session at the end of the presentations by the IDEA lab team. The program conclude by a formal vote of thanks by Dr. Sophiya (CSSE) and Ms.G.Sandhya (ECE) , members of the AICTE IDEA lab at 5.00 P.M.


Dr. N Padmaja
Co-ordinator
AICTE IDEA Lab