

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS) SREE SAINATH NAGAR, A. RANGAMPET-517 102

Agenda for 12th Meeting of Board of Studies for B. Tech. (Electronics and Instrumentation Engineering) on 09-01-2021 at 11.45 AM

- 1. Course Structures for I B. Tech (I & II Semesters)
- Academic Calendar for I B. Tech -2020-21.
- 3. Any other item

Dr. A. Yasmine Begum Chairman, BOS (EIE)



SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS) Sree Sainath Nagar, Tirupati - 517102

Department of Electronics and Instrumentation Engineering

12th Board of Studies Meeting held on 9th January 2021 at 11.45 AM

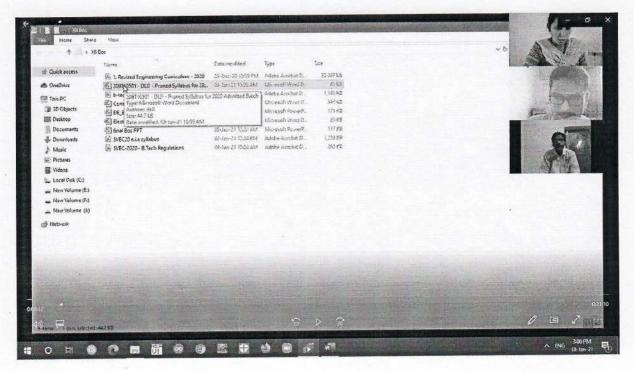
Minutes

Members Present:

1	Dr. A.Yasmine Begum	Chairman
2	Dr. G. Sreenivasulu	External Member (Academics)
3	Dr. S. Mourauga Pragash	External Member (Academics)
4	Dr. G. P. Ramesh	External Member (Academics)
5	Dr. Y. Dileep Kumar	Internal Member
6	Mr. C. Ravindra murthy	Internal Member
7	Mr. G. Hemanth Kumar	Internal Member
8	Mr. K. Ayyappa Swamy	Internal Member
9	Mr. C. Somasundar Reddy	Internal Member
10	Ms.N. Harathi	Internal Member

12th Meeting of Board of Studies of Electronics and Instrumentation Engineering (EIE) was conducted **through online video conferencing** using ZOOM platform. The screenshots taken during the meeting are shown below:





Minutes:

1. Action Taken Report on minutes of 11th BOS meeting held on 15th July 2020

The Chairperson BOS apprised the members the action taken on the minutes/ resolutions of 11^{th} BOS meeting held on 15^{th} July 2020. After deliberations the members ratified the report and the same is placed at **Annexure – I**.

2. Course Structure for I B. Tech (I & II Semesters) of Electronics and Instrumentation Engineering under SVEC-20 Curriculum.

The chairperson BOS presented the proposed SVEC-20 Course Structure of EIE with reference to the curriculum prepared by APSCHE. The guidelines given by APSCHE were strictly followed and the credits distribution across courses under various categories was made exactly same as that of the course structure proposed by APSCHE.

The External members have suggested realigning the units of Network analysis. The members also have suggested adding DC machines in the course Basic Electrical and Electronics Engineering.

After deliberations and discussions, the members have ratified the Course Structure and the same is placed at **Annexure-II**. It is resolved to authorize the Chairperson BOS to adapt the improvements suggested and submit to the Chairman (Academic council) for further processing.

3. Any other item

Resolved to authorize the Chairman, BOS (EIE) to prepare the detailed syllabi of II, III & IV B. Tech including electives and Honor Degree Courses.

Meeting concluded with thanks to the Chair.

Chairperson, BoS in EIE



SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

SREE SAINATH NAGAR, TIRUPATI-517 102

Department of Electronics and Instrumentation Engineering

Action Taken Report on minutes/ resolutions of 11th BOS meeting held on 12.07.20 for B.Tech in EIE

1. Academic Calendars

Resolution: The commencement of class work for I, II, III and IV B. Tech. program for the academic year 2020-21 was not finalized due to Covid-19 pandemic. So, it was resolved to authorize the Chairperson BOS to prepare academic calendars, in accordance with the A.P. State Government/APSCHE/JNTUA, Ananthapuramu directives and submit to the Chairman (Academic Council) for approval.

Action Taken: As authorized, the academic calendars for I, II, III and IV B. Tech. program for the academic year 2020-21 were prepared and got approved by the Chairman (Academic Council).

2. Vision & Mission of the Department, PEOs, POs & PSOs and Course Structure for B. Tech.(EIE) Program

Resolution: It was resolved to authorize the Chairperson BOS to adapt certain changes suggested in the course structure and submit the same to the Chairman (Academic Council) for further processing.

Action Taken: As authorized, the Chairperson BOS has adapted the changes suggested in the course structure and got approved by the Chairman (Academic Council) in Academic Council Meeting.

3. Syllabi with Course Objectives, Course Outcomes, Lesson Plans and Model Question Papers for the Courses of II B. Tech (Under SVEC-19)

Resolution: It is resolved to authorize the Chairman BOS to make suggested improvements in the course structure, syllabi, lesson plans and model question papers and submit the same to the Chairman (Academic Council) for further processing.

Action Taken: As authorized, the Chairman BOS has adapted all the improvements suggested in the course structure, syllabi, lesson plans and question banks and got approved by the Chairman (Academic Council) in Academic Council meeting.

4. Panel of Examiners for Question paper setting for II B. Tech courses (Under SVEC-19)

Resolution: It was resolved to authorize the Chairperson BOS to make certain changes suggested in the Panel of Examiners and submit the same to the Chairman (Academic Council) for further processing.

Action Taken: As authorized, the Chairperson BOS has adapted the changes suggested in the panels and got approved by the Chairman (Academic Council) in Academic Council meeting.

5. Amendments to SVEC-19 B. Tech Academic Regulations

Resolution: It was resolved to make amendments to SVEC-19 B. Tech Academic Regulations regarding the introduction of Assignments for attainment of program outcomes, Challenging Evaluation/Personal Verification/Revaluation/Recounting and Re-Registration for Improvement of Internal Marks according to the University guidelines and submit to the Chairman (Academic Council) for approval.

Action Taken: As suggested, the amendments got approved by the Chairman (Academic Council) in Academic Council meeting.

Course Structure

Electronics and Instrumentation Engineering (EIE)

I B.Tech. - I Semester

SI. No.	Subject Code	Course Title			Per wee	iods k	C	Scheme of Examination Max. Marks		
			E	Т	Р	Total		Int. Marks	Ext. Marks	Total Marks
1.	20BT1BS01	Differential Equations and Multivariable Calculus	3	-	-	3	3	30	70	100
2.	20BT1BS03	Engineering Physics	3		-	3	3	30	70	100
3.	20BT10201	Basic Electrical and Electronics Engineering	3	-	-	3	3	30	70	100
4.	20BT10501	Digital Logic Design	3	-	-	3	3	30	70	100
5.	20BT1BS32	Engineering Physics Lab	-	=	3	3	1.5	30	70	100
6.	20BT10231	Basic Electrical and Electronics Engineering Lab	-	3	3	3	1.5	30	70	100
7.	20BT10331	Computer Aided Engineering Drawing	-	1	4	5	3	30	70	100
8.	20BT10332	Engineering Workshop	-	-	3	3	1.5	30	70	100
9.	20BT1HSAC	Spoken English (Audit Course)	2	-	-	2	-	-4	-	
		Total:	14	1	13	28	19.5	240	560	800

I B.Tech. - II Semester

SI. No.	Subject Code	Course Title	Contact Periods per week					Scheme of Examination Max. Marks		
			L	т	Р	Total	С	Int. Marks	Ext. Marks	Total Marks
1.	20BT2BS01	Transformation Techniques and Linear Algebra	3	-	-	3	3	30	70	100
2.	20BT1BS02	Engineering Chemistry	3	-	-	3	3	30	70	100
3.	20BT1HS01	Communicative English	3	-	-	3	3	30	70	100
4.	20BT20241	Network Analysis	3	-	-	3	3	30	70	100
5.	20BT20541	Programming in C and Data Structures	3	•	-	3	3	30	70	100
6.	20BT1BS31	Engineering Chemistry Lab	-	-	3	3	1.5	30	70	100
7.	20BT1HS31	Communicative English Lab	-	-	3	3	1.5	30	70	100
8.	20BT20551	Programming in C and Data Structures Lab	-	-	3	3	1.5	30	70	100
9.	20BT1MC01	Universal Human Values (Mandatory Course)	2	-	-	2	-	30		30
		Total:	17	-	09	26	19.5	270	560	830

Chairperson, BoS in EIE