



**SREE VIDYANIKETHAN ENGINEERING COLLEGE  
(AUTONOMOUS)**

SREE SAINATH NAGAR, A. RANGAMPET-517102

**Agenda for 12<sup>th</sup> Meeting of Board of Studies for B. Tech. (Computer  
Science and Systems Engineering) on 09.01.2021 at 11:45 AM**

1. Action taken report on minutes/ resolutions of 11<sup>th</sup> BOS meeting.
2. Syllabi with Course Objectives, Course Outcomes, Lesson Plans and Model Question Papers for the Courses of **I B. Tech** (Under SVEC-20).
3. Any Other item.

**Dr. P.DhanaLakhmi**

Chairperson, BOS(CSSE)

**Dr. P. DHANA LAKSHMI**

ASSOCIATE PROFESSOR,  
Department of C.S.S.E.

SREE VIDYANIKETHAN ENGINEERING COLLEGE  
(AUTONOMOUS)

Sree Sainath Nagar, A. RANGAMPET  
Chittoor (Dist.) - 517 102, A.P., INDIA.

# SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet-517102

**DEPARTMENT OF COMPUTER SCIENCE AND SYSTEMS ENGINEERING**

## Board of Studies for B. Tech. in Computer Science and Systems Engineering

**12<sup>th</sup> Meeting held on 09<sup>th</sup> January 2021 @ 11:45 AM**

### Members Present

S.No	Name	Designation
1.	<b>Dr.P.Dhana Lakshmi</b> Associate Professor Dept. of CSSE, SVEC	Chairperson
2.	<b>Dr.S.VASUNDARA</b> Professor, Dept. of CSE JNTUA, College of Engineering, JNTU Anantapur, Ananthapuramu-515002	JNTUA Nominee (External Member)
3.	<b>Dr. B.K. TRIPATHY</b> Senior Professor & Dean, School of Computing Science and Engineering, VIT University, Vellore-632014, Tamil Nadu	AC Nominee (External Member)
4.	<b>Dr. P. VARALAKSHMI</b> Associate Professor of Computer Technology, Madras Institute of Technology, MIT Road, Radha Nagar, Chromepet, Chennai - 600044	AC Nominee (External Member)
5.	<b>Mr. PAVAN YARA</b> Vice President Technology, First Walkin Technologies #736 4th Floor, Gupta House, 7 <sup>th</sup> Cross Koramangala, Bengaluru-560034 Karnataka	Alumni
6.	<b>Prof. C.MadhusudhanaRao</b> Professor & HoD Dept. of CSSE, SVEC	Internal Member

7.	<b>Dr. A. Balasubramani</b> Professor, Dept. of CSSE, SVEC	Internal Member
8.	<b>Dr.C.Sushama</b> Associate Professor Dept. of CSSE, SVEC	Internal Member
9.	<b>Mr.S.Shiva Praksh</b> Assistant Professor Dept. of CSSE, SVEC	Internal Member
10.	<b>Mr.M.Ramu</b> Assistant Professor Dept. of CSSE, SVEC	Internal Member
11.	<b>Dr. E. Sophiya</b> Assistant Professor Dept. of CSSE, SVEC	Internal Member

12<sup>th</sup> Meeting of Board of Studies of Computer Science and Systems Engineering (CSSE) was conducted **through online video conferencing** using ZOOM platform. The screenshots taken during the meeting are shown below



## **Minutes:**

### **1. Action Taken Report on minutes of 11<sup>th</sup> BOS meeting held on 12<sup>th</sup> July 2020**

The Chairperson BOS appraised the members the action taken report on the minutes/resolutions of 11<sup>th</sup> BOS meeting held on 15<sup>th</sup> July 2020. After deliberations the members ratified the report.

### **2. Course structure, Syllabi with Course Objectives, Course Outcomes, Lesson Plans and Model Question Papers for the Courses of I B. Tech (CSSE) Program under SVEC-20 Regulations.**


The chairperson BOS presented the Course Structure, syllabi with Course Objectives, Course Outcomes, Lesson Plans and Model Question Papers for the Courses of I B. Tech (CSSE) Program prepared by the department. The Chairperson BOS appraised the members with the procedure followed in articulation of CEOs, COs and mapping of COs with POs & PSOs. The members suggested modifications and then approved the course structures, syllabus of I B.Tech (CSSE) after thorough discussion. The following suggestions are given by the board members on the syllabi and course structure proposed by the department:

#### **In CSSE Course structure:**

1. Lab experiments should not be specific, arrange the experiments in concept wise.

### **3. Any other item:**

A copy of Academic Calendar for 2020-21 pertaining to B. Tech, I and II semesters was presented and members approved the same.

  
**Chairperson, BOS (CSSE)**

**Dr. P. DHANA LAKSHMI**  
ASSOCIATE PROFESSOR,  
Department of C.S.S.E.  
**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
(AUTONOMOUS)  
Sree Sainath Nagar, A. RANGAMPET  
Chittoor (Dist.) - 517 102, A.P., INDIA.

## Approved Course Structure

**Program:** B. Tech.- Computer Science and Systems Engineering

**Regulations:** SVEC-20

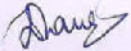
Mandatory Induction Program	03 weeks duration
Induction program offered before commencement of the I-Semester course work	Physical activity
	Creative Arts
	Universal Human Values
	Literary
	Proficiency Modules
	Lectures by Eminent People
	Visits to local Areas
	Familiarization to Department/Branch and Innovations

### I B. Tech. – I Semester

Sl. No.	Course Code	Course Title	Contact Periods per week				C	Scheme of Examination Max. Marks		
			L	T	P	Total		Int. Marks	Ext. Marks	Total Marks
1	20BT1BS01	Differential Equations and Multivariable Calculus	3	-	-	3	3	30	70	100
2	20BT1BS02	Engineering Chemistry	3	-	-	3	3	30	70	100
3	20BT1HS01	Communicative English	3	-	-	3	3	30	70	100
4	20BT11201	Programming for Problem Solving	3	-	-	3	3	30	70	100
5	20BT10201	Basic Electrical and Electronics Engineering	3	-	-	3	3	30	70	100
6	20BT1BS31	Engineering Chemistry Lab	-	-	3	3	1.5	30	70	100
7	20BT1HS31	Communicative English Lab	-	-	3	3	1.5	30	70	100
8	20BT11231	Programming for Problem Solving Lab	-	-	3	3	1.5	30	70	100
9	20BT1MC01	Universal Human Values (Mandatory Course)	2	-	-	2	-	30	-	30
<b>Total:</b>			<b>17</b>	<b>-</b>	<b>09</b>	<b>26</b>	<b>19.5</b>	<b>270</b>	<b>560</b>	<b>830</b>

### I B. Tech. – II Semester

Sl. No.	Course Code	Course Title	Contact Periods per week				C	Scheme of Examination Max. Marks		
			L	T	P	Total		Int. Marks	Ext. Marks	Total Marks
1	20BT2BS01	Transformation Techniques and Linear Algebra	3	-	-	3	3	30	70	100
2	20BT1BS03	Engineering Physics	3	-	-	3	3	30	70	100
3	20BT20501	Digital Logic Design	3	-	-	3	3	30	70	100
4	20BT21501	Object Oriented Programming through Java	3	-	-	3	3	30	70	100
5	20BT10331	Computer Aided Engineering Drawing	-	1	4	5	3	30	70	100
6	20BT1BS32	Engineering Physics Lab	-	-	3	3	1.5	30	70	100
7	20BT10332	Engineering Workshop	-	-	3	3	1.5	30	70	100
8	20BT21531	Object Oriented Programming through Java Lab	-	-	3	3	1.5	30	70	100
9	20BT1HSAC	Spoken English (Audit Course)	2	-	-	2	-	-	-	-
<b>Total:</b>			<b>14</b>	<b>1</b>	<b>13</b>	<b>28</b>	<b>19.5</b>	<b>240</b>	<b>560</b>	<b>800</b>

  
**Chairperson, BOS (CSSE)**

**Dr. P. DHANA LAKSHMI**

ASSOCIATE PROFESSOR,

Department of C.S.S.E.

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, A. RANGAMPET  
Chittoor (Dist.) - 517 102, A.P., INDIA.