



SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

SREE SAINATH NAGAR, A. RANGAMPET-517 102

Department of Electrical & Electronics Engineering

**Agenda for 12th Meeting of Board of Studies for
B. Tech. (Electrical and Electronics Engineering)
on 09.01.2021 at 11:45 AM**

1. Action taken report on minutes/ resolutions of 11th BOS meeting.
2. Administrative set-up for B. Tech Program.
3. Syllabi with Course Objectives, Course Outcomes, Lesson Plans and Model Question Papers for the Courses of **I B. Tech** (Under SVEC-20).
4. Any Other item.


Dr. T. Nageswara Prasad
Chairman, BOS(EEE)

Meeting of Board of Studies in
B.Tech. Electrical and Electronics Engineering
on
9th January 2021

Minutes of meeting:

BoS Members:

S. No.	Name	Designation
1	Dr. T. Nageswara Prasad Professor, SVEC	Chairman
2	Dr. G. V. Marutheswar Professor, Department of Electrical Engineering, SV University College of Engineering, Tirupathi	External Member (Academics)
3	Dr. S. Sivanagaraju Professor, Department of EEE, JNTUK College of Engineering, Kakinada.	External Member (Academics)
4	Mr. V. Sreedeeep Senior Lead Engineer, Intel India Pvt. Ltd., Bangalore	External Member (Industry)
5	Mr. P. Sessa Sai Kumar Research Engineer, Rolls-Royce EPL-SG Lab, Singapore	Alumni (Industry)
6	Dr. M.S. Sujatha Professor and Head, Dept. of EEE, SVEC	Internal Member
7	Dr. P. Umapathi Reddy Professor, Dept. of EEE, SVEC	Internal Member
8	Dr. S. Hema Chandra Professor, Dept. of EEE, SVEC	Internal Member
9	Dr. T. Devaraju Professor, Dept. of EEE, SVEC	Internal Member
10	Dr. N.M.G. Kumar Professor, Dept. of EEE, SVEC	Internal Member
11	Dr. S. Farook Assoc. Professor, Dept. of EEE, SVEC	Internal Member
12	Dr. E. Parimala Sundar Assoc. Professor, Dept. of EEE, SVEC	Internal Member
13	Dr. G. Hari Krishnan Assoc. Professor, Dept. of EEE, SVEC	Internal Member
14	Dr. V. Arun Assoc. Professor, Dept. of EEE, SVEC	Internal Member
15	Dr. I. Kumara Swamy Assoc. Professor, Dept. of EEE, SVEC	Internal Member
16	Dr. D. Suresh Babu Assoc. Professor, Dept. of EEE, SVEC	Internal Member

S. No.	Name	Designation
17	Dr. K. Suresh Asst. Professor, Dept. of EEE, SVEC	Internal Member
18	Dr. S. Prabhu Asst. Professor, Dept. of EEE, SVEC	Internal Member
19	Dr. B. Hemanth Kumar Asst. Professor, Dept. of EEE, SVEC	Internal Member
20	Mr. M. Manohara Assoc. Professor, Dept. of EEE, SVEC	Internal Member

1. Action Taken Report on minutes/ resolutions of 11thBoS Meeting

The Chairman, BoSDr. T. Nageswara Prasad presented the action taken report on resolutions of BoS meeting held on 13thJuly, 2020. After deliberations the members ratified the report.

2. Administrative set-up for B.Tech (EEE) Program.

The Chairman, BoS presented the administrative set-up followed in preparing the curriculum of B.Tech. (EEE). The board congratulated the members on systematic approach followed in the design of course structure and curriculum.

3. Syllabi with Course Objectives, Course Outcomes and Lesson Plans for the Courses of I B. Tech (Under SVEC-20)

The Course Structure prepared, under SVEC-20 Regulations, in-line with AICTE Model Curriculum-2018, AICTE Approval Process Handbook 2020-21, the structure proposed by APSCHE, 2020 and the suggestions made by the board in its previous meeting is presented by Chairman, BOS and discussed. Also, the syllabus of the courses of I and II semesters of I B.Tech. is presented and discussed. The following are the observations and suggestions made by the board on the syllabi and course structure proposed by the department:

- Basic Electrical and Electronics Engineering course and Basic Electrical and Electronics Engineering Lab course are meant for other branches but not for EEE branch.
- include AC source topics in unit-I of Electric Circuits Course and also in Network Analysis course.
- Rename unit-IV name as Magnetic Circuits in Electric Circuits Course.
- Include Spoken English course in the curriculum.
- Look at the possibility of having the following two courses in Course Structure. Programming Through C and Data structures through C.

The board has gone through the Course Objectives, Course Outcomes, Lesson Plan of I B.Tech. courses. The board congratulated the department for the efforts put in by the members in articulating the CEOs, COs, the methodology followed in mapping of COs to POs

and PSOs, and also exercising the number of hours required with the help of Lesson Plan. The board also approved the Model Question Papers of the courses that are in-line with the one suggested by AICTE under Examination Reforms.

The course structure of B.Tech. (EEE) is approved for I Year only subject to modifications that may arise due to suggestions made above. BoS chairman is authorized to amend the courses and course structure considering the suggestions made above and the need of stakeholders with the information to the BoS members.

4. Any other item:


Members suggested to

- a. For a particular course, cover the topics from a particular text book only.
- b. Prepare Semester End Examination question papers by faculty of same college if possible for judging better way.

Members appreciated the pro-active approach of college management and efforts taken by department for the growth of the department and college.

Members Present:

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1	Dr. T. Nageswara Prasad Professor, SVEC	Chairman
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Dr. T. Nageswara Prasad
Chairman BOS, EEE

Course Structure

SVEC-20 B.Tech. EEE Course Structure

I B.Tech I-Semester

S. No.	Course Code	Course Title	Contact Periods per Week				Credits (C)	Scheme of Examination Max. Marks		
			L	T	P	Total		Int. Marks	Ext. Marks	Total Marks
1.	20BT1BS01	Differential Equations and Multivariable Calculus	3	-	-	3	3	30	70	100
2.	20BT1BS03	Engineering Physics	3	-	-	3	3	30	70	100
3.	20BT10201	Basic Electrical and Electronics Engineering	3	-	-	3	3	30	70	100
4.	20BT10341	Basic Civil and Mechanical Engineering	3	-	-	3	3	30	70	100
5.	20BT1BS32	Engineering Physics Lab	-	-	3	3	1.5	30	70	100
6.	20BT10231	Basic Electrical and Electronics Engineering Lab	-	-	3	3	1.5	30	70	100
7.	20BT10331	Computer Aided Engineering Drawing	-	1	4	5	3	30	70	100
8.	20BT10332	Engineering Workshop	-	-	3	3	1.5	30	70	100
9.	20BT1HSAC	Spoken English(Audit Course)	2	-	-	2	-	-	-	-
Total:			14	1	13	28	19.5	240	560	800


Dr. T. Nageswara Prasad
Chairman BOS, EEE

SREE VIDYANIKETHAN ENGINEERING COLLEGE

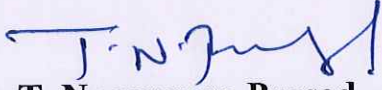
(AUTONOMOUS)

SREE SAINATH NAGAR, A. RANGAMPET -517 102.

Department of Electrical and Electronics Engineering

I B.Tech II-Semester

S. No.	Course Code	Course Title	Contact Periods per Week				Credits (C)	Scheme of Examination Max. Marks		
			L	T	P	Total		Int. Marks	Ext. Marks	Total Marks
1.	20BT2BS01	Transformation Techniques and Linear Algebra	3	-	-	3	3	30	70	100
2.	20BT1BS02	Engineering Chemistry	3	-	-	3	3	30	70	100
3.	20BT1HS01	Communicative English	3	-	-	3	3	30	70	100
4.	20BT20201	Electrical Circuits	3	-	-	3	3	30	70	100
5.	20BT20541	Programming in C and Data Structures	3	-	-	3	3	30	70	100
6.	20BT1BS31	Engineering Chemistry Lab	-	-	3	3	1.5	30	70	100
7.	20BT1HS31	Communicative English Lab	-	-	3	3	1.5	30	70	100
8.	20BT20551	Programming in C and Data Structures Lab	-	-	3	3	1.5	30	70	100
9.	20BT1MC01	Universal Human Values (Mandatory Course)	2	-	-	2	-	30	-	30
Total			17	-	09	26	19.5	270	560	830


Dr. T. Nageswara Prasad
Chairman BOS, EEE