SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)
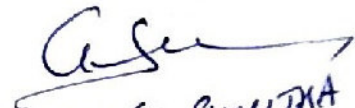### SREE SAINATH NAGAR, A. RANGAMPET-517 102

Agenda for 12th Meeting of Board of Studies for
B. Tech. - Computer Science and Engineering(Artificial Intelligence)
on 09.01.2021 at 11:45 AM

1. Administrative set-up for B. Tech.-CSE(Artificial Intelligence) Program under SVEC20 regulations

2. Syllabi with Course Objectives, Course Outcomes, Lesson Plans and Model Question Papers for the Courses of I B. Tech.-CSE(Artificial Intelligence) under SVEC-20 regulations
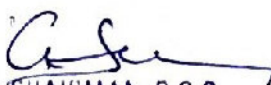
3. Any Other item

Dr. G. SUNITHA

CHAIRMAN, B.O.S,
COMPUTER SCIENCE & ENGINEERING
SREE VIDYANIKETHAN ENGINEERING COLLEGE
Sree Sainath Nagar, A. Rangampet
Near TIRUPATI - 517 102. A.P.

Department of Computer Science and Engineering

Minutes of the 12th Meeting of Board of Studies (CSE) for the UG Program B.Tech.-CSE(Artificial Intelligence) conducted on 09.01.2021 at 11:45 AM

Members Present:

S. No	Name	Designation
1.	**Dr. G. Sunitha** Professor, Dept. of CSE, SVEC	Chairperson
2.	**Dr. P. Chenna Reddy** Professor of CSE, JNTUA College of Engineering, JNTU Ananthapuramu, Ananthapuramu	External Member (Nominated by Vice-Chancellor, JNTUA)
3.	**Dr. D.V.V. Prasad** Professor of CSE, SSN College of Engineering, Chennai	External Member (Nominated by Academic Council)
4.	**Dr. T. Chitralekha** Professor & Dean, School of Engineering & Technology, Pondicherry University Puducherry	External Member (Nominated by Academic Council)
5.	**Ms. Annie Joyce. V** Joint Director, RTS-IoT Group, C-DAC, Bengaluru	External Member (Industry)

S. No	Name	Designation
6.	**Dr. V.V. Rama Prasad** Professor, Dept. of CSE, Sree Vidyanikethan Engineering College	Internal Member
7.	**Dr. B. Narendra Kumar Rao** Professor & Head, Department of CSE, Sree Vidyanikethan Engineering College	Internal Member
8.	**Dr. M. Sunil Kumar** Professor, Department of CSE, Sree Vidyanikethan Engineering College	Internal Member
9.	**Dr. A.V. Sriharsha** Professor, Department of CSE, Sree Vidyanikethan Engineering College	Internal Member
10.	**Dr. J. Aswini** Professor, Department of CSE, Sree Vidyanikethan Engineering College	Internal Member
11.	**Mr. Shaik Salam** Associate Professor, Department of CSE, Sree Vidyanikethan Engineering College	Internal Member
12.	**Dr. M. Sakthivel** Associate Professor, Department of CSE, Sree Vidyanikethan Engineering College	Internal Member
13.	**Dr. J. Avanija** Associate Professor, Department of CSE, Sree Vidyanikethan Engineering College	Internal Member Coordinator B.Tech.-CSE (AI)
14.	**Dr. K. Reddy Madhavi** Associate Professor, Department of CSE, Sree Vidyanikethan Engineering College	Internal Member Coordinator B.Tech.-CSE (DS)
15.	**Dr. V. Anantha Natarajan** Associate Professor, Department of CSE, Sree Vidyanikethan Engineering College	Internal Member
16.	**Dr. B. Uma Maheswara Rao** Associate Professor, Department of CSE, Sree Vidyanikethan Engineering College	Internal Member
17.	**Dr. K. Suresh** Associate Professor, Department of CSE, Sree Vidyanikethan Engineering College	Internal Member

Minutes:

1. **Administrative setup for B. Tech.-CSE(Artificial Intelligence) Program**

   Members of BoS were apprised of the Administrative Setup for preparation of syllabi of courses of I B.Tech.-CSE(Artificial Intelligence) program under SVEC20 regulations. The board members have appreciated the efforts of the department in preparation of course structure and syllabi of I B.Tech.-CSE(Artificial Intelligence) courses under SVEC20 regulations.

2. **Course Structure, Syllabi of I B.Tech.-CSE(Artificial Intelligence) under SVEC20 Regulations**

   The Chairperson, BoS presented the course structure of I B.Tech.-CSE(Artificial Intelligence) under SVEC20 regulations prepared in-line with AICTE Model Curriculum

2018, AICTE Approval Process Handbook 2020-21 and APSCHE Revised Engineering Curriculum for B.Tech. Regular/Honors/Minor 2020.

After deliberations and discussions, the board members have made following suggestions on the academic regulations (under SVEC20 regulations):

- The conversion of CGPA into equivalent percentage (section 14.3) may be reconsidered.
- The criteria for the Award of the B.Tech. Degree (section 17.1) and for Withholding of Results (section 19) may be reconsidered.

After deliberations and discussions, the board members have made following suggestions on the procedure for internal evaluation of assignment (under SVEC20 regulations):

- Students may be given option to take up a certification course offered by industries/Professional bodies and the completion certificate may be considered for internal evaluation of assignment. The board felt that this will incentivize students towards self-learning.

After deliberations and discussions, the board members have made following suggestions on the course structure of I B.Tech.-CSE(Artificial Intelligence) under SVEC20 regulations proposed by the department:

- For the course "Programming for Problem Solving", the title "Problem Solving and Programming" may be appropriate.

The I B.Tech.-CSE(Artificial Intelligence) course structure under SVEC20 regulations was approved subject to the modifications that may arise due to suggestions made by the board.

The draft syllabi of I B.Tech. courses (under SVEC20 regulations) along with course objectives, course outcomes, lesson plans, model question papers were presented for discussion. The Chairperson BoS apprised the members that the model question papers of the courses are in-line with the AICTE Examination Reform Policy 2018. The syllabi of the following I B.Tech. courses prepared by CSE department was approved subject to the modifications that may arise due to suggestions made by the board.

- Programming in C and Data Structures
- Programming in C and Data Structures Lab
- Digital Logic Design

It is resolved to authorize the Chairperson, BoS to adapt improvements suggested by the board members and submit to the Chairman (Academic Council) for further processing.

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3. **Panel of Examiners for question paper setting of I B.Tech.-CSE(Artificial Intelligence) courses under SVEC20 regulations**

Regarding Panels of Question Paper setters for I B.Tech.-CSE(Artificial Intelligence) courses under SVEC20 regulations, it is resolved to authorize the Chairperson, BoS to approve panel of question paper setters for theory subjects. Board suggested to submit the panel of question paper setters and model question papers, to the Principal well in advance of the commencement of class work.

4. **Any other item:**

Board members have made following suggestions:

*   The board felt that companies such as TCS, Infosys may not offer short-term internships for students to take up in summer vacation. Ms. Annie Joyce, Joint Director, CDAC, Bengaluru mentioned that CDAC is providing only long-term internships to students(internship of six months duration for UG students and one year duration for PG students). In this regard, board suggested to prepare alternative plans before making summer internships as mandatory to students of B.Tech. under SVEC20 regulations. Board suggested that internships may be offered by the institute for on- campus and off-campus students. The board also suggested to consider online internships, on-campus internships under internship evaluation procedure.

The BoS members appreciated the pro-active approach of college management and efforts taken by department for the growth of the department. The meeting concluded with thanks to the Chairperson, BoS.
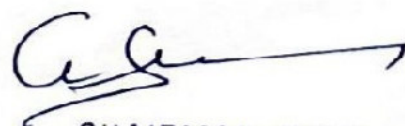
(Dr. G. Sunitha)

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(Dr. B. Narendra Kumar Rao)

PROFESSOR & HEAD
Dept. of Computer Science & Engineering
SREE VIDYANIKETHAN ENGINEERING COLLEGE
Sree Sainath Nagar, A. Rangampet
Chittoor (Dt.) - 517 102, A.P.

Sl. No.	Course Code	Course Title	Contact Periods per week				C	Scheme of Examination Max. Marks		
			L	T	P	Total		Int. Marks	Ext. Marks	Total Marks
1.	20BT1BS01	Differential Equations and Multivariable Calculus	3	-	-	3	3	30	70	100
2.	20BT1BS03	Engineering Physics	3	-	-	3	3	30	70	100
3.	20BT1HS01	Communicative English	3	-	-	3	3	30	70	100
4.	20BT11201	Programming for Problem Solving	3	-	-	3	3	30	70	100
5.	20BT10201	Basic Electrical and Electronics Engineering	3	-	-	3	3	30	70	100
6.	20BT1BS32	Engineering Physics Lab	-	-	3	3	1.5	30	70	100
7.	20BT1HS31	Communicative English Lab	-	-	3	3	1.5	30	70	100
8.	20BT11231	Programming for Problem Solving Lab	-	-	3	3	1.5	30	70	100
9.	20BT1MC01	Universal Human Values (Mandatory Course)	2	-	-	2	-	30	-	30
		Total:	17	-	09	26	19.5	270	560	830

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I B.Tech II–Semester

Sl. No.	Course Code	Course Title	Contact Periods per week				C	Scheme of Examination Max. Marks		
			L	T	P	Total		Int. Marks	Ext. Marks	Total Marks
1.	20BT2BS01	Transformation Techniques and Linear Algebra	3	-	-	3	3	30	70	100
2.	20BT1BS02	Engineering Chemistry	3	-	-	3	3	30	70	100
3.	20BT20501	Digital Logic Design	3	-	-	3	3	30	70	100
4.	20BT21501	Object Oriented Programming through Java	3	-	-	3	3	30	70	100
5.	20BT1BS31	Engineering Chemistry Lab	-	1	4	5	3	30	70	100
6.	20BT10331	Computer Aided Engineering Drawing	-	-	3	3	1.5	30	70	100
7.	20BT10332	Engineering Workshop	-	-	3	3	1.5	30	70	100
8.	20BT21531	Object Oriented Programming through Java Lab	-	-	3	3	1.5	30	70	100
9.	20BT1HSAC	Spoken English (Audit Course)	2	-	-	2	-	-	-	-
		Total:	14	01	13	28	19.5	240	560	800

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