

Department of Electronics and Communication Engineering

1.3.4 List of Students undertaken Field Projects/Internships/Student Projects and Supporting Evidences (During 2020-21)

Program: M.Tech – VLSI

S. NO.	Name of the Student	Roll Number	Class and Semester	Field Project / Internship / Student project	Start and end dates	Name of the organization, and place
1.	NARAKALA VIVEK	19121D5710	M.Tech, III & IV semester	Internship, Student Project	28.10.20 to 28.05.21 24.08.20 to 27.06.21	SION Semiconductors, Banglore Sree Vidyanikethan Engineering College
2.	AIYYAPPU SURENDRA BABU	19121D5701	M.Tech, III & IV semester	Student Project	24.08.20 to 27.06.21	Sree Vidyanikethan Engineering College
3.	AMBALA NIHARIKA	19121D5702	M.Tech, III & IV semester	Student Project	24.08.20 to 27.06.21	Sree Vidyanikethan Engineering College
4.	BOKKISAM PURUSHOTHAMAN	19121D5703	M.Tech, III & IV semester	Student Project	24.08.20 to 27.06.21	Sree Vidyanikethan Engineering College
5.	CHIKKANI RAJYALAKSHMI	19121D5704	M.Tech, III & IV semester	Student Project	24.08.20 to 27.06.21	Sree Vidyanikethan Engineering College
6.	GORIGARI MOHAMMED ALTHAF	19121D5705	M.Tech, III & IV semester	Student Project	24.08.20 to 27.06.21	Sree Vidyanikethan Engineering College
7.	GOVINDAREDDYGARI CHANDINI	19121D5706	M.Tech, III & IV semester	Student Project	24.08.20 to 27.06.21	Sree Vidyanikethan Engineering College
8.	GUJJUNURU SREEDEVI	19121D5707	M.Tech, III & IV semester	Student Project	24.08.20 to 27.06.21	Sree Vidyanikethan Engineering College
9.	KAJJAM MOUNIKA	19121D5708	M.Tech, III & IV semester	Student Project	24.08.20 to 27.06.21	Sree Vidyanikethan Engineering College
10.	LINGAMDINNE VAMSIKRISHNA REDDY	19121D5709	M.Tech, III & IV semester	Student Project	24.08.20 to 27.06.21	Sree Vidyanikethan Engineering College
11.	RASAPUTHRA RAMA KANTH SINGH	19121D5711	M.Tech, III & IV semester	Student Project	24.08.20 to 27.06.21	Sree Vidyanikethan Engineering College
12.	SAMUDRALA DIVYA LAKSHMI	19121D5712	M.Tech, III & IV semester	Student Project	24.08.20 to 27.06.21	Sree Vidyanikethan Engineering College
13.	SYED KHASIMUNNISA	19121D5713	M.Tech, III & IV semester	Student Project	24.08.20 to 27.06.21	Sree Vidyanikethan Engineering College
14.	T BHANU SANDHYA	19121D5714	M.Tech, III & IV semester	Student Project	24.08.20 to 27.06.21	Sree Vidyanikethan Engineering College
15.	VANNIYAR SATYANARAYANA	19121D5715	M.Tech, III & IV semester	Student Project	24.08.20 to 27.06.21	Sree Vidyanikethan Engineering College



PRINCIPAL
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SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)

Sree Sainath Nagar, A. RANGAMPET
Chittoor (Dist.) - 517 102, A.P., INDIA.

Date: 28/05/2021

To Whomsoever It May Concern

This is to certify that **Mr. Narakala Vivek** was working as "DV Intern" with us from **28th Oct'20** to **28th May'21**.

During the tenure of his Internship, we found him hard working, responsible and diligent.

One can feel free to contact us for any queries on his candidature. We wish him all the success in the future.



Sincerely,

Mr.Muralidher Reddy B

Human Resource





Department of Electronics and Communication Engineering
SREE VIDYANIKETHAN ENGINEERING
COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupathi - 517102.

Certificate

This is to certify that the project report entitled

“Design of ALU Design using 2T X-OR Gate and Decoder”

is the bonafide work done and submitted by

A.SURENDRA BABU (19121D5701)

in the Department of Electronics and Communication Engineering, Sree Vidyanikethan Engineering College, A.Rangampet, affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu in partial fulfillment of the requirements for the award of the degree of Master of Technology in VLSI during 2018-2020.

GUIDE

Dr N .ASHOK KUMAR, M.E, Ph.D.
Associate Professor Dept. of ECE

HOD

Dr. N.GIREESH M.Tech., Ph.D.
Professor & Head, Dept. of ECE.



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SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupathi - 517102.

Certificate

This is to certify that the project report entitled

“Design and Optimization of Three Input Floating Point Unit”

is the bonafide work done and submitted by

AMBALA NIHARIKA (19121D5702)

in the Department of Electronics and Communication Engineering, Sree Vidyanikethan Engineering College, A. Rangampet, affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu in partial fulfillment of the requirements for the award of the degree of Master of Technology in VLSI during 2019-2021

GUIDE

G. Nares
Mr. G. NARESH, M. Tech., (Ph.D)
Assistant Professor, Department of ECE

HOD

N. Gireesh
Dr. N. GIREESH, M.Tech., Ph.D.,
Professor & Head, Dept. of ECE.



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SREE VIDYANIKETHAN ENGINEERING COLLEGE
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Sree Sainath Nagar, A. Rangampet, Tirupathi - 517102.

Certificate

This is to certify that the project report entitled

“A Novel GDI Latch for Sense Amplifier Based Flip-flop”

is the bonafide work done and submitted by

B. PURUSHOTHAMAN (19121D5703)

in the Department of Electronics and Communication Engineering, Sree Vidyanikethan Engineering College, A.Rangampet, affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu in partial fulfillment of the requirements for the award of the degree of Master of Technology in VLSI during 2018-2020.

GUIDE

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Sree Sainath Nagar, A. Rangampet, Tirupathi - 517102.

Certificate

This is to certify that the project report entitled


**“Floating Point MAC Using Distributed Arithmetic and Offset
Binary Coding Techniques for Efficient DSP”**

is the bonafide work done and submitted by


C. RAJYALAKSHMI (19121D5704)

*in the Department of Electronics and Communication Engineering, Sree Vidyanikethan Engineering
College, A. Rangampet, affiliated to Jawaharlal Nehru Technological University
Anantapur, Anantapuramu in partial fulfillment of the requirements for the award of the
degree of Master of Technology in VLSI during 2019-2021*

GUIDE


Ms. M. Bharathi, M. Tech., (Ph.D)
Assistant Professor, Department of ECE

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Sree Sainath Nagar, A. Rangampet, Tirupathi - 517102.

Certificate


This is to certify that the project report entitled
“Design and Optimization of Different parameters
of a Capacitive Pressure Sensor for Biomedical
Applications”

is the bonafide work done and submitted by


G.MOHAMMED ALTHAF (19121D5705)

in the Department of Electronics and Communication Engineering, Sree Vidyanikethan Engineering College, A.Rangampet, affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu in partial fulfillment of the requirements for the award of the degree of Master of Technology in VLSI during 2018-2020.

GUIDE


Mr. KAUSTUBH KUMAR SHUKLA, M.E.(Ph.D).,
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Certificate

This is to certify that the project report entitled

**“Matrix Codes for Diabetic Retinopathy Images in
Telemedicine”**

is the bonafide work done and submitted by

GOVINDAREDDYGARI CHANDINI (19121D5706)

*in the Department of Electronics and Communication Engineering,
Sree Vidyanikethan Engineering College, A.Rangampet, affiliated to
Jawaharlal Nehru Technological University Anantapur, Anantapuramu in
partial fulfillment of the requirements for the award of the degree of
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GUIDE

neelima

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HOD

GW

**Dr. N. Gireesh, M.Tech., Ph.D.,
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Certificate

This is to certify that the project report entitled

“Image Encryption with Automatic Key Generation”

is the bonafide work done and submitted by

G. SREE DEVI (19121D5707)

in the Department of Electronics and Communication Engineering, Sree Vidyanikethan Engineering College, A.Rangampet, affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu in partial fulfillment of the requirements for the award of the degree of Master of Technology in VLSI during 2019-2021

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Sree Sainath Nagar, A. Rangampet, Tirupathi - 517102.

Certificate

This is to certify that the project report entitled

**FPGA IMPLEMENTATION OF AREA
EFFICIENT FLOATING POINT ALU UNIT**

is the bonafide work done and submitted by

KAJJAM MOUNIKA (19121D5708)

*in the Department of Electronics and Communication Engineering,
Sree Vidyanikethan Engineering College, A.Rangampet, affiliated to
Jawaharlal Nehru Technological University Anantapur, Ananthapuramu
in partial fulfillment of the requirements for the award of the degree of
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GUIDE

A blue ink signature of Dr. P. V. RAMANA.

Dr. P. V. RAMANA, M.Tech., Ph.D,
Professor of ECE & Dean(Academics)

HOD

A blue ink signature of Dr. N. GIREESH.

Dr. N. GIREESH, M.Tech., Ph.D.
Professor & Head, Dept. of ECE.



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SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)
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Certificate

This is to certify that the project report entitled

“Adjustable Radio Technology Using EPSK Modulation”

is the bonafide work done and submitted by

L.VAMSIKRISHNA REDDY(19121D5709)

in the Department of Electronics and Communication Engineering, Sree Vidyanikethan Engineering College, A.Rangampet, affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu in partial fulfillment of the requirements for the award of the degree of Master of Technology in VLSI during 2019-2021

GUIDE

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SREE VIDYANIKETHAN ENGINEERING
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Certificate

This is to certify that the project report entitled

*“An Effective Approach to Reduce Standard cell Local
utilization and Congestion using TCL”*

is the bonafide work done and submitted by

N. VIVEK (19121D5710)

*in the Department of Electronics and Communication Engineering, Sree
Vidyanikethan Engineering College, A. Rangampet, affiliated to Jawaharlal Nehru
Technological University Anantapur, Anantapuramu in partial fulfillment
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GUIDE

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Department of Electronics and Communication Engineering
SREE VIDYANIKETHAN ENGINEERING COLLEGE
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Certificate

This is to certify that the project report entitled

“High Performance Ternary Logic Gates Using GNR-FET”

is the bonafide work done and submitted by

R.RAMA KANTH SINGH (19121D5711)

in the Department of Electronics and Communication Engineering, Sree Vidyanikethan Engineering College, A.Rangampet, affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu in partial fulfillment of the requirements for the award of the degree of Master of Technology in VLSI during 2019-2021

GUIDE 

**Dr. D Leela Rani, M.Tech., Ph.D.,
Professor, Department of ECE**

HOD 

**Dr. N. GIREESH, M.Tech., Ph.D.,
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SREE VIDYANIKETHAN ENGINEERING COLLEGE
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Sree Sainath Nagar, A. Rangampet, Tirupathi - 517102.

Certificate

This is to certify that the project report entitled
“Proficient Matrix Codes for NOC Applications”
is the bonafide work done and submitted by

S. DIVYA LAKSHMI (19121D5712)

*in the Department of Electronics and Communication Engineering,
Sree Vidyanikethan Engineering College, A.Rangampet, affiliated to
Jawaharlal Nehru Technological University Anantapur, Anantapuramu in
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GUIDE

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GN

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SREE VIDYANIKETHAN ENGINEERING COLLEGE
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Sree Sainath Nagar, A. Rangampet, Tirupathi - 517102.

Certificate

This is to certify that the project report entitled
**“Design and Optimization of Hybrid Multiplier and
Comparative Study of Various Multipliers”**

is the bonafide work done and submitted by

S.KHASIMUNNISA(19121D5713)

*in the Department of Electronics and Communication Engineering, Sree Vidyanikethan
Engineering College, A.Rangampet, affiliated to Jawaharlal Nehru Technological
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GUIDE

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Department of Electronics and Communication Engineering
SREE VIDYANIKETHAN ENGINEERING COLLEGE
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Certificate

This is to certify that the project report entitled

“Design and Synthesis of Sequential Circuits using Reversible Logic”

is the bonafide work done and submitted by

T. BHANU SANDHYA (19121D5714)

in the Department of Electronics and Communication Engineering, Sree Vidyanikethan Engineering College, A. Rangampet, affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu in partial fulfillment of the requirements for the award of the degree of Master of Technology in VLSI during 2019-2021

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C.V. Sudhakar
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Assistant Professor, Department of ECE

HOD

N. Gireesh
Dr. N. GIREESH, M.Tech., Ph.D.,
Professor & Head, Dept. of ECE.



Department of Electronics and Communication Engineering
SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupathi - 517102.

Certificate

This is to certify that the project report entitled


*“Optimal of 1-bit Comparator Design and Energy Estimation
using Quantum Dot Cellular Automata”*

is the bonafide work done and submitted by


V. SATYANARAYAN (19121D5715)

*in the Department of Electronics and Communication Engineering,
Sree Vidyanikethan Engineering College, A.Rangampet, affiliated to
Jawaharlal Nehru Technological University Anantapur, Anantapuramu in
partial fulfillment of the requirements for the award of the degree of
Master of Technology in VLSI during 2019-2021.*

GUIDE


Mr. M. BALAJI, M.Tech., (Ph.D).,
Assistant Professor, Dept. of ECE

HOD


Dr. N. GIREESH, M.Tech., Ph.D.,
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