

**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
(Autonomous)  
Sree Sainath Nagar, A. Rangampet-517 102

**Agenda for 7<sup>th</sup> Meeting of Board of Studies for B. Tech. in Electronics and Communication Engineering on 22.4.2016 @ 2:15 PM**

1. Action taken report on minutes/ resolutions of 6<sup>th</sup> BOS meeting.
2. Report on implementation of autonomy with respect to syllabi and regulations (SVEC14 II B. Tech.).
3. B. Tech. Academic Calendar for 2016-17.
4. Vision and Mission of the Department, PEOs, POs & PSOs and Course Structure of B. Tech. Program.
5. Administrative Setup for B. Tech. in ECE.
6. Syllabi with Course Objectives & Outcomes, Lesson Plans and Question Banks of Courses of I B. Tech.
7. Panel of paper setters for I Semester Courses of I B. Tech.
8. Teaching, Learning and Evaluation.
9. Research Activities.
10. Faculty Development Activities.
11. Staff Development Activities.
12. Student Development Activities.
13. Student Support Systems.
14. Extension Activities.
15. Any other item.

  
Chairman, BOS in ECE

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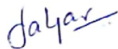


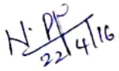

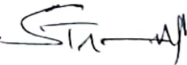

**Department of Electronics & Communication Engineering**

**Board of Studies for B. Tech. in Electronics and**

**Communication Engineering**

**7<sup>th</sup> Meeting held on 22<sup>nd</sup> April 2016 @ 2:15 PM**

**Members Present:**

S.No.	Name and Address of the member	Signature
1.	<b>Dr. C. Subhas,</b> Professor of ECE & Dean (Academics), Sree Vidyanikethan Engineering College, Sree Sainath Nagar - 517102, Tirupati.	
2.	<b>Mr. K. Sagar</b> Design Engineer-II, Advanced Micro Devices India Pvt. Ltd. Building No. 11, Mind Space, Madhapur, HYDERABAD - 500081	
3.	<b>Dr. P Venkata Ramana,</b> Professor and Head, Dept. of ECE, Sree Vidyanikethan Engineering College, Sree Sainath Nagar - 517102, Tirupati.	
4.	<b>Dr. V R Anitha,</b> Professor, Dept. of ECE, Sree Vidyanikethan Engineering College, Sree Sainath Nagar - 517102, Tirupati.	 22/04/16
5.	<b>Dr. N. Padmaja,</b> Professor, Dept. of ECE, Sree Vidyanikethan Engineering College, Sree Sainath Nagar - 517102, Tirupati.	 N. P. 22/4/16
6.	<b>Dr. D Leela Rani,</b> Professor, Dept. of ECE, Sree Vidyanikethan Engineering College, Sree Sainath Nagar - 517102, Tirupati.	
7.	<b>Mr. S. Tulasi Prasad,</b> Associate Professor, Dept. of ECE, Sree Vidyanikethan Engineering College, Sree Sainath Nagar - 517102, Tirupati.	
8.	<b>Ms. K. Neelima,</b> Assistant Professor, Dept. of ECE, Sree Vidyanikethan Engineering College, Sree Sainath Nagar - 517102, Tirupati.	

9.	<b>Dr. P. Geetha</b> Associate Professor, Dept. of ECE, Sree Vidyanikethan Engineering College, Sree Sainath Nagar – 517102, Tirupati.	<i>P Geetha</i> 22/6/16
10.	<b>Dr. A. B. Yadav</b> Associate Professor, Dept. of ECE, Sree Vidyanikethan Engineering College, Sree Sainath Nagar – 517102, Tirupati.	<i>A. B. Yadav</i>
11.	<b>Ms. M. Bharathi</b> Assistant Professor, Dept. of ECE, Sree Vidyanikethan Engineering College, Sree Sainath Nagar – 517102, Tirupati.	<i>M. Bharathi</i>

Dr. NVS Narasimha Sarma, Dr. K. Babulu, Dr. K. Ramanaidu and Mr. T. R. Rama Mohan could not attend the meeting.

### Minutes:

#### 1. Action Taken Report on minutes of BOS meeting on 18<sup>th</sup> April 2015

The Chairman BOS appraised the members the action taken on the minutes/ resolutions of BOS meeting held on 18<sup>th</sup> April 2015. After deliberations the members ratified the report and the same is placed at Annexure – I.

#### 2. Report on implementation of Autonomy

The Chairman BOS presented a report on implementation of autonomy pertaining to regulations and I year courses to the members. The report is accepted after discussions and the same is placed at Annexure-II.

#### 3. Academic Calendars

The Chairman BOS presented the members the academic calendars for II, III and IV B. Tech. classes for the academic year 2016-'17. After deliberations and discussions the calendars are approved and the same are placed at Annexure-III. As the admission dates are not finalized for I B. Tech. classes during the academic year 2016-'17, it is resolved to authorize the Chairman BOS to prepare academic calendar for I B. Tech. and to submit to the Chairman (Academic Council) for approval.

#### 4. Vision & Mission of the Dept., PEOs, POs & PSOs and Course Structure

The Chairman BOS has appraised the members the Vision & Mission of the dept. which was already approved, for continuity. The Chairman BOS has presented the Program Education Objectives, Program Outcomes, Program Specific Outcome and Course Structure. After deliberations and discussions, the members have ratified the PEOs, POs and PSOs. Certain changes are suggested in the course structure. The PEOs, POs, PSOs and the modified Course Structure are placed at Annexure-IV.

#### 5. Administrative Setup

The Chairman BOS appraised the members that the syllabi is prepared in a decentralized way involving all the faculty members and keeping checks at multiple levels to ensure

quality output. The members have appreciated the efforts of the department and ratified the same after discussions. The ratified administrative setup is placed at Annexure-V.

#### **6. Syllabi with Course Objectives and Outcomes, Lesson Plans and Question Bank for I B. Tech. Courses**

The Chairman BOS presented the draft syllabi prepared by the department. After deliberations and discussions, the members have suggested certain improvements to the syllabi, lesson plans and question banks. It is resolved to authorize the Chairmen BOS to adapt the improvements suggested, finalize the syllabi with lesson plan & question banks and submit to the Chairman (Academic Council) for further processing.

#### **7. Panel of paper setters for I B. Tech. Courses**

After discussions, the board has suggested certain changes to the panels. It is resolved to authorize the Chairman BOS to make suggested changes to the panels and submit the same to the Chairman (Academic Council) for further processing.

#### **8. Teaching, Learning and Evaluation**

The Chairman BOS presented the teaching, learning and evaluation practices followed in the department. The members of BOS have appreciated the good practices followed by the department in the conduct of diagnostic and formative tests and remedial classes and suggested to continue the same.

#### **9. Research Activities**

The Chairman BOS has briefed the members about the research initiatives, such as thrust areas of research, research clusters, technical talks and research project proposals. The chairman also appraised the members that the renowned researchers namely Dr. Prahlada, Dr. Lazar Mathew and Dr. Madhusudan V Atre keep visiting the campus for guiding the faculty members in research activities. The members have appreciated the efforts of the college in providing such expert guidance. It is suggested to encourage the faculty members to intensify their research efforts as an excellent guidance is available.

#### **10. Faculty Development Activities**

The Chairman BOS has presented the members information on the research publications/ presentations by the faculty, books published, faculty incentives, training need analysis for faculty, qualification up-gradation, FDPs attended, FDPs organized, and student feedback on faculty. The members have appreciated the efforts and suggested to organize few faculty development programs in frontier areas.

#### **11. Staff Development Activities**

The Chairman BOS has presented the members information on training need analysis for staff and qualification up-gradation. The members have appreciated the efforts of the department and suggested to continue the same. The members have suggested to sponsor staff to development programs to premier training institutes and industry.

#### **12. Students Development Activities**

The members perused and noted the information provided on Gold medals, Guest/ Expert Lectures organized, Student paper publications, Prizes won in Co-curricular and

extra-curricular activities. The members have appreciated the student centric activities of the department and suggested to conduct add-on courses as and when required.

### **13. Student Support Systems**

The members noted the information on good practices such as Counseling Mechanism, CPRS, Training Activities by Seventh Sense & Campus Selections/ Companies visited, Entrepreneurship Activities, and IIIC activities followed for all round growth and development of students. The BOS members have appreciated the proactive approach of the college in organizing the above activities for increasing the employability of the students and suggested to continue the same.

### **14. Extension Activities**

The members have perused, noted and appreciated the efforts of department/ college in organizing professional, technical and community development activities. The BOS members have suggested to continue the same.

### **15. Quality Initiatives**

The Chairman BOS appraised the members that the department is in the process of applying for re-accreditation of B. Tech. program. The Chairman also appraised the members that the college got additional grant of Rs. 2 crore from TEQIP-II under subcomponent 1.1. The members have appreciated the efforts of the department in the above quality initiatives.

Meeting concluded with thanks to the Chair.

  
**Chairman, BoS in ECE**

**SREE VIDYANIKETHAN ENGINEERING COLLEGE****(Autonomous)****COURSE STRUCTURE (2014-2015)****ELECTRONICS AND COMMUNICATION ENGINEERING  
I Year B.Tech. (Yearly Pattern)**

Code	Subject	Periods per week			C	Scheme of Examination Max. Marks		
		L	T	P		Int.	Ext.	Total
14BT1HS01	Technical English	2	-	-	4	30	70	100
14BT1BS01	Engineering Physics	2	1	-	4	30	70	100
14BT1BS02	Engineering Chemistry	2	1	-	4	30	70	100
14BT1BS03	Engineering Mathematics	3	1	-	6	30	70	100
14BT1BS04	Mathematical Methods	3	1	-	6	30	70	100
14BT1ES01	Programming in C and Data Structures	3	1	-	6	30	70	100
14BT1ES03	Computer-Aided Engineering Drawing	-	1	3	3	25	50	75
14BT1BS05	Engineering Physics and Engineering Chemistry Lab.	-	-	3	3	25	50	75
14BT1ES04	Programming in C and Data Structures Lab.	-	-	3	3	25	50	75
14BT1ES06	Engineering and IT Workshop	-	-	3	3	25	50	75
14BT1HS02	English Language Communication Skills Lab.	-	-	3	3	25	50	75
	<b>TOTAL</b>	<b>15</b>	<b>6</b>	<b>15</b>	<b>45</b>	<b>305</b>	<b>670</b>	<b>975</b>

**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
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**COURSE STRUCTURE (2014-2015)**  
**ELECTRONICS AND COMMUNICATION ENGINEERING**  
**II B.Tech. I Semester**

Course Code	Course Title	Periods per week			C	Scheme of Examination Max. Marks		
		L	T	P		Int.	Ext.	Total
14BT3BS02	Special Functions and Complex Analysis	3	1	-	3	30	70	100
14BT3HS01	Environmental Sciences	3	1	-	3	30	70	100
14BT30401	Probability and Stochastic Processes	3	1	-	3	30	70	100
14BT30402	Semiconductor Devices and Circuits	3	1	-	3	30	70	100
14BT30231	Electrical Technology	3	1	-	3	30	70	100
14BT30232	Network Analysis	3	1	-	3	30	70	100
14BT30421	Semiconductor Devices and Circuits Lab	-	-	3	2	25	50	75
14BT30222	Electrical Technology Lab	-	-	3	2	25	50	75
<b>Total:</b>		<b>18</b>	<b>6</b>	<b>6</b>	<b>22</b>	<b>230</b>	<b>520</b>	<b>750</b>

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**COURSE STRUCTURE (2014-2015)**  
**ELECTRONICS AND COMMUNICATION ENGINEERING**  
**II B.Tech. II Semester**

Course Code	Course Title	Periods per week			C	Scheme of Examination Max. Marks		
		L	T	P		Int.	Ext.	Total
14BT4HS02	Professional Ethics	3	1	-	3	30	70	100
14BT40401	Analog Communications	3	1	-	3	30	70	100
14BT40402	Electronic Circuit Analysis and Design	3	1	-	3	30	70	100
14BT40403	Electromagnetic Theory and Transmission Lines	3	1	-	3	30	70	100
14BT40404	Signals and Systems	3	1	-	3	30	70	100
14BT40405	Switching Theory and Logic Design	3	1	-	3	30	70	100
14BT50201	Control Systems	3	1	-	3	30	70	100
14BT40421	Electronic Circuit Analysis and Design Lab	-	-	3	2	25	50	75
14BT40422	Signals and Systems Lab	-	-	3	2	25	50	75
<b>Total:</b>		<b>21</b>	<b>7</b>	<b>6</b>	<b>25</b>	<b>260</b>	<b>590</b>	<b>850</b>



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**COURSE STRUCTURE (2014-2015)**  
**ELECTRONICS AND COMMUNICATION ENGINEERING**  
**III B.Tech. I Semester**

Course Code	Course Title	Periods per week			C	Scheme of Examination Max. Marks		
		L	T	P		Int.	Ext.	Total
14BT4HS01	Business Communication and Presentation Skills	3	1	-	3	30	70	100
14BT5HS01	Managerial Economics and Principles of Accountancy	3	1	-	3	30	70	100
14BT50401	Antennas and Propagation	3	1	-	3	30	70	100
14BT50402	Digital Communications	3	1	-	3	30	70	100
14BT50403	Digital IC Applications	3	1	-	3	30	70	100
14BT50404	Linear IC Applications	3	1	-	3	30	70	100
14BT50405	Pulse and Digital Circuits	3	1	-	3	30	70	100
14BT50421	Analog and Digital Communications Lab	-	-	3	2	25	50	75
14BT50422	PDC and IC Lab	-	-	3	2	25	50	75
<b>Total:</b>		<b>21</b>	<b>7</b>	<b>6</b>	<b>25</b>	<b>260</b>	<b>590</b>	<b>850</b>

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**COURSE STRUCTURE (2014-2015)**  
**ELECTRONICS AND COMMUNICATION ENGINEERING**  
**III B.Tech. II Semester**

Course Code	Course Title	Periods per week			C	Scheme of Examination Max. Marks		
		L	T	P		Int.	Ext.	Total
14BT60401	Digital Signal Processing	3	1	-	3	30	70	100
14BT60402	Microwave Engineering	3	1	-	3	30	70	100
14BT60403	VLSI Design	3	1	-	3	30	70	100
14BT60201	Microprocessors and Microcontrollers	3	1	-	3	30	70	100
14BT40501	Computer Organization	3	1	-	3	30	70	100
Professional Elective-I		3	1	-	3	30	70	100
14BT60404	Analog IC Design							
14BT60405	Image Processing							
14BT60406	TV & Radar Engineering							
14BT41201	Object Oriented Programming							
14BT60421	Digital IC Applications Lab	-	-	3	2	25	50	75
14BT60222	Microprocessors and Microcontrollers Lab	-	-	3	2	25	50	75
<b>Total:</b>		<b>18</b>	<b>6</b>	<b>6</b>	<b>22</b>	<b>230</b>	<b>520</b>	<b>750</b>

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**COURSE STRUCTURE (2014-2015)**  
**ELECTRONICS AND COMMUNICATION ENGINEERING**  
**IV B.Tech. I Semester**

Course Code	Course Title	Periods per week			C	Scheme of Examination Max. Marks		
		L	T	P		Int.	Ext.	Total
14BT70401	Electronic Measurements and Instrumentation	3	1	-	3	30	70	100
14BT70402	Embedded Systems	3	1	-	3	30	70	100
14BT70403	Light Wave Communications	3	1	-	3	30	70	100
14BT51201	Computer Networks	3	1	-	3	30	70	100
--	Open Elective	3	1	-	3	30	70	100
Professional Elective-II		3	1	-	3	30	70	100
14BT70404	Advanced Digital Signal Processing							
14BT70405	Digital CMOS IC Design							
14BT70406	Telecommunication Switching Systems							
14BT51501	Operating Systems							
14BT70421	Digital Signal Processing Lab	-	-	3	2	25	50	75
14BT70422	Microwave and Light Wave Communications Lab	-	-	3	2	25	50	75
14BT70423	Seminar	-	-	-	2	-	50	50
<b>Total:</b>		<b>18</b>	<b>6</b>	<b>6</b>	<b>24</b>	<b>230</b>	<b>570</b>	<b>800</b>

# SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

## COURSE STRUCTURE (2014-2015)

### ELECTRONICS AND COMMUNICATION ENGINEERING

#### IV B.Tech. I Semester

#### Open Electives

Offering Dept	Course Code	Course Title
BS&H	14BT6HS01	Banking and Insurance
BS&H	14BT6HS02	Cost Accounting and Financial Management
BS&H	14BT6HS03	Entrepreneurship for Micro, Small and Medium Enterprises
CE	14BT70105	Disaster Mitigation and Management
CE	14BT70106	Environmental Pollution and Control
CE	14BT70107	Contract Laws and Regulations
CE	14BT70108	Planning for Sustainable Development
CE	14BT70109	Rural Technology
ME	14BT60305	Artificial Intelligence and Robotics
ME	14BT60306	Global Strategy and Technology
ME	14BT60307	Intellectual Property Rights and Management
ME	14BT60308	Managing Innovation and Entrepreneurship
ME	14BT60309	Material Science
CSE	14BT60502	Engineering Systems Analysis and Design
EIE	14BT71005	Microelectromechanical Systems
IT	14BT61203	Bio-Informatics
IT	14BT61204	Cyber Security and Laws

### IV Year – II Semester

Course Code	Course Title	Periods per week			C	Scheme of Examination Max. Marks		
		L	T	P		Int.	Ext.	Total
14BT5HS02	Management Science	3	1	-	3	30	70	100
14BT80401	Cellular and Mobile Communications	3	1	-	3	30	70	100
<b>Professional Elective-III</b>		3	1	-	3	30	70	100
14BT80402	Mixed Signal Design							
14BT80403	Satellite Communications							
14BT80531	Wireless Sensor Networks							
14BT81202	Cryptography and Network Security							
<b>Professional Elective-IV</b>		3	1	-	3	30	70	100
14BT80404	Low Power VLSI Design							
14BT80405	Speech Processing							
14BT80406	Spread Spectrum Communications							
14BT40502	Data Base Management Systems							
14BT80421	Comprehensive Viva-Voce	-	-	-	2	-	100	100
14BT80422	Project Work	-	-	20	10	60	140	200
<b>Total:</b>		<b>12</b>	<b>4</b>	<b>20</b>	<b>24</b>	<b>180</b>	<b>520</b>	<b>700</b>
<b>Grand Total:</b>					<b>187</b>	<b>1695</b>	<b>3980</b>	<b>5675</b>

  
 Chairman, BOS in ECE

**LIST OF NEW COURSES INTRODUCED**

**Program: B.Tech.- Electronics and Communication Engineering**

**Regulations : SVEC-14**

Course Code	Name of the course
14BT60404	Analog IC Design
14BT6HS01	Banking and Insurance
14BT6HS02	Cost Accounting and Financial Management
14BT6HS03	Entrepreneurship for Micro, Small and Medium Enterprises
14BT60305	Artificial Intelligence and Robotics
14BT60306	Global Strategy and Technology
14BT60307	Intellectual Property Rights and Management
14BT60308	Managing Innovation and Entrepreneurship
14BT60309	Material Science
14BT70405	Digital CMOS IC Design
14BT70106	Environmental Pollution and Control
14BT70107	Contract Laws and Regulations
14BT70108	Planning for Sustainable Development
14BT70109	Rural Technology
14BT80402	Mixed Signal Design
14BT80531	Wireless Sensor Networks
14BT80405	Speech Processing

  
Chairman, BOS in ECE