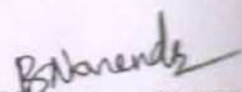# SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, A. Rangampet-517 102

Agenda for 7th Meeting of Board of Studies for B. Tech. in Computer Science and Systems Engineering on 22.4.2016 @ 2:15 PM

1. Action taken report on minutes/ resolutions of 6th BOS meeting.

2. Report on implementation of autonomy with respect to syllabi and regulations (SVEC14 II B. Tech.).

3. B. Tech. Academic Calendar for 2016-17.

4. Vision and Mission of the Department, PEOs, POs & PSOs and Course Structure of B. Tech. Program.

5. Syllabi with Course Objectives & Outcomes, Lesson Plans and Question Banks of Courses of I B. Tech.

6. Panel of paper setters for I Semester Courses of I B. Tech.

7. Teaching, Learning and Evaluation.

8. Research Activities.

9. Faculty Development Activities.

10. Staff Development Activities.

11. Student Development Activities.

12. Student Support Systems.

13. Extension Activities.

14. Any other item.

Chairman (BOS)

Members Present

✓

Meeting concluded with thanks to the chair

S.No	Name of the Person	Signature
External Members		
1.	**Prof. O.B.V. Ramanaiah,** Professor in CSE Dept., JNTUH College of Engineering, Hyderabad. Phone: 9440501888, email: obvramanaiah@jntuh.ac.in	
2.	**Dr. Kannan Srinathan,** Assistant Professor, IIIT, Hyderabad Phone: 9885311702, Email id: srinathan@iiit.ac.in	K. Srinathan
3.	**Dr. Kontham Raja Kumar,** Assistant Professor, Department of Computer Science and Systems Engineering, Andhra University College of Engineering (Autonomous), Andhra University, Visakapattanam Phone: 9490132765, Email id: krajakumar@yahoo.com	K. R 22/4/16
Industrial Member		
4.	**Mr. M. Muni Kumar,** Technical Services Lead, IBM India Pvt. Ltd., Bangalore Phone:+91- 9036795463, Email id: munikumar.ksn@gmail.com	Mallu Ren
Alumni Member		
5.	**Mr. Krishna Prashanth T,** Systems Engineer, TCS, Bangalore Phone: +91-9686899986, Email id: krishnaprashanth.csse@gmail.com	
Internal Members		
6.	**Mr. B. Narendra Kumar Rao,** Associate Professor & HOD, Department of CSSE, SVEC, A.Rangampet	BNarendr
7.	**Ms K. Lalitha,** Assistant Professor (SL), Department of CSSE, SVEC, A.Rangampet	Kavita
8.	**Ms C. Sushama,** Assistant Professor, Department of CSSE, SVEC, A.Rangampet	C. Sushe
9.	**Ms T. Kavitha,** Assistant Professor, Department of CSSE, SVEC, A.Rangampet	—
10.	**Ms P. Dhana Lakshmi,** Assistant Professor, Department of CSSE, SVEC, A.Rangampet	dhanu
11.	**Mr. A. Chandra,** Assistant Professor, Department of CSSE, SVEC, A.Rangampet	A. Chandra

BNarendr
Chairman-BOS, CSSE

Action Taken Report on Minutes/ Resolutions of

6th BOS Meeting held on 19th April 2015

1. Academic Calendar for B.Tech. Programme:

As approved in 6th BOS Meeting held on 19th April 2015, Academic Calendar for B.Tech. programme was implemented for the academic year 2015-2016.

2. Substitute Subjects

The Chairman, BOS (CSSE) was authorized to identify the substitute subjects for the readmitted students. The Academic Council approved the list of substitute subjects prepared for the academic year 2015-2016.

3. Model Question Papers and Panel of Paper Setters

The Chairman, BOS (CSSE) was authorized to prepare the model question papers and panel of question paper setters for theory subjects. And it was submitted to the chairman, Academic Council for approval. It was approved and implemented.

4. Add-on Courses

Add-on courses approved in the BOS meeting were successfully conducted for B.Tech. students

5. Academic, Research and Extension Activities

Some improvements were taken up in academic, research and extension activities. Project proposals were prepared for various funding agencies. DST Seminar Grant was received for conduct of Two-Day National Seminar on "Internet of Things". Research proposals are under preparation.

6. Faculty and Staff Development Activities

As approved in the 6th BOS meeting, faculty and staff members have undergone training programs in various reputed institutions as per TNA.

7. Revision of Syllabi and Regulations

Syllabus for II, III and IV B.Tech. CSSE is approved for SVEC 14 regulations.

8. Suggestions from previous BOS meeting

- Human Computer Interaction was removed from Course Structure.
- Included Embedded Systems Lab in IV B.Tech I Semester under SVEC-16 regulations.
- Kernel Programming subject is introduced in to the Curriculum.
- Case study on Windows and Linux included in Operating System subject.
- Embedded System Architecture renamed to Embedded System Programming in IV B.Tech I Semester in SVEC-14 Regulations.

Minutes:

1. Resolved to ratify Action Taken Report on minutes / resolutions of the 6th BOS meeting held on 19-04-2015 submitted by the Chairman, BOS (CSSE).   (Refer Annexure – I)

2. Chairman BOS presented a report on implementation of autonomy to the members. The board has congratulated the college authorities for successful implementation of SVEC14 regulations and syllabus for II Year. (Refer Annexure-II).

3. It was resolved to approve the academic calendar of II, III and IV B.Tech. for the academic year 2016-2017. (Refer Annexure-III)

4. Approved the Vision, Mission, PEOs, POs, PSOs and Course Structure (SVEC-16) of the UG Programme after thorough discussion. (Refer Annexure IV)

5. Chairman, BOS (CSSE) presented the course structure and syllabus of I B.Tech. CSSE to the members. It is resolved to accept the course structure and syllabus of I B.Tech. (CSSE) after thorough discussion. Suggestions and remarks given by members were taken into consideration. Model question papers and panel of question paper setters of theory subjects were approved by the members of BOS. (Refer Annexure V)

6. Panels of Question Paper setters for I B.Tech I Semester were presented to the Members of BOS and they were approved. It is resolved to authorize the Chairman, BOS (CSSE) to make changes if necessary in panel of question paper setters for each theory subject. Submit the panel of question paper setters and model question papers, to the Principal well in advance of the commencement of class work. (Refer Annexure VI)

7. The members of BOS have appreciated the good practices followed by the department in conducting diagnostic test, formative tests, remedial and reinforcement classes as well as offering of the necessary add-on courses. (Refer Annexure VII)

8. The Chairman, BOS (CSSE) has briefed the members on research initiatives in the Department. The Chairman also submitted the details of faculty publications in refereed Journals and Technical talks delivered during the academic year. (Refer Annexure VIII)

9. Members appreciated the faculty development activities, faculty feedback mechanism and action taken on faculty who got less feedback in the department. Resolved to authorize the head of the department to update TNA as and when required. (Refer Annexure IX)

10. The Chairman, BOS (CSSE) submitted the details of student development activities such as equity action plan & remediation, Guest/Expert lectures, student paper presentations and technical association activities. Members have expressed their satisfaction regarding the student development activities. (Refer Annexure X)

11. The Chairman, BOS (CSSE) presented the student support activities such as training programs, placement activities, student counseling, etc. carried out in the academic year 2015-16. The members have appreciated the efforts and suggested to continue. (Refer Annexure XI, XII)

12. Any other item

Chairman presented the administrative setup of the department for framing the SVEC16 syllabus of II, III & IV B.Tech. to the members. Members have appreciated the effort. (Refer Annexure XIII)

13. Suggestions

Course structure:

Check prerequisites of the courses Computer Organization/Operating Systems and Theory of Computation/Design Analysis Algorithms.

Suggested following reshuffling for electives

Inter Disciplinary Elective-I (III B.Tech I Semester)	Inter Disciplinary Elective-I (III B.Tech II Semester)
Computer Graphics	Data Analytics(New)
Scripting Languages(Include Python and Perl)	Digital Image processing
Natural Language Processing (New)	Optimization Techniques
Computational Biology (New)	Mobile application Development
Program Elective-I **(III B.Tech II Semester)**	**Program Elective-II** **(IV B.Tech I Semester)**
Advanced Java Programming	Network programming
Software Testing Techniques	High Performance Computing
Software Project Management	Cyber Security (New)
Kernel programming	Intelligent Computing Systems

Cryptography and Network Security should be included.

Digital Logic Design syllabus:

Introduce FPGA concept as Self Learning

Approved Course Structure

Program   : B.Tech.- Computer Science and Systems Engineering
Regulations : SVEC-14

SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)
COURSE STRUCTURE (2014-2015)
COMPUTER SCIENCE AND SYSTEMS ENGINEERING

I Year B.Tech. (Yearly Pattern)

S. No	Code	Subject	Periods per week			C	Scheme of Examinations Max. Marks		
			L	T	P		Int.	Ext.	Total
1	14BT1HS01	Technical English	2	-	-	4	30	70	100
2	14BT1BS01	Engineering Physics	2	1	-	4	30	70	100
3	14BT1BS02	Engineering Chemistry	2	1	-	4	30	70	100
4	14BT1BS03	Engineering Mathematics	3	1	-	6	30	70	100
5	14BT1BS04	Mathematical Methods	3	1	-	6	30	70	100
6	14BT1ES02	Problem Solving and Computer Programming	3	1	-	6	30	70	100
7	14BT1ES03	Computer Aided Engineering Drawing	-	1	3	3	25	50	75
8	14BT1BS05	Engineering Physics and Engineering Chemistry Lab	-	-	3	3	25	50	75
9	14BT1ES05	Problem Solving and Computer Programming Lab	-	-	3	3	25	50	75
10	14BT1ES06	Engineering and IT Workshop	-	-	3	3	25	50	75
11	14BT1HS02	English Language communication skill Lab	-	-	3	3	25	50	75
		Total	**15**	**6**	**15**	**45**	**305**	**670**	**975**

II Year – I Semester

S. No	Code	Subject	Periods per week			C	Scheme of Examinations Max. Marks		
			L	T	P		Int.	Ext.	Total
1	14BT3BS03	Probability and statistics	3	1	-	3	30	70	100
2	14BT30236	Foundations of Electrical Engineering	3	1	-	3	30	70	100
3	14BT30431	Electronic Devices and Circuits	3	1	-	3	30	70	100
4	14BT30501	Data Structures	3	1	-	3	30	70	100
5	14BT30502	Digital Logic Design	3	1	-	3	30	70	100
6	14BT31201	Discrete Mathematical Structures	3	1	-	3	30	70	100
7	14BT30422	Analog and Digital Electronics Lab	-	-	3	2	25	50	75
8	14BT30521	Data Structures Lab	-	-	3	2	25	50	75
		Total	**18**	**6**	**6**	**22**	**230**	**520**	**750**

II Year – II Semester

S. No	Code	Subject	Periods per week			C	Scheme of Examinations Max. Marks		
			L	T	P		Int.	Ext.	Total
1	14BT3HS01	Environmental Sciences	3	1	-	3	30	70	100
2	14BT4HS01	Business Communication and Presentation Skills	3	1	-	3	30	70	100
3	14BT41501	Computer Graphics	3	1	-	3	30	70	100
4	14BT40501	Computer Organization	3	1	-	3	30	70	100
5	14BT40502	Database Management Systems	3	1	-	3	30	70	100
6	14BT50501	Theory of Computation	3	1	-	3	30	70	100
7	14BT41201	Object Oriented Programming	3	1	-	3	30	70	100
8	14BT40521	Database Management Systems Lab	-	-	3	2	25	50	75
9	14BT41221	Object Oriented Programming Lab	-	-	3	2	25	50	75
		Total	**21**	**7**	**6**	**25**	**260**	**590**	**850**

III Year – I Semester

S. No	Code	Subject	Periods per week			C	Scheme of Examinations Max. Marks		
			L	T	P		Int.	Ext.	Total
1	14BT4HS02	Professional Ethics	3	1	-	3	30	70	100
2	14BT5HS02	Management Science	3	1	-	3	30	70	100
3	14BT51501	Operating Systems	3	1	-	3	30	70	100
4	14BT51502	Systems Software	3	1	-	3	30	70	100
5	14BT50431	Micro Processors and Interfacing	3	1	-	3	30	70	100
6	14BT40503	Design and Analysis of Algorithms	3	1	-	3	30	70	100
7	14BT51202	Software Engineering	3	1	-	3	30	70	100
8	14BT51521	Operating Systems and Systems Software Lab	-	-	3	2	25	50	75
9	14BT50424	Micro Processor and Interfacing Lab	-	-	3	2	25	50	75
		Total	**21**	**7**	**6**	**25**	**260**	**590**	**850**

III Year – II Semester

S. No	Code	Subject	Periods per week			C	Scheme of Examinations Max. Marks		
			L	T	P		Int.	Ext.	Total
1	14BT5HS01	Managerial Economics and Principles of Accountancy	3	1	-	3	30	70	100
2	14BT60501	Object Oriented Analysis and Design	3	1	-	3	30	70	100
3	14BT70501	Compiler Design	3	1	-	3	30	70	100
4	14BT51201	Computer Networks	3	1	-	3	30	70	100
5	14BT61202	Web Programming	3	1	-	3	30	70	100
6	---	Open Elective	3	1	-	3	30	70	100
7	14BT61521	Computer Networks and Compiler Design Lab	-	-	3	2	25	50	75
8	14BT61522	OOAD and web Programming Lab	-	-	3	2	25	50	75
		Total	**18**	**6**	**6**	**22**	**230**	**520**	**750**

List of Open Electives

Offering Dept	Course Code	Course Title
BS&H	14BT6HS01	Banking and Insurance
BS&H	14BT6HS02	Cost Accounting and Financial Management
BS&H	14BT6HS03	Entrepreneurship for Micro, Small and Medium Enterprises
CE	14BT70105	Disaster Mitigation and Management
CE	14BT70106	Environmental Pollution and Control
CE	14BT70107	Contract Laws and Regulations
CE	14BT70108	Planning for Sustainable Development
CE	14BT70109	Rural Technology
ME	14BT60305	Artificial Intelligence and Robotics
ME	14BT60306	Global Strategy and Technology
ME	14BT60307	Intellectual Property Rights and Management
ME	14BT60308	Managing Innovation and Entrepreneurship
ME	14BT60309	Material Science
CSE	14BT60502	Engineering Systems Analysis and Design
EIE	14BT71005	Microelectromechanical Systems
IT	14BT61203	Bio-Informatics
IT	14BT61204	Cyber Security and Laws

IV Year – I Semester

S. No	Code	Subject	Periods per week			C	Scheme of Examinations Max. Marks		
			L	T	P		Int.	Ext.	Total
1	14BT71501	Embedded Systems Programming	3	1	-	3	30	70	100
2	14BT71502	Network Programming	3	1	-	3	30	70	100
3	14BT70331	Optimization Techniques	3	1	-	3	30	70	100
4	14BT61201	Data Warehousing and Data Mining	3	1	-	3	30	70	100
5		**Professional Elective - I**	3	1	-	3	30	70	100
	14BT71503	Kernel Programming							
	14BT71504	Simulation and Modeling							
	14BT70503	Advanced Computer Architecture							
	14BT81202	Cryptography and Network Security							
	14BT71204	Software Testing Techniques							
6		**Professional Elective - II**	3	1	-	3	30	70	100
	14BT71505	Real-Time Systems							
	14BT71506	Soft Computing							
	14BT71507	Software Project Management							
	14BT70502	Mobile Computing							
	14BT81203	.Net Technologies							
7	14BT71521	Data Warehousing and Data Mining Lab	-	-	3	2	25	50	75
8	14BT71522	Network Programming Lab	-	-	3	2	25	50	75
9	14BT71523	Seminar	-	-	-	2	-	50	50
		Total	18	6	6	24	230	570	800

IV Year – II Semester

S. No	Code	Subject	Periods per week			C	Scheme of Examinations Max. Marks		
			L	T	P		Int.	Ext.	Total
1	14BT81501	High Performance Computing	3	1	-	3	30	70	100
2	14BT81201	Cloud Computing	3	1	-	3	30	70	100
3		**Professional Elective - III**							
	14BT81502	Embedded Processors	3	1	-	3	30	70	100
	14BT81503	Human Computer Interaction							
	14BT81504	Performance Evaluation of Computer Systems							
	14BT71201	Mobile Application Development							
	14BT71205	Machine Learning							
4		**Professional Elective - IV**							
	14BT81505	Software Architecture	3	1	-	3	30	70	100
	14BT80502	Big Data							
	14BT80533	Ad hoc Wireless Networks							
	14BT71206	Service Oriented Architecture							
	14BT81204	Information Retrieval Systems							
5	14BT81521	Comprehensive Viva-Voce	-	-	-	2	-	100	100
6	14BT81522	Project Work	-	-	20	10	60	140	200
		Total	**12**	**4**	**20**	**24**	**180**	**520**	**700**
						187	**1695**	**3980**	**5675**

BOS, Chairman

Approved Course Structure

Program: B.Tech.- Computer Science and Systems Engineering
Regulations: SVEC-16

SREE VIDYANIKETHAN ENGINEERING COLLEGE
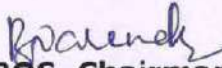## (AUTONOMOUS)
COURSE STRUCTURE (2016-2017)
COMPUTER SCIENCE AND SYSTEMS ENGINEERING
I B.Tech(I Semester)

S. No	Course Code	Course Title	Contact Periods/ Week				C	Scheme of Examinations		
			L	T	P	Total		Int.	Ext.	Total
1	16BT1HS01	Technical English	3	1	-	4	3	30	70	100
2	16BT1BS01	Engineering Chemistry	3	1	-	4	3	30	70	100
3	16BT1BS03	Matrices and Numerical Methods	3	1	-	4	3	30	70	100
4	16BT1BS04	Multi- Variable Calculus and Differential Equations	3	1	-	4	3	30	70	100
5	16BT10501	Programming in C	3	1	-	4	3	30	70	100
6	16BT1HS31	English Language Lab	-	-	3	3	2	50	50	100
7	16BT1BS31	Engineering Chemistry Lab	-	-	3	3	2	50	50	100
8	16BT10331	Computer Aided Engineering Drawing	-	1	6	7	3	50	50	100
9	16BT10531	Programming in C Lab	-	-	3	3	2	50	50	100
		Total	**15**	**6**	**15**	**36**	**24**	**350**	**550**	**900**

I B.Tech (II Semester)

S. No	Course Code	Course Title	Contact Periods/ Week				C	Scheme of Examinations		
			L	T	P	Total		Int.	Ext.	Total
1	16BT 1BS02	Engineering Physics	3	1	-	4	3	30	70	100
2	16BT 2BS01	Transformation Techniques and Partial Differential Equations	3	1	-	4	3	30	70	100
3	16BT20441	Basic Electronic Devices and Circuits	3	1	-	4	3	30	70	100
4	16BT21201	Object Oriented Programming through C++	4	1	-	5	4	30	70	100
5	16BT21501	Digital Logic Design	3	1	-	4	3	30	70	100
6	16BT 1BS32	Engineering Physics Lab	-	-	3	3	2	50	50	100
7	16BT20451	Analog and Digital Electronic s Lab	-	-	3	3	2	50	50	100
8	16BT21231	IT Workshop	-	-	3	3	2	50	50	100
9	16BT21232	Object Oriented Programming Lab	-	-	3	3	2	50	50	100
		Total	**16**	**5**	**12**	**33**	**24**	**350**	**550**	**900**

BOS, Chairman

LIST OF NEW COURSES INTRODUCED

Program: B.Tech.- Computer Science and Systems Engineering

Regulations : SVEC-14

Course Code	Name of the course
14BT6HS01	Banking and Insurance
14BT6HS02	Cost Accounting and Financial Management
14BT6HS03	Entrepreneurship for Micro, Small and Medium Enterprises
14BT70106	Environmental Pollution and Control
14BT70107	Contract Laws and Regulations
14BT70108	Planning for Sustainable Development
14BT70109	Rural Technology
14BT60305	Artificial Intelligence and Robotics
14BT60306	Global Strategy and Technology
14BT60307	Intellectual Property Rights and Management
14BT60308	Managing Innovation and Entrepreneurship
14BT60309	Material Science
14BT71501	Embedded System Programming
14BT71503	Kernel Programming
14BT71504	Simulation and Modeling
14BT71505	Real Time Systems
14BT70503	Advanced Computer Architecture
14BT70502	Mobile Computing
14BT81203	.Net Technologies
14BT71201	Mobile Application Development
14BT80502	Big Data

BOS,Chairman