

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

SREE SAINATH NAGAR, A. RANGAMPET-517 102

Agenda for 10th Meeting of Board of Studies for B. Tech. (Computer Science and Engineering) on 10.06.2019 at 2:15 PM

- Action taken report on minutes/ resolutions of 9th BOS meeting.
- Report on implementation of autonomy with respect to syllabi & academic regulations for III B. Tech (under SVEC16).
- 3. Academic Calendar for II, III and IV Year B. Tech for 2019-20.
- Vision and Mission of the Department, PEOs, POs & PSOs and Course Structure (All 4 years) of B. Tech. Program under SVEC-19 Curriculum.
- 5. Administrative set-up for B. Tech Program.
- 6. Syllabi with Course Objectives & Course Outcomes, Lesson Plans and Model Question Papers for Courses of I B. Tech (Under SVEC-19).
- Panel of Examiners for Question paper setting for I B.Tech courses (Under SVEC-19).
- Implementation of Teaching, Learning and Evaluation Process.
- 9. Substitute Courses
- 10. Research Activities.
- Faculty Development Activities.
- 12. Staff Development Activities.
- 13. Student Development Activities.
- 14. Student Support Systems.
- Extension Activities.
- 16. Any other item.

Prof C.M.Rao

Chairman, BOS (CSE)



SREE SAINATH NAGAR, A. RANGAMPET - 517102

Department of Computer Science and Engineering

10th meeting of BOS (CSE) for U.G. program-B.Tech (CSE) held on 10.06.2019

S.	Name	Designation	Signature		
No 1.	Prof. C. Madhusudhana Rao Professor of CSE	Chairman	_08960		
2.	Professor of CSE & Professor of CSE & Director (Industrial Relations) JNTUA College of Engineering JNTU Ananthapurmu.	External Member Nominated by Vice- Chancellor, JNTUA	riddy		
3.	Dr. D.V.V. Prasad Professor of CSE, SSN College of Engineering, Chennai	External Member Nominated by Academic Council	Bud ,		
4.	Dr. V.V. Rama Prasad Professor & Dean (Academics) CSE Dept., SVEC	Internal Member	mamabe		
5.	Dr. K. Delhi Babu Dean (Training and Placement) CSE Dept., SVEC	Internal Member	ريان		
6.	Dr. M. Sunil Kumar Professor & Head, CSE Dept., SVEC	Internal Member	Wil		
7.	Dr. A. Senguttuvan Professor	Internal Member	assignit		
8.	CSE Dept, SVEC Dr. A.V. Sriharsha Professor, CSE Dept, SVEC	Internal Member	Wharha		
9.	Dr. G. Sunitha Professor,	Internal Member	82		
10.	CSE Dept, SVEC Mr. Shaik Salam Associate Professor, CSE Dept, SVEC	Internal Member	Please		
11.	Dr. M. Sakthivel Associate Professor, CSE Dept, SVEC	Internal Member	was		
12.	Dr. J. Avanija Associate Professor, CSE Dept, SVEC	Internal Member	460		
13.	Dr. K. Reddy Madhavi Associate Professor, CSE Dept, SVEC	Internal Member	40		
14.	Dr. Vi Anantha Natarajan Associate Professor, CSE Dept, SVEC	Internal Member	de		
15.	Dr. R. Kamalraj Associate Professor CSE Dept, SVEC	Internal Member	this		
16.	Dr. K.S. Kannan Associate Professor CSE Dept, SVEC	Internal Member	& fine		
17.	Dr. B. Uma Maheswara Rao Associate Professor CSE Dept, SVEC	Internal Member	Bun 4		
18.	Mr. K. Siva Krishna Rao Assistant Professor (SL) CSE Dept, SVEC	Internal Member	Chen		

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SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, A. Rangampet - 517 102

Department of Computer Science and Engineering

Minutes of the 10th Meeting of Board of Studies (CSE) for the UG Program B.Tech. (CSE) conducted on 10.06.2019 at 2.15 PM

- Action Taken Report on minutes/resolutions of 9th BoS Meeting Members of the BoS were apprised on the action taken report on minutes of the 9th BoS meeting held on 25.05.2018. After deliberations the members ratified the report.
- Report on implementation of autonomy with respect to syllabi and regulations 2. The Chairman BoS apprised the members about the implementation of Autonomy with respect to syllabi and regulations of III B.Tech.(CSE) under SVEC16 regulations. The feedback of the students, faculty and external experts on the courses of B.Tech.(CSE) under SVEC16 regulations is apprised. The board perused the report.
- 3. B.Tech. Academic Calendar for 2019-20

The Chairman BoS presented the academic calendar for II, III, IV B.Tech. (I & II Semesters) for the academic year 2019-20. The same is recommended to Academic Council without any modification.

Members of BoS have been apprised that the Chairman, Academic Council is empowered to finalize the academic calendar pertaining to I B. Tech. (I & II Semesters) for the academic year 2019-20.

- Vision and Mission of the Department, PEOs, POs and PSOs of B.Tech.(CSE) The Chairman, BoS apprised the members about the Vision & Mission of the Department, Program Educational Objectives, Program Outcomes and Program Specific Outcomes of B.Tech.(CSE) under SVEC19 Regulations. The members approved the Vision & Mission of the Department and Program Educational Objectives, Program Outcomes and Program Specific Outcomes of B.Tech.(CSE) under SVEC19 Regulations. The approved Vision & Mission of the Department, Program Educational Objectives of B.Tech.(CSE) program under SVEC19 regulations is sent to the Academic Council for perusal and ratification.
- SVEC19 Regulations B.Tech. (CSE) Academic Regulations 5. It is resolved to adopt the SVEC19 B.Tech. Academic Regulations as approved in the combined Board of Studies for the B.Tech.(CSE) program for the students admitted from the academic year 2019-20. The board approves to implement SVEC19 Academic Regulations and I B. Tech. Course Structure for the batches being admitted from the academic year 2019-2020 by implementing the "Induction Programme" of 3 weeks duration as suggested by AICTE.
- SVEC19 Regulations B.Tech. (CSE) Course Structure and Syllabi

The committee discussed the Model Curriculum 2018, Course Structure and Syllabi suggested by AICTE and the Course structure proposed by APSCHE. The Course Structure prepared, under SVEC-19 Regulations, in-line with AICTE Model Curriculum 2018 and the APSCHE is presented by Chairman, BoS and discussed. The administrative setup for SVEC19 B.Tech. (CSE) curriculum development is apprised and approved. The syllabus of the following courses of I and II semesters of B.Tech.(CSE) is presented and discussed.

- Programming for Problem Solving 1)
- Programming for Problem Solving Lab ii)
- iii) Digital Logic Design

CHAIRMAN, B.O.S.

COMPUTER SCIENCE & ENGINEERING DYANIKETHAN ENGINEERING COLLEGE · Sainath Nagar, A. Rangampet

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The following are the observations and suggestions made by the board on the syllabi and course structure proposed by the Department:

Suggested to remove Scratch and Raptor tools from the syllabus of course "Programming for Problem Solving" and the syllabus to be focused on problem solving and Python programming.

Suggested to take up "R. Nageswara Rao, Core Python Programming, 2nd edition, Dreamtech Press, 2018" as text book for the course "Programming for Problem

Solving".

Suggested to include a course on "C Programming". Discussions are made on possibility to include an exclusive course for the same. It has been decided to include "C Programming" as part of "CSE Workshop" course.

The contents and the complexity of the topics included in the AI Tools Lab course designed by APSCHE was discussed and suggested to redesign to manage the trade-off

between the industry needs and the complexity level.

Suggested to add OpenMP and MPI concepts to "Multi-Core Architectures", "GPU Computing" courses to impart parallel programming skills to students.

Suggested to include the course "GPU Computing" in the B.Tech. (CSE) curriculum.

The scope and the syllabi to be included in the "Design Thinking" course is discussed and BoS members suggested to refer to syllabi offered by online course providers including NPTEL, Stanford University and design the syllabi accordingly.

The board appreciated the idea of including socially relevant projects in the course

structure of B.Tech.(CSE).

The course structure of Minor Degree in B.Tech. (CSE) is presented to the board and is discussed. Board members appreciated that offering Minor Degree will help Non-Computer Science discipline students to broaden their career opportunities (Higher studies and the job market) and approved the same.

The course structure of Honors Degree in B.Tech. (CSE) is presented to the board and is discussed. Board members appreciated that offering Honors Degree will help advanced learners of B.Tech. CSE to improve their skills and approved the same.

The course structure and syllabi of I B.Tech. (CSE) (I & II Semesters) under SVEC19 Regulations is approved. However, the BoS decided that the course structures for II, III and IV B.Tech.(CSE) under SVEC19 Regulations is tentative and suggested for presenting the same with detailed syllabi in the ensuing B.O.S meeting to be held on April/May 2020. The board authorizes the Chairman, BoS to prepare and submit the model question papers and panel of question paper setters for theory courses as and when demanded by the Chairman, Academic Council or Chief Controller of Exams.

Teaching, Learning and Evaluation 7.

The Chairman BoS presented the teaching, learning and evaluation practices followed in the department. The members of BoS appreciated the good practices being followed by the department in terms of conducting diagnostic test, formative tests and the Mid exams. The Assessment manual for B.Tech.(CSE) under SVE19 Regulations is discussed and ratified by the BoS and is sent to the Academic Council for approval.

Research Activities В.

The Chairman BoS has briefed the members about the ongoing research activities in the department made by the faculty (Thrust Areas of Research, Research Mentoring Committee, Research Proposals submitted by the Faculty for Research Funding, Paper Publications, Filing Patents, In-house Seed Funding by SVET). The members appreciated the efforts made by the faculty and the department.

> CHAIRMAN, B.O.S. COMPUTER SCIENCE & ENGINEERING SREE WOYANIKETHAN ENGINEERING COLLEGE - Sainath Nagar, A. Rangan or t 'RIPAT! F.17

9. Faculty Development Activities

The Chairman apprised the board about the technical talks by the faculty in the department, faculty incentives, FDPs attended, FDPs organized, and student feedback on faculty. The members have appreciated the efforts of the department and suggested to continue the same. The Chairman BoS assured the members that the efforts in this direction will be intensified.

10. Staff Development Activities

The board is apprised with the information about staff development programs organized by the department and attended by the staff. The members have appreciated the efforts of the department and suggested to continue the same.

11. Students Development Activities

The members perused and noted the information provided on the student development activities organized by the department (Guest and Expert Lectures organized, Add-on/Workshop/Training courses conducted). The members appreciated the student centric activities of the department.

12. Student Support Activities

The members noted the information on good practices such as Counseling Mechanism, Student Association Activities, On Campus Training for Higher Studies, Placement Training, Training Activities by Companies, Campus Placements, Entrepreneurship Activities and IIIC activities for all round growth and development of students.

13. Extension Activities

The members have perused, noted and appreciated the efforts of department/college in organizing professional, technical and community development activities. The BoS members have suggested continuing the same. They suggested to encourage students to participate in more number of community activities so that they get integrated with society.

14. Any other item:

Members suggested to

- Encourage students to do International Internships. (Ex: Internships at Mytacs
 Canada can be applied online. It offers global research internship platform to get
 internship offers from Canadian Universities)
- Foreign Language Package can be offered to all students (French, German and Spanish)
- Assessment tools like quiz, assignment, seminar may be included in each course for direct assessment (for 5 to 10 marks) along with mid and semester end examinations.
- More students to be encouraged for pursuing internships at IITs/IIITs/NITs.
 Department shall plan for offering on campus summer internships catering to the needs of local students.
- To focus more on entrepreneurship related programmes to encourage start-ups.
- Conduct skill-oriented courses as add-on courses for students.
- Encourage students to pursue certification programs in their area of interest for focused career and research.

The BoS members appreciated the pro-active approach of college management and efforts taken by department for the growth of the department.

BoS chairman is empowered to change the context of the syllabus as on when required with the information to the BoS members.

CHAIRMAN, B.O.S.
COMPUTER SCIENCE & ENGINEERING
SREE VIDYANIKETHAN ENGINEERING COLLEGE
Sree Sainath Nagar, A. Rangampet
Near TIRUPATI - 517 102, A.P.

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APPROVED COURSE STRUCTURE SVECIAL COMPUTER SCIENCE AND ENGINEERING I B.Tech. – I Semester

SI. No.	Course Code	Course Title	Contact Periods per week				c	Scheme of Examination Max. Marks		
			L	Т	P	Total		Int. Marks		Total Marks
1.	19BT1BS01	Differential Equations and Multivariable Calculus	3	1		4	4	40	60	100
2.	19BT1BS04	Engineering Chemistry	3		-	3	3	40	60	100
3.	19BT1HS01	Communicative English	3			3	3	40	60	100
4.	19BT10501	Programming for Problem Solving	3	1		4	4	40	60	100
5.	19BT1BS32	Engineering Chemistry Lab	-	-	2	2	1	50	50	100
6.	19BT1HS31	Communicative English Lab	-	-	2	2	1	50	50	100
7.	19BT10331	Computer Aided Engineering Drawing	-	1	2	3	2	50	50	100
8.	19BT10531	Programming for Problem Solving Lab	-		2	2	1	50	50	100
		Total:	12	3	8	23	19	360	440	800

I B. Tech. - II Semester

SI. No.	Course Code	Course Title	Contact Periods per week				C	Scheme of Examination Max. Marks		
			L	т	P	Total		Int. Marks	Ext. Marks	Total Marks
1.	19BT2BS01	Transformation Techniques and Linear Algebra	3	1	-	4	4	40	60	100
2.	19BT1BS02	Biology for Engineers	2	-	-	2	2	40	60	100
3.	19BT1BS03	Engineering Physics	3	-	-	3	3	40	60	100
4.	19BT10201	Basic Electrical and Electronics Engineering	3		-	3	3	40	60	100
5.	19BT20501	Digital Logic Design	3	-	-	3	3	40	60	100
6.	19BT21501	Object Oriented Programming through Java	3	-		3	3	40	60	100
7.	19BT1BS31	Engineering Physics Lab	-	-	2	2	1	50	50	100
8.	19BT10231	Basic Electrical and Electronics Engineering Lab		-	2	2	1	50	50	100
9.	19BT20331	Engineering Workshop	-		2	2	1	50	50	100
10.	19BT21531	Object Oriented Programming through Java Lab		1	2	3	2	50	50	100
	2 Total		17	2	8	27	23	440	560	1000
11.	19BT1AC01	Spoken English AN. B.	1.5%	-	-	2		-	-	

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