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SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)



SREE SAINATH NAGAR, A. RANGAMPET - 517102

Department of Computer Science & Engineering

Agenda for the 6th Meeting of Board of Studies for U.G Program-B. Tech. (C.S.E) on 18.4,2015 at 10:00 AM

- 1. Action taken report on minutes/resolutions of 5th BOS meeting.
- Report on implementation of autonomy with respect to syllabi and regulations for I-B.Tech (SVEC-14)
- 3. Academic Calendar for II, III and IV B.Tech. Programs.
- Vision and Mission of the Department, PEOs & POs and Course Structures of B. Tech. Program in CSE.
- 5. Administrative Setup for B.Tech. Program in CSE.
- Syllabi of II, III and IV B.Tech. (CSE) Courses with lesson plans, model question papers and panels.
- 7. Teaching, Learning and Evaluation.
- 8. Research Activities.
- 9. Faculty Development.
- 10. Student Development.
- 11. Student Support.
- 12. Extension Activities.
- 13. Any other item.

(Dr. V. Rama Prasad)

CHAIRMAN, B.D.S. (CSE)

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SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS) SREE SAINATH NAGAR, A. RANGAMPET - 517102



Department of Computer Science & Engineering

Sub: 6th Meeting of B.O.S (CSE) for U.G Program - B. Tech. (C.S.E) held on 18.4.2015 at 10:00 AM.

Members Present:

S. No	Name	Designation	Signature
1.	Dr. V.V. Rama Prasad Professor of CSE, SVEC	Chairman, BOS	Manch
2.	Dr. V. Kamakshi Prasad Professor of CSE JNTU College of Engineering HYDERABAD - 500085	External Member (JNTUA VC-Nominee)	Negros
3.	Dr. K. Bhaskaran Associate Professor of CSE Anna University, Guindy CHENNAI-600025	External Member (Academic)	not present
4.	Dr. R. B. V. Subramanyam Associate Professor of CSE N.I.T, WARANGAL-506004	External Member (Academic)	RISMANY
5.	Dr. Amit Kesarkar, Scientist-D, N.A.R.L, GADANKI, Chittoor (DT)	External Member (Industry)	presaules
6.	Mr.Thota Madan Mohan Vice-President Goldman Sachs Pvt. Ltd, #49, 3 rd Cross, Amar Regency, Horamavu, BANGALORE – 560043.	Alumni Member	M. M.
7.	Dr. A. Senguttuvan Professor of CSE, SVEC	Internal Member	Clarist
8.	Prof.C.Madhusudhana Rao Professor & Head, CSE Dept., SVEC	Internal member	(ಲೀಖ
9.	Mr. Shaik Salam Associate Professor, CSE Dept, SVEC	Internal Member	Plem
10.	Mr. A.V. Sriharsha Associate Professor, CSE Dept, SVEC	Internal Member	Marko
11.	Mr. K. Munivara Prasad Assistant Professor (SL) CSE Dept, SVEC	Internal Member	Jenn presul
12.	Mr. M. Sunil Kumar	Internal Member	HILL

CHARMAN, B.O.S. COMPUTER SCIENCE & ENGINEERING SREE VIDYANIKETHAN ENGINEERING COLLEGE Sree Sainath Nagar, A. Rangampet Near TIRUPATI - 517 102, A.P.



SREE SAINATH NAGAR, TIRUPATI - 517 102

Department of Computer Science and Engineering

Minutes of the 6th Meeting of B.O.S. (C.S.E) for the UG Program B.Tech (CSE) conducted on 18.04.2015 @ 10.00 AM

- Members of the B.O.S were apprised of the Action Taken Report on minutes of the 5th B.O.S meeting held on 25.04.2014.
- A copy of the report on implementation of autonomy for I B.Tech (CSE) with respect to SVEC-14 regulations is circulated among the members of B.O.S.
- A copy of the Academic Calendar pertaining to II, III and IV B.Tech (I-Semester and II-Semester) for the academic year 2015-16 is circulated among the members of B.O.S.

Members noted and approved the same.

 Members of the B.O.S were apprised of the Vision and Mission of the department, Program Educational Objectives (PEOs), Program Outcomes (POs), and proposed course structure for the II, III and IV B.Tech (CSE) under SVEC-14 regulations.

Members noted and approved the same.

Members of the B.O.S were apprised of the Administrative set-up for the B.Tech (CSE) program for preparation of syllabi for various courses under SVEC-14 curriculum.

Members noted and approved the same.

6. A copy of the detailed syllabus, lesson plans, model question papers and panel of examiners prepared by the CSE and other departments for the all the courses (Core, Professional Elective and Open-Elective) of II, III and IV B.Tech (CSE) is circulated to the members.

The detailed syllabus, lesson plans, model question papers and panel of examiners prepared by CSE department for ECE, CSSE and IT departments is also included.

Members perused and approved the detailed syllabus, lesson plans, and model question papers for all the courses of II, III, and IV B.Tech (CSE) and for the other department courses prepared by the CSE department.

The panel of examiners for all the courses of II B.Tech (CSE) is approved by the members.

 Members have been apprised of the Teaching, Learning and Evaluation practices at S.V.E.C, specifically in C.S.E. Department for the B.Tech (C.S.E) program.

Members noted and approved the same.

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 The ongoing Research Activities (Thrust areas of research, Research mentoring Committee, Technical talks, Research proposals and projects) have been noted by the members.

Members appreciated the same.

Members of B.O.S have been apprised of the Faculty Development Programs conducted and sponsored in the Department during 2014-15.

Members appreciated the same.

10. Members of B.O.S have been apprised of the student development activities conducted during 2014-15 in the Department.

Members noted and appreciated the same.

11. Members of B.O.S have been apprised of the student support systems (Counseling, Extra-curricular activities, Games and Sports, Grievances etc.) functioning at the college and department level.

Members noted and appreciated the same.

12. Members of B.O.S have been apprised of the extension activities (NSS/Outreach) organized at the college and department level during 2014-15.

Members noted and appreciated the same.

13. Any other item.

Following Suggestions/Comments were given by the external members of B.O.S:

- a) Suggested to remove the topic " 4-Color theorem" in Unit-V of the course "Discrete Mathematical Structures" in II B.Tech I-Semester
- b) Suggested to replace the topic "Cook's Theorem" as " Cook's Theorem (Without Proof)" in Unit-V of the course "Design and Analysis of Algorithms" in II B.Tech II-Semester
- c) Suggested to rename the Course "O.O.P" in II B.Tech II-Semester as "O.O.P through Java"
- d) Suggested to rewrite the Units I, II and III of the course "DBMS" in II
 B.Tech II-Semester by distributing the contents evenly.
- e) Suggested to remove the Unit-V "Undecidability" in the course "Theory of Computation" in III B.Tech I-Semester and rewrite the contents of Unit I, II, III and IV for 5 Units of syllabus.
 - f) Suggested to prescribe the title "Machine Learning" by Tom M Mitchell as text book and rewrite the syllabus for the course "Machine Learning" (PE-I) in III B.Tech I-Semester.

- g) Suggested to remove the Unit-V "Data Mining Trends and Research Frontiers" in the course "DWDM" in IV B.Tech I-Semester and rewrite the contents of Unit I, II, III and IV for 5 Units of syllabus.
- h) Suggested to rewrite the syllabus for the course "Cloud Computing" under IV B.Tech II-Semester by including the topics on Virtualization.
- i) Suggested to replace the title for the course "Big Data Analytics" as "Big Data" in IV B.Tech II-Semester
 - j) Suggested to conduct the Comprehensive Viva-Voce exam with 50 objective questions (50 marks) Viva-Voce for 25 Marks for a total of 75 marks instead of 100 marks as it carries only 02 credits.

(V.V.RAMA PRASAD)

CHAIRMAN, B.O.S,
COMPUTER SCIENCE & ENGINEERING
SREE VIDYANIKETHAN ENGINEERING COLLE
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OF THEOPATI 517 102, A.P.

Approved Course Structure

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

COURSE STRUCTURE (2014-2015) COMPUTER SCIENCE AND ENGINEERING I Year B.Tech. (Yearly Pattern)

Code	Subject		riods week	-	С	Scheme of Examination Max. Marks		
		L	T	P		Int.	Ext.	Total
14BT1HS01	Technical English	2		(i e)	4	30	70	100
14BT1BS01	Engineering Physics	2	1	(4)	4	30	70	100
14BT1B \$02	Engineering Chemistry	2	1	(/ -)	4	30	70	100
14BT1BS03	Engineering Mathematics		1	170	6	30.	70	100
14BT1BS04	Mathematical Methods		1	100	6	30	70	100
14BT1ES02	Problem Solving and Computer Programming	3	1	(24)	6	30	70	100
14BT1ES03	Computer-Aided Engineering Drawing	12.0	1	3	3	25	50	75
14BT1BS05	Engineering Physics and Engineering Chemistry Lab.	-	141	3	3	25	50	75
14BT1ES05	Problem Solving and Computer Programming Lab.	6 - W	(7)	3	3	25	50	75
14BT1ES06	Engineering and IT Workshop	35	656	3	3	25	50	75
14BT1HS02	English Language Communication Skills Lab.	2		3	3	25	50	75
	TOTAL	15	6	15	45	305	670	975

II Year B.Tech. I Semester

Code	Subject	Periods per week			С	Scheme of Examination Max. Marks				
		L	T	Р		Int.	Ext.	Total		
14BT3BS03	Probability and Statistics	3	1	-	3	30	70	100		
14BT30501	Data Structures	3	1	-	3	30	70	100		
14BT30502	Digital Logic Design	3	1	137	3	30	70	100		
14BT31201	Discrete Mathematical Structures	3	1	-	3	30	70	100		
14BT30235	Basic Electrical Engineering	3	1	12	3	30	70	100		
14BT30431	Electronic Devices and Circuits	3	1		3	30	70	100		
14BT30521	Data Structures Lab	=	-	3	2	25	50	75		
14BT30422	Analog and Digital Electronics Lab	*	-	3	2	25	50	75		
	TOTAL	18	6	6	22	230	520	750		

II Year B.Tech. II Semester

Code	Subject	Periods per week			С	Scheme of Examination Max. Marks				
		L	T	Р	2 8 8	Int	Ext	Total		
14BT4HS01	Business Communication and Presentation Skills	3	1	-	3	30	70	100		
14BT3HS01	Environmental Sciences	3	1	-	3	30	70	100		
14BT40501	Computer Organization	3	1	=	3	30	70	100		
14BT40502	Database Management Systems	3	1	-	3	30	70	100		
14BT40503	Design and Analysis of Algorithms	3	1	*	3	30	70	100		
14BT41201	Object Oriented Programming	3	1	-	3	30	70	100		
14BT41501	Computer Graphics	3	1.	Δ.	3	30	70	100		
14BT40521	Database Management systems Lab	<u>L</u>	(2)	3	2	25	50	75		
14BT41221	Object Oriented Programming Lab	5	(<u>=</u>)	3	2	25	50	75		
	Total	21	7	6	25	260	590	850		

III Year B.Tech. I Semester

Code	Subject	Periods per week			с	Scheme of Examination Max. Marks				
	200000000000000000000000000000000000000	L	T	P		Int.	Ext.	Total		
14BT5HS02	Management Science	3	1		3	30	70	100		
14BT4H502	Professional Ethics	3	1	14	3	30	70	100		
14BT50501	Theory of Computation	3	1	17	3	30	70	100		
14BT50502	UNIX Internals	3	1		3	30	70	100		
14BT51202	Software Engineering	3	1	14	3	30	70	100		
148T51501	Operating Systems	3	1	. •	3	30	70	100		
14BT50431	Microprocessors and Interfacing	3	1	14	3	30	70	100		
14BT50521	Operating Systems and Unix Lab	×		3	2	25	50	75		
14BT50424	Microprocessors and Interfacing Lab	-		3	2	25	50	7.5		
	Total	21	7	6	25	260	590	850		

III Year B.Tech. II Semester

Code	Periods per Subject week			c	Scheme of Examination Max. Marks				
Code		L	T	P		Int.	Ext.	Total	
14BTSH501	Managerial Economics and Principles of Accountancy	3	1	-	3	30	70	100	
148760501	Object Oriented Analysis and Design	3	1	-	3	30	70	100	
14BT51201	Computer Networks	3	1	-	3	30	70	100	
148T61202	Web Programming	3	1	*	.3	30	70	100	
14BT71507	Software Project Management	3	1	-	3	30	70	100	
	OPEN- ELECTIVE	3	1	-	3	30	70	100	
14BT60521	Object Oriented Analysis and Design Lab	×	*	3	2	25	50	75	
14BT61222	Web Programming Lab	=	-	3	2	25	50	75	
	Total:	18	6	6	22	230	520	750	

IV Year B.Tech. I Semester

Code	Subject	Periods per week			c	Scheme of Examination Max. Marks					
		L	Т	P		Int.	Ext.	Tota			
14BT70501	Compiler Design	3	1	÷	3	30	70	100			
14BT70502	Mobile Computing	3	1	+	3	30	70	100			
14BT61201	Data Warehousing and Data Mining	3	1	2	3	30	70	100			
14BT71204	Software Testing Techniques	3	1	-	3	30	70	100			
	Professional Elective-I										
14BT70503	Advanced Computer Architecture										
14BT71205	Machine Learning	3	1	-	3	30	70	100			
14BT71501	Embedded System Programming										
14BT71504	Simulation and Modeling										
	Professional Elective-II										
14BT71202	Multimedia and Application Development										
14BT71206	Service Oriented Architecture	3	1	1 -	3	30	70	100			
14BT81503	Human computer Interaction										
14BT81505	Software Architecture										
14BT70521	Software Testing Techniques Lab	-	-	3	2	25	50	75			
14BT71521	Data Warehousing and Data Mining Lab	-	-	3	2	25	50	75			
14BT70522	Seminar		- 2	L2	2	-	50	50			
	TOTAL	18	6	6	24	230	570	800			

IV Year B.Tech. II Semester

Code	Subject	Periods per week		c	Scheme of Examination Max. Marks			
		L	T	P		Int.	Ext.	Tota
14BT80501	Design Patterns	3	1	¥.	3	30	70	100
14BT81201	Cloud Computing	3	1	*	3	30	70	100
	Professional Elective-III							
148780502	Big Data							
14BT80503	Computer Forensics	1925	1		529	1225	70	100
14BT80504	Distributed Systems	3	1	*	3	30		
14BT81202	Cryptography and Network Security	-						
	Professional Elective-IV							
14BT80505	Network Management							
14BT71201	Mobile Application Development	3	1	¥	3	30	70	100
14BT81204	Information Retrieval Systems							
14BT81206	Semantic Web							
14BT80521	Comprehensive Viva-Voce	(*)			2	-	100	100
14BT80522	Project Work	*		20	10	60	140	200
	TOTAL	12	4	20	24	180	520	700

Open Electives

Offering Dept	Course Code	Course Title
BS&H	14BT6HS01	Banking and Insurance
BS&H	14BT6HS02	Cost Accounting and Financial Management
BS&H	14BT6HS03	Entrepreneurship for Micro, Small and Medium Enterprises
CE	14BT70105	Disaster Mitigation and Management
CE	14BT70106	Environmental Pollution and Control
CE	14BT70107	Contract Laws and Regulations
CE	14BT70108	Planning for Sustainable Development
CE	14BT70109	Rural Technology
ME	14BT60305	Artificial Intelligence and Robotics
ME	14BT60306	Global Strategy and Technology
ME	14BT60307	Intellectual Property Rights and Management
ME	14BT60308	Managing Innovation and Entrepreneurship
ME	14BT60309	Material Science
CSE	14BT60502	Engineering Systems Analysis and Design
EIE	14BT71005	Microelectromechanical Systems
IT	1 <mark>4BT</mark> 61203	Bio-Informatics
IT	14BT61204	Cyber Security and Laws

CHAIRMAN BOS.
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LIST OF NEW COURSES INTRODUCED

Program: B.Tech.- Computer Science and Engineering

Regulations: SVEC-14

Course Code	Name of the course
14BT4HS01	Business Communication and Presentation Skills
14BT50521	Operating Systems and Unix Lab
14BT6HS01	Banking and Insurance
14BT6HS02	Cost Accounting and Financial Management
14BT6HS03	Entrepreneurship for Micro, Small and Medium Enterprises
14BT70105	Disaster Mitigation and Management
14BT70106	Environmental Pollution and Control
14BT70107	Contract Laws and Regulations
14BT70108	Planning for Sustainable Development
14BT70109	Rural Technology
14BT60305	Artificial Intelligence and Robotics
14BT60306	Global Strategy and Technology
14BT60307	Intellectual Property Rights and Management
14BT60308	Managing Innovation and Entrepreneurship
14BT60309	Material Science
14BT60502	Engineering Systems Analysis and Design
14BT61204	Cyber Security and Laws
14BT70502	Mobile Computing
14BT70503	Advanced Computer Architecture
14BT71205	Machine Learning
14BT71501	Embedded System Programming
14BT71504	Simulation and Modeling
14BT71202	Multimedia and Application Development
14BT71206	Service Oriented Architecture
14BT71521	Data Warehousing and Data Mining Lab
14BT80501	Design Patterns
14BT80502	Big Data
14BT80503	Computer Forensics
14BT80504	Distributed Systems
14BT80505	Network Management
14BT71201	Mobile Application Development
14BT81204	Information Retrieval Systems
14BT81206	Semantic Web

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