

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, Tirupati - 517102

DEPARTMENT OF COMPUTER SCIENCE AND SYSTEMS ENGINEERING

One Day Intellectual Property Rights Programme on

"Benefits of Patents to MSMES and Startups"

The Department of Computer Science and Systems Engineering organized a One Day Intellectual Property Rights Programme on, "Benefits of Patents to MSMES and Startups", on 24th January, 2017. The target group is members of Faculty and IV B.Tech students from Department of CSSE, SVEC.

Dr. M. Naresh Babu, Associate Professor, CSSE, SVEC discussed on Center of Excellence in IP, CDAC, Types of IPRs protection, Strategy of an Organization while dealing with IPR and MeitY scheme to support International Patent Filing.

The objective of this programme is to provide awareness on available IPR schemes among the faculty and students. Dr. Naresh, advocated PAMS (Patent Analysis and Management System Portal) is a single window interface (www.ict-ipr.in) to avail the services. This seminar facilitated the transfer of knowledge on IPR schemes.



Dr. M. Naresh Babu, Associate Professor, CSSE, SVEC delivering a Lectureon "Awareness Program on Benefits of Patents to MSMES and Startups"

The seminar provided insights on types and strategies in filing of Intellectual property. He also justified why MSMEs should encourage the protection and licensing of IP to organizations which can be effectively utilized for commercial exploitation. He concluded the seminar with few success stories.

30 students and 21 faculty participated and gained knowledge on Types of IPRs protection, Strategy of an Organization while dealing with IPR and MeitY scheme to support International Patent Filing.