



**SREE VIDYANIKETHAN ENGINEERING COLLEGE**

**(AUTONOMOUS)**

Sree Sainath Nagar, Tirupati - 517102

**DEPARTMENT OF COMPUTER SCIENCE AND SYSTEMS ENGINEERING**

**Faculty Development Programme on  
"High Performance Computing"**

The Department of Computer Science and Systems Engineering, organized Faculty Development Programme on **"High Performance Computing"**, 30<sup>th</sup> -31<sup>st</sup>, August 2013 under TEQIP – II.

This Faculty Development Programme covered both hardware and software aspects of High Performance parallel and distributed systems. It mainly focused on Fundamentals of HPC-Parallel Computing, Services of HPC, Parallel Programming with MPI, Introduction to Multicore Architectures, Parallel programming with Open MP, Parallel Algorithms for Multicore Architectures, Green HPC, HPC & Cloud.

The faculty from various colleges across the nation attended the FDP.



*Inaugural Function of Faculty Development Programme on "High Performance Computing"*



**Dr. V.C.V. Rao**, C-DAC, Pune delivering lecture on "Fundamentals of HPC Parallel Computing, Processor Architecture for HPC, Parallel Programming with MPI"



**Dr. Amit Parashuram Kesarkar**, NARL, Gadanki discussing "Introduction to Multicore Architectures"



**Dr. Chapram Sudhakar**, NIT, Warangal delivering lecture on "Green HPC, HPC & Cloud"



**Dr. M. Rajasekhar Babu**, VIT, Vellore delivering lecture on "Parallel Programming with OpenMP, Parallel Algorithms for Multicore Architectures"



*Group Photo*

40 Faculty members participated and gained knowledge on High Performance Computing.

Outcome and Importance of the Faculty Development Programme on "High Performance Computing"

- Exposure to knowledge in advanced subject area.
- It helps in designing B.Tech or M.Tech Curriculum.
- It helps for setting up new laboratory in the area of High Performance Computing.
- Interaction with subject experts and exchange of new ideas.