

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, Tirupati - 517102

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A 3-Day Workshop on "SCILAB"

By Ms. G Alekhya & D Gireesha, Mentors, APSSDC, Govt. of Andhra Pradesh 15-17 July 2019

The Department of Electronics and Communication Engineering of Sree Vidyanikethan Engineering College has organized a 3-Day Workshop for Students of III B.Tech ECE during 15-17 July 2019.

The objective of this workshop is to impart job-oriented training on Basic Mathematics Operation, Matrices, Functions, DSP, DIP & XCOS using SCILAB. By using this knowledge, they can develop solutions to simple real time problems that helps the end user for simulation to solve constrained and unconstrained continuous and discrete problems. More emphasis was given to hands-on (using Software) sessions for better understanding.

A total of 60 students from III B.Tech Electronics and Communication Engineering have participated in the Workshop. Dr. V. V. Satyanarayana Tallapragada, Associate Professor and Mr. S. Jaya Krishna, Assistant Professor organized this workshop under the guidance of Dr. P. V. Ramana, Professor and Head, Department of Electronics and Communication Engineering.

The resource persons are experienced trainers from APSSDC, Govt. of Andhra Pradesh. The feedback on participants understanding and learning, resource person's knowledge, teaching and organization of the workshop is extraordinary and encouraging to organize many workshops of this kind.

Overall, the event has proved successful. The photographs of workshop are as follows.



Lecture by APSSDC mentors



Students listening to the lecture



Mentors clarifying doubts to students



Monitoring the conduct of workshop by faculty



Faculty Instructing regarding APSSDC registration



Group Photo with HOD-ECE, Principal SVEC, participants, mentors and faculty organizers.