



**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
**(AUTONOMOUS)**

Sree Sainath Nagar, A. Rangampet – 517102

**Department of Electronics and Communication Engineering**

*Cordially invites you*

*to*

**“NATIONAL LEVEL WORKSHOP ON  
EXPLORING THE ISSUES AND RIGHTS OF  
INTELLECTUAL PROPERTY”**

**11<sup>th</sup> October, 2016**

***Resource Persons***

**Dr.N.Padmaja,**

Professor, Dept. of ECE,  
Sree Vidyanikethan Engineering College,  
Tirupathi

**Mr. Prasanta Kumar Pradhan,**

Associate Professor, Dept. of ECE,  
Sree Vidyanikethan Engineering College,  
Tirupathi

**Date** : 11<sup>th</sup> October 2016  
**Time** : 2.00 PM - 5.00 PM  
**Venue** : Signals and Systems Lab (Room No: 304)  
**Target Group** : Faculty, Research Scholars, Students