

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

SreeSainath Nagar, Tirupati – 517 102, A.P.

Department of Computer Science and Systems Engineering

Sanctioned Research Projects

Sl. No.	Name of the Faculty	Dept.	Title of the Project	Funding Agency	Year of Sanction	Duration	Amount (Rs.)	Status
2017-18								
1.	Dr. G. Sasikumar Dr. M. NareshBabu	CSSE	Intelligent System to classify human brain signals for finding brain diseases	DST-SERB	2017-18	3Years	23.738L	Sanctioned
2.	Dr.G.Sasi Kumar		Modernization of Data Analytics Lab	AICTE	2017-18	2Years	1.8L	Sanctioned
3.	Dr.D.R.Kumar Raja		Automation of Aerators Based System on Water Quality Parameters in Aquaculture	SVET-Internal	2017-18	2Years	1.5L	Sanctioned
4.	Mr.D.N.Kartheek		Class Time Indicator	SVET	2017-18	2 Months	10,000	Completed
2016-17								
5.	Ms. P. Dhanalakshmi	CSSE	Development of Mathematical Model for the Prediction of Customer Behavior in	UGC Minor	2016-17	2Years	1.95L	Completed

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			Online Databases					