

**Report on An Expert Lecture on “Approximate Computing in VLSI Design” under IEEE student chapter on 03<sup>rd</sup> August, 2019**

IEEE Student Chapter of SVEC has organized an Expert Lecture on “Approximate Computing in VLSI Design” on 03<sup>rd</sup> August, 2019. The objective of this expert lecture is to make the students and faculty familiarize by incorporate approximate hardware for high speed and power aware designing from which the energy overhead can be abridged. However obtaining the optimal trade-off between Energy and Quality in the Hardware for multimedia applications.



Dr.Noor Mohammad Addressing the Audience on Approximate Computing



An Expert Interacting with the Audience

Dr. Sk. Noor Mahammad, Asst. Professor, Department of Computer Engineering, IIITD&M, Chennai has discussed about the introduction to Computing and how to do apprimations in computing. He also emphasized on various research issues to approximate circuit designs where the functional elements are incorporated for image/video processing. The event was scheduled in Dasari Auditorium from 10:30AM to 01:00PM about 400 students of III, & IV B.Tech – ECE and Members of the Faculty have been benefited from the event.

Finally, concluded the session by open discussion with students and members of the faculty.

**IEEE Student Branch Counselor**