



**AICTE Sponsored Short Term Training Programme on
"Recent Trends and Research Challenges on
Nano CMOS VLSI Circuits by Industry Standard EDA Tools"**

AUGUST 26th–31st, 2019

Organized by
**Department of Electronics and Communication Engineering
(Centre for VLSI and Embedded systems)
Sree Vidyanikethan Engineering College (Autonomous)**

College Profile:

Sree Vidyanikethan Engineering College (Autonomous) was established in 1996 by Sree Vidyanikethan Educational Trust under the stewardship of Dr. M. Mohan Babu, renowned Film Artiste and Former Member of Parliament (Rajya Sabha). The College was established in the backward region of Rayalaseema to serve the cause of technical education with an initial intake of 180. The intake has increased exponentially to 2112 in 2019-20. The College now offers 8 B.Tech programs; 8 M.Tech programs; MCA Program; and 3 Doctoral Programs. AICTE has also accorded permission for 2nd Shift Polytechnic from the academic year 2009-10 and presently 5 Diploma courses are being offered. Today, Sree Vidyanikethan Engineering College is one of the largest, most admired and sought after institutions in Andhra Pradesh. The College is approved by AICTE and affiliated to JNTUA, Ananthapuramu. The College has been accorded Autonomous Status by the UGC, New Delhi in 2010-11 which was extended for six years (from 2016-17 to 2021-22).

The College is known for its quality initiatives which are amply reflected in accreditations by National Board of Accreditation (NBA) for UG & PG programs, National Assessment and Accreditation Council (NAAC) with 'A' Grade and many multinational organizations such as TCS, WIPRO & IBM. The College has successfully implemented TEQIP-II under Sub-component 1.1: Strengthening Institutions to improve Learning Outcomes and Employability of Graduates, funded by the Ministry of HRD, Govt. of India. The College has been accorded "UGC-Colleges with Potential for Excellence" status under CPE Scheme by UGC, New Delhi. It also has been accorded 'GOLD' category by CII-AICTE Survey; and was conferred with 'A' Grade by Department of Higher Education, Andhra Pradesh. The college participated in National Institution Ranking Frame Work (NIRF), 2019 and awarded the rank of 167. SIEMENS established 6 State-of-the art t-SDI laboratories.

Courses Offered:

The college offers B. Tech Programs in ECE, CSE, IT, EEE, EIE, CSSE, Civil, MECH. The college also offers M. Tech. in DECS, VLSI, CMS, CS, CNIS, SE, EPS, PED & MCA along with PhDs in ECE, EEE & CS.

About Tirupati:

Tirupati city is famous for Sri Venkateswara Swamy temple located about 15 kilometers North West of Tirupati in Tirumala hills at an elevation of 853 meters (2,799 ft). It is one of the most important pilgrimage centres in the World and the temple draws millions of pilgrims across the world. Also, the city is a major economic and educational hub in the southern region of the state.

Location:

The college is located 15 km from the temple town of Tirupati on Tirupati- Madanapalle National Highway 205.

<https://goo.gl/maps/SxeqCCprSUMpkTME7>

KINDLY SCAN QR CODE FOR LOCATION



About the Department:

The department of ECE started in 1996, from the inception of the College. The Department has a team of highly motivated and dedicated faculty to the cause of academics and striving to do the best in the interest of the students.

The Department of Electronics and Communication Engineering offers an Undergraduate program in Electronics and Communication Engineering and three Postgraduate programs in DECS, VLSI and CMS specializations. The Department has been recognized as research centre by JNTUA, Anantapuramu. The Department has full time Ph.D Scholars and JRFs. In addition department has established Research Laboratories like National MEMS Design Centre, Antenna Research Lab and Nanoelectronics Lab. The Department is undertaking research projects from DST, AICTE, ISRO, INUP, Govt of A.P. & etc.,

Centre for VLSI and Embedded systems:

The VLSI and Embedded Systems based design are gaining momentum in all application areas. As a result, there exists an exclusive center on 2018 for design and development of solutions for the society based problems. Research Centre creates a platform to conduct extensive research in the area of VLSI and Embedded Systems. The Research Centre exposes the faculties, researchers, and students to the latest Research works and technologies by involving them in projects and collaborates with industries. At present there are 15 active Faculty members working in the area of Low Power CMOS VLSI Design, Circuit and device modeling in VLSI Applications, Network on Chip, Energy harvesting Applications, Wearable devices , Sensors in Medical and Environmental Applications, Error Mitigation in High Density Memories, Remote Sensing, IOT Application, etc.,

About the Program:

Research and development in VLSI system have been growing in the public and private sectors in both developed and developing countries. The continuous increase of integration densities in CMOS technology has driven the rapid growth of VLSI circuit for today's high-tech electronics industries from consumer products to telecommunications and computers. As CMOS technologies are scaled down into the nanometer range IC design and testing have become a real challenge to ensure the functionality and quality of the product.

In the light of these developments, Sree Vidyanikethan Engineering College, Tirupati in collaboration with AICTE is organizing a STTP on Recent trends and Research challenges in NANO CMOS VLSI circuits using industry standard EDA tools as a forum on new and emerging applications of handheld and portable devices.

Objectives:

- Upgrade the knowledge of academicians and practioners in teaching /research/industry in the field of VLSI design.
- Strengthen the research activities in the field of Nano scale VLSI among the faculty and researchers by providing hands on experience on the usage of various industrial standard EDA tools.
- Analyze and apply the experience on the various methodologies and techniques in Nano scale VLSI
- Provide forum to exchange views, ideas and innovations with eminent experts in the field of Nano Scale VLSI design.
- Helps to identify the thrust areas and develop new applications.

Experts:

Resource persons from Mentor Graphics –Core EL Technologies, Bangalore , TCAD-Cadre Design Systems, Ghaziabad and Academicians from reputed institutions.

Eligibility Criteria:

- Faculty from AICTE affiliated institutions
- Research scholars / students
- Industry Professionals

Accommodation details:

Accommodation will be provided to the outstation participants subject to the availability. The reimbursement of travel expenses will be made as per AICTE guidelines. TA will be provided to the outstation participants in the shortest route on submission of the necessary documents.

Please send the filled registration forms before the deadline to vlsiembeddcentre@gmail.com

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

SREE SAINATH NAGAR, TIRUPATI - 517 102 (A.P.)

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Title Mr. Ms. Mrs.

Name of the Participant

Gender Male Female

Designation

Organization

Institution

Address for Communication

Email-ID

Mobile No.

Subjects taught

Experience (in Yrs.)

Accommodation Required Yes No

Is the Institution approved by AICTE? Yes No

DECLARATION

The given information is true to the best of my knowledge. I agree to abide by the rules and regulations of the programme. If selected, I shall attend the workshop for the entire duration.

Signature of the Applicant

Date :
Place :

Signature of the Head of Institution/
Sponsoring Authority (for sponsored candidates)

Chief Patron:

Dr. M. Mohan Babu, Chairman, SVET
Mr. Vishnu Manchu, CEO, SVET

Patron:

Dr. P. C. Krishnamachary, Principal, SVEC

Advisors:

Dr. T. Lazar Mathew, Research Advisor, SVET
Dr. D.V.S. Bhagavanulu, Director, SVEC
Dr. I. Sudharsan Kumar, Director(Q&D), SVET
Dr. P. Venkat Ramana, Professor & Head of ECE, SVEC

Co-ordinator:

Dr. N. Vithyalakshmi, Assoc.Professor,ECE,SVEC

Co-ordinators:

Dr. N. Ashok Kumar, Assoc. Professor, ECE , SVEC
Dr. P. Nagarajan, Assoc. Professor, ECE, SVEC
Mr. P. Madhu Kumar, Asst. Prof.(SL), ECE, SVEC

Organizing Secretaries:

Dr. V.R. Anitha, Professor, ECE
Dr. N. Padmaja, Professor, ECE
Dr. D. Leela Rani, Professor, ECE
Dr. P. Geetha, Assoc. Professor, ECE
Ms. K. Neelima, Asst. Prof., ECE

Organizing Members:

Dr. B. Senthil Kumar, Assoc. Professor, ECE
Dr. C. Venkataramanan, Assoc. Professor, ECE
Dr. V.V. Satyanarayana, Assoc. Professor, ECE
Dr. G. Arul Elango, Assoc. Professor, ECE
Dr. T.V.S. Gowtham Prasad, Assoc. Professor, ECE
Dr. V.M.S.N. Pavan Kumar Ch., Assoc. Professor, ECE
Dr. Argha Sarkar, Assoc. Professor, ECE
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Mr. G. Naresh, Asst. Prof. ECE
Mr. K. Ramesh, Asst. Prof., ECE
Mr. Kaustubh Kumar Shukla, Asst. Prof. ECE
Mr. P.V.S.R. Bharadwaja, Asst. Prof. ECE
Ms. B. Gowthami, Asst. Prof. ECE & Faculty, Dept. of ECE

NO REGISTRATION FEES

The registration is free for faculty members from AICTE approved institutions. The number of participants is limited to 40 and the selection is based on first come - first serve basis

Important Dates:

Registration Open	August 02 nd , 2019
Last Date for Registration	August 19 th , 2019
Intimation of Acceptance	August 21 st , 2019

contact

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