Documentary Evidences for Qualitative Metric:

2.2.1: The institution assesses the learning levels of the students, after admission and organizes special programs for advanced learners and slow learners

2.3.1: Student centric methods, such as experiential learning, participative learning and problem solving methodologies are used for enhancing learning experiences

2.6.1: Program outcomes, program specific outcomes and course outcomes for all Programs offered by the institution are stated and displayed on website and communicated to teachers and students

2.2.1

The institution assesses the learning levels of the students, after admission and organizes special programs for advanced learners and slow learners

1. Evidences for QIM metric 2.2.1:

(i) **Induction and orientation programs for I year students - *reports of the events and photographs***

(ii) **Diagnostic Tests are conducted to identify the learning ability of students – *Sample Copy of Question Paper and Analysis***

(iii) **Formative Tests are conducted for continuous assessment of Learning Outcomes – *Sample Copy of Question Paper and Analysis***

(iv) **Remedial classes are organized for slow learners - *Schedule, List of students and attendance***

(v) **Reinforcement classes are given to those students who fail in the periodic summative tests - *Schedule, List of students and attendance***

(vi) **For advanced learners are advised to undertake literature survey and mini projects which subsequently help them in seminar and project courses - *Seminar schedules and List of student reports***

(vii) **For rural background students special classes in English communication, mathematics, computer programming and fundamental core courses are conducted to facilitate easy learning - *Schedule, List of students and attendance***

(viii) **Special expert lectures on motivation by nationally renowned speakers for the first year students to facilitate them in goal setting and etiquette - *Reports of the events, students' attendance report and photographs***

(ix) **Fresher's day to introduce I year students with senior students - *Report of the event and photographs***

(x) **Senior students taking anti ragging oath and written undertaking - *Template of anti ragging oath and photographs***

Summary of Activities under 2.2.1

Academic Year	Orientation Program (Each Department)	Diagno-stic Tests	Forma-tive Tests	Remedial Classes for Slow Learners	Reinforce-ment Classes for Failed Students	Assignments/ Mini-Projects/ survey for Advanced Learners	Special Sessions on English	Special Lecture on Motivation	Fresher's Day
2017-18	1 for UG 1 for PG	316	1128	1303	351	228	432	5	19th August 2017
2016-17	1 for UG 1 for PG	313	1234	1206	567	293	252	4	9th September 2016
2015-16	1 for UG 1 for PG	286	1332	1152	380	141	243	3	1st September 2015
2014-15	1 for UG 1 for PG	279	1499	1184	476	278	243	5	1st November 2014
2013-14	1 for UG 1 for PG	270	1307	797	431	216	216	3	16th November 2013
2012-13	1 for UG 1 for PG	200	975	606	573	111	216	2	10th November 2012

(i) Induction and orientation programs for I year students - *reports of the events and photographs*

After admission, the institution organizes induction/orientation program for I year students.

i) Report of Induction Program:

A two week Induction Program was organized to the I Year B.Tech Students (2018 admitted) from 2nd July, 2018 to 15th July, 2018. This program is majorly aimed to acclimatize the I B.Tech Engineering Students to the new environment and create a bonding between the Teacher and Student, making them feel at ease.

The Induction Program was inaugurated at 10:00 AM on 02-07-2018 in Dasari Auditorium with a warm welcome to the I B.Tech students. The Special Officer, Chief Operating Officer, Director, Principal, Deans & HoDs while welcoming them briefed on the importance of Induction Program and advised them to embrace the value & essence of carrying technical perception in solving day-to-day problems of society and nation.

The activities under Induction program was carefully planned such that the new students are provided with basic information and prerequisites needed for engineering course and their chosen domain specialization. Understanding of society, ethics and human relationships were part of value added programs intending to build the character with essential qualities, to become a fulfilling engineer, a good citizen and a peaceful human being.

List of activities organized as part of the two weeks induction program:

1. Understanding Human Values:
- a. Introduction to Value Education
- b. Understanding Harmony at Various Levels
- c. Implication of Right Understanding in Life and Profession
- d. Personal Etiquette

2. Special Lectures:
- a. Personality Development
- b. Time Management
- c. Goal Setting

3. Regulatory Affairs:
- a. Academic Regulations
- b. Examination Regulations

4. Familiarization to Departments:
- a. Significance of Branch of Study
- b. Basic Teaching Learning Methods
- c. Application of Domain to Society and Industry
- d. Visits to Labs, Workshops and other Facilities.

5. Lectures by Senior Professors:
- a. Career by Entrepreneurship
- b. Career by Research
- c. Higher Education Opportunities

6. English Language Proficiency:
- a. Parts of Speech
- b. Word Building

c. Sentence Formation
d. Active and Passive Voices
e. Non-Verbal Communications
f. Assertive and Grapevine Communications
g. Story Telling

7. Computer Proficiency:

a. MS Word
b. MS Excel
c. MS PowerPoint

8. Creative Arts:

a. Drawing and Painting
b. Significance of Varieties of Indian Music
c. Significance of Varieties of Indian Dance

9. Physical Activities:

a. Yoga
b. Awareness on Indoor Sports like Chess, Volleyball, Basketball
c. Awareness on Outdoor Sports like Football, Rugby, Baseball

10. Co-Curricular Activities:

a. Debates
b. Seminars

11. Informal Interactions:

a. Mimicry
b. Story Telling
c. Presentation of Student Groups

Goal setting, personality development besides orientation to the domain and specialization were the core objectives of the program. It was hugely successful in energizing and reorienting them to their chosen elite engineering profession.

Dr. I. Sudarsan Kumar, COO, SVET delivering a Special Lecture on 'Goal Setting'

Dr. K. Saradhi, Controller of Examinations, SVEC explaining the 'Regulatory Affairs: Examination Regulations of SVEC'

Dr. L.V. Reddy, Professor, Dept. of IT, SVEC, Creating 'Awareness on Yoga & Therapy'

***ii) Report of Orientation program**:*

Dept. of Electronics and Communication Engineering organized an orientation programme for the newly admitted B. Tech (ECE) students at the institute premises on 13th July, 2015. The objective of the programme was to make the students aware of the academic aspects regulations of the program and the rules of the Institution. The Head of the Department welcomed the students and wished them a happy stay on the campus for four years and prosperous career.

HOD, ECE presented the Department Profile, Academic Regulation, Course structure to all the students. The following academic Regulations were highlighted in the presentation

- Profile of the Department
- Vision, Mission, PEOs, POs and PSOs
- Academic calendar
- Composition of Course Structure

- Credit System
- Examination System & Distribution of Marks, Evaluation methods etc.,
- Attendance requirements/eligibility to appear for the Year-end/Semester-end Examinations
- Academic requirement for promotion/completion of program of study
- Award of Degree & Award of Division
- Additional Academic Regulation

There were about 152 first year students present during the orientation.

HOD presenting Academic regulation to I B.Tech ECE & EEE

(ii) Diagnostic Tests are conducted to identify the learning ability of students – *Sample Copy of Question Paper and Analysis*

Diagnostic Tests are conducted to identify the learning ability of students

Sample Copy for the course Process Control Instrumentation

SREE VIDYANIKETHAN ENGINEERING COLLEGE

III B.Tech. II Semester Diagnostic Test

Process Control Instrumentation

(2016-17)

(Common to EIE A & B)

Roll number: 14I12A1071 Date: 02/11/17 Max. Marks: 10

SET-1

6

1. Write the difference between open loop and closed loop system.

Open loop – with out feed back

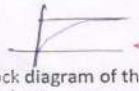
closed loop – with feed back.

2. Define the transfer function.

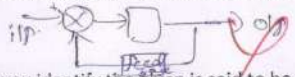
It is given by the ratio of change in o/p due to the change in input

$TF = \frac{o/p}{I/p}$

3 Draw the step response of the first order process



4. Draw the block diagram of the closed loop system



5 How do you identify the given is said to be stable using Bode Plot?

6. . What are the objectives of the control system?

→ TO analyse the characteristics of the systems

→ TO obtain the transfer function.

7. What is the difference between compensator and controller?

Controller – o/p is controlled by the change in ib

Compensator – A seperate ckt used in the system to compensate the error.

8. Write the mathematical equation of PID controller.

$$p(t) = K_p e(t) + \frac{K_p}{T_i}\int_0^t e(t)\,dt + K_p T_d \frac{de(t)}{dt} + p(0)$$

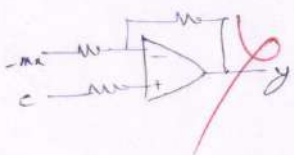
9. Write the transfer function of the given differentional equation

$$2d^2y/dt + 3dy/dt - y = 4x$$

$$2 \cdot s^2 y(s) + 3 s y(s) - y(s) = 4x(s)$$

$$TF\ G(s) = \frac{y(s)}{x(s)} = \frac{4}{2s^2+s-1}$$

10. Develop the equation y=mx+c using operational amplifiers



Student Performance analysis in Diagnostic Test for the Course Process Control Instrumentation

SREE VIDYANIKETHAN ENGINEERING COLLEGE

SREE SAINATH NAGAR, A.RANGAMPET - 517102

III B.TECH. II SEMESTER

Process Control Instrumentation (SEC-B)

Academic Year 2016-17

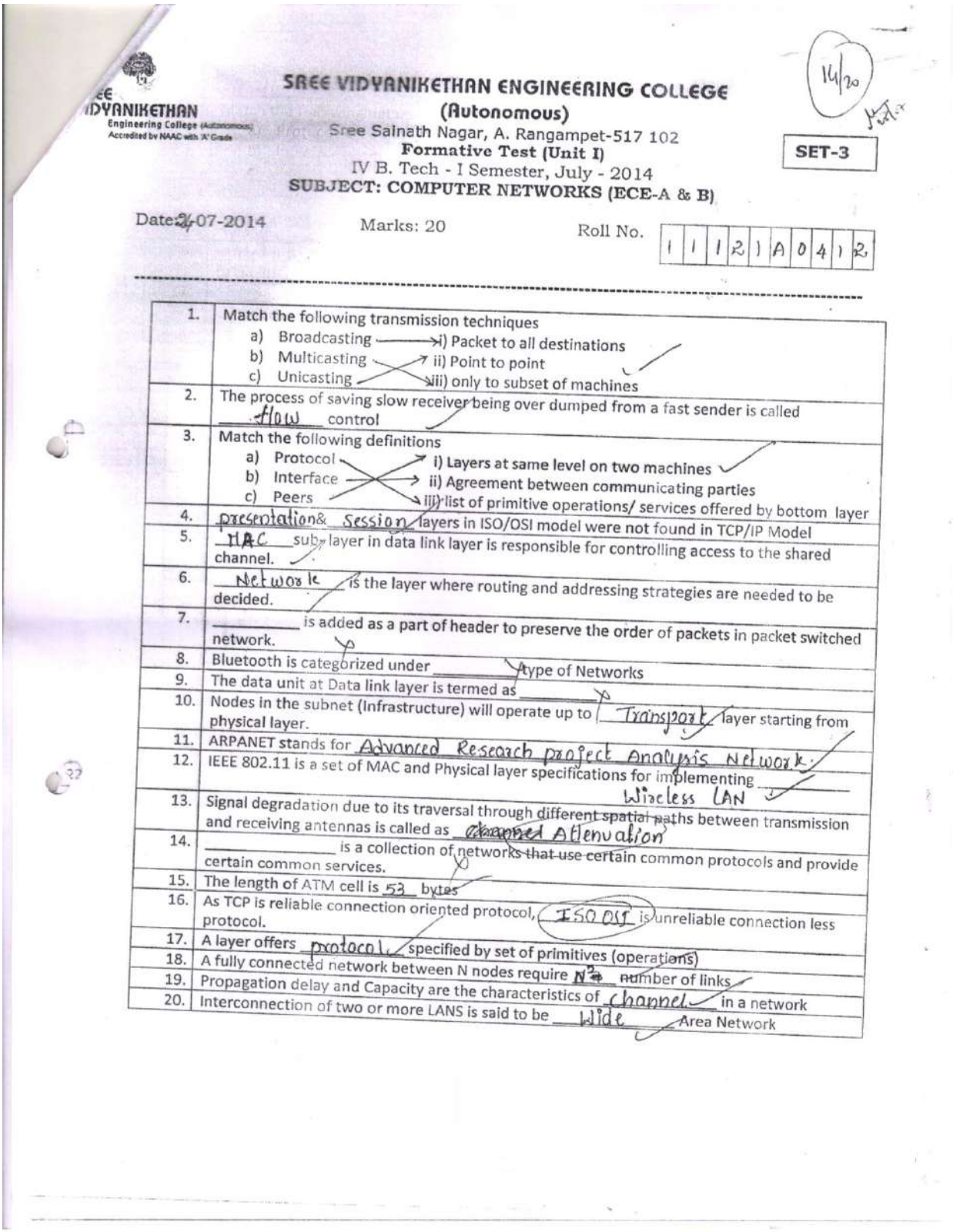
S.No.	Roll Number	Marks
1	14121A1058	A
2	14121A1059	5
3	14121A1060	A
4	14121A1061	3
5	14121A1063	7
6	14121A1064	5
7	14121A1065	3
8	14121A1066	A
9	14121A1067	A
10	14121A1069	A
11	14121A1070	0
12	14121A1071	6
13	14121A1072	A
14	14121A1073	3
15	14121A1074	7
16	14121A1075	3
17	14121A1076	5
18	14121A1077	A
19	14121A1078	A
20	14121A1079	0
21	14121A1080	4
22	14121A1081	A
23	14121A1082	1
24	14121A1083	A
25	14121A1084	A
26	14121A1085	2
27	14121A1086	2
28	14121A1087	0
29	14121A1088	3
30	14121A1089	1
31	14121A1090	0
32	12121A1065	A
33	13121A1025	4
34	13121A1053	4
35	13121A1065	1
36	13121A10A5	A
37	151215A1001	3
38	151215A1002	2
39	151215A1003	1
40	151215A1004	2
41	151215A1005	5
42	151215A1006	A
43	151215A1007	5
44	151215A1008	4
45	151215A1009	5
46	151215A1010	1
47	151215A1011	7
48	151215A1012	1
49	151215A1013	1

Signature of the Faculty

(iii) Formative Tests are conducted for continuous assessment of Learning Outcomes – *Sample Copy of Question Paper and Analysis*

Formative Tests are conducted for continuous assessment of Learning Outcomes

Sample Copy for the course Computer Networks



SREE VIDYANIKETHAN ENGINEERING COLLEGE
(Autonomous)
Sree Sainath Nagar, A. Rangampet-517 102
Formative Test (Unit I)
IV B. Tech - I Semester, July - 2014
SUBJECT: COMPUTER NETWORKS (ECE-A & B)

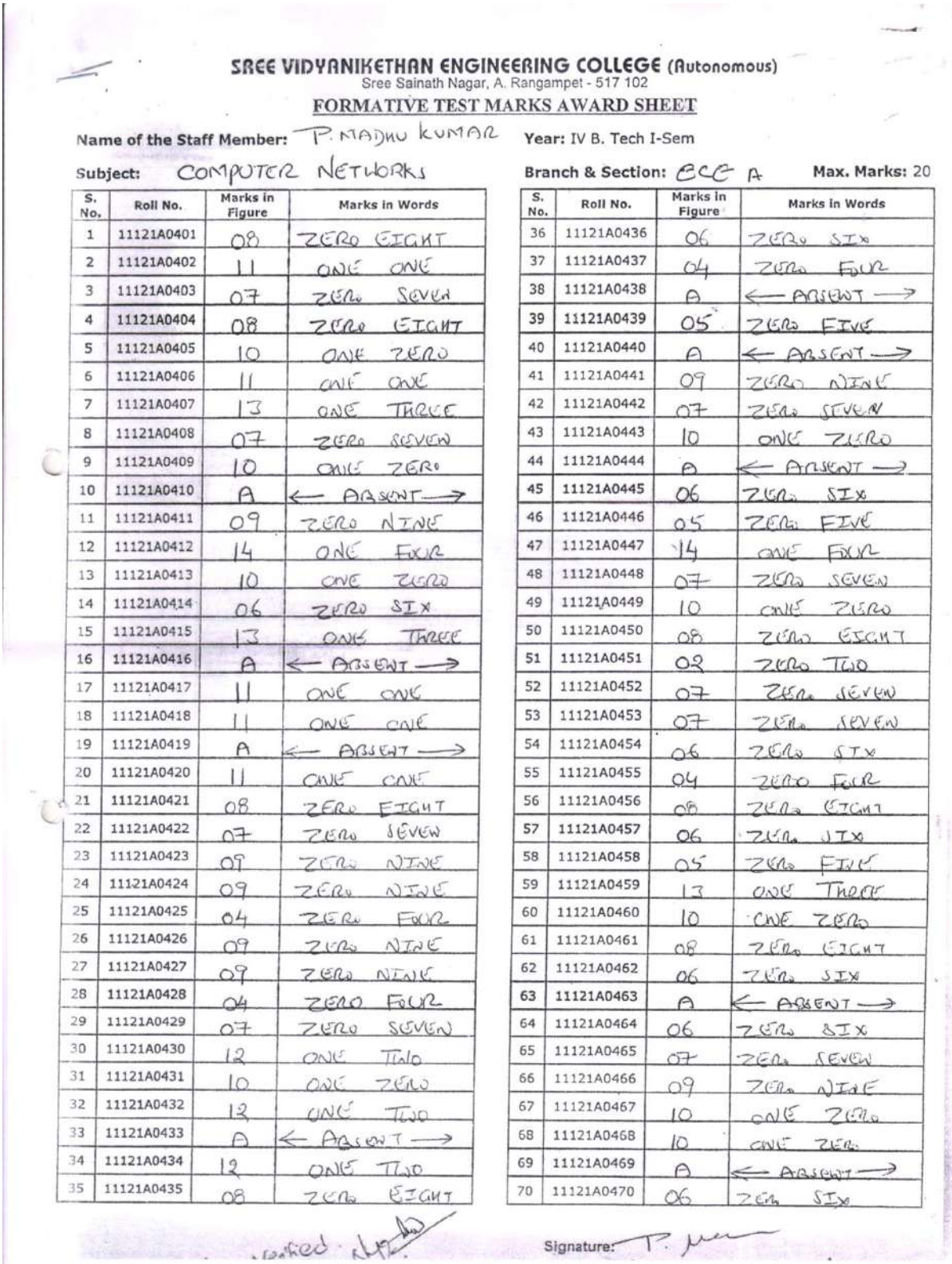
14/20

SET-3

Date: 24-07-2014 Marks: 20 Roll No. 1 1 1 2 1 A 0 4 1 2

1.	Match the following transmission techniques a) Broadcasting → i) Packet to all destinations b) Multicasting → iii) only to subset of machines c) Unicasting → ii) Point to point
2.	The process of saving slow receiver being over dumped from a fast sender is called flow control
3.	Match the following definitions a) Protocol → iii) list of primitive operations/ services offered by bottom layer b) Interface → i) Layers at same level on two machines c) Peers → ii) Agreement between communicating parties
4.	presentation & Session layers in ISO/OSI model were not found in TCP/IP Model
5.	MAC sub layer in data link layer is responsible for controlling access to the shared channel.
6.	Network is the layer where routing and addressing strategies are needed to be decided.
7.	_____ is added as a part of header to preserve the order of packets in packet switched network.
8.	Bluetooth is categorized under Type of Networks
9.	The data unit at Data link layer is termed as _____
10.	Nodes in the subnet (Infrastructure) will operate up to Transport layer starting from physical layer.
11.	ARPANET stands for Advanced Research project Analysis Network.
12.	IEEE 802.11 is a set of MAC and Physical layer specifications for implementing Wireless LAN
13.	Signal degradation due to its traversal through different spatial paths between transmission and receiving antennas is called as Channel Attenuation
14.	_____ is a collection of networks that use certain common protocols and provide certain common services.
15.	The length of ATM cell is 53 bytes
16.	As TCP is reliable connection oriented protocol, ISO OSI is unreliable connection less protocol.
17.	A layer offers protocol specified by set of primitives (operations)
18.	A fully connected network between N nodes require N^2 number of links
19.	Propagation delay and Capacity are the characteristics of channel in a network
20.	Interconnection of two or more LANS is said to be Wide Area Network

Student Performance analysis in Formative Test for the Course Computer Networks



SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)
Sree Sainath Nagar, A. Rangampet - 517 102

FORMATIVE TEST MARKS AWARD SHEET

Name of the Staff Member: P. MADHU KUMAR **Year:** IV B. Tech I-Sem

Subject: COMPUTER NETWORKS **Branch & Section:** ECE A **Max. Marks:** 20

S. No.	Roll No.	Marks in Figure	Marks in Words
1	11121A0401	08	ZERO EIGHT
2	11121A0402	11	ONE ONE
3	11121A0403	07	ZERO SEVEN
4	11121A0404	08	ZERO EIGHT
5	11121A0405	10	ONE ZERO
6	11121A0406	11	ONE ONE
7	11121A0407	13	ONE THREE
8	11121A0408	07	ZERO SEVEN
9	11121A0409	10	ONE ZERO
10	11121A0410	A	← ABSENT →
11	11121A0411	09	ZERO NINE
12	11121A0412	14	ONE FOUR
13	11121A0413	10	ONE ZERO
14	11121A0414	06	ZERO SIX
15	11121A0415	13	ONE THREE
16	11121A0416	A	← ABSENT →
17	11121A0417	11	ONE ONE
18	11121A0418	11	ONE ONE
19	11121A0419	A	← ABSENT →
20	11121A0420	11	ONE ONE
21	11121A0421	08	ZERO EIGHT
22	11121A0422	07	ZERO SEVEN
23	11121A0423	09	ZERO NINE
24	11121A0424	09	ZERO NINE
25	11121A0425	04	ZERO FOUR
26	11121A0426	09	ZERO NINE
27	11121A0427	09	ZERO NINE
28	11121A0428	04	ZERO FOUR
29	11121A0429	07	ZERO SEVEN
30	11121A0430	12	ONE TWO
31	11121A0431	10	ONE ZERO
32	11121A0432	12	ONE TWO
33	11121A0433	A	← ABSENT →
34	11121A0434	12	ONE TWO
35	11121A0435	08	ZERO EIGHT

S. No.	Roll No.	Marks in Figure	Marks in Words
36	11121A0436	06	ZERO SIX
37	11121A0437	04	ZERO FOUR
38	11121A0438	A	← ABSENT →
39	11121A0439	05	ZERO FIVE
40	11121A0440	A	← ABSENT →
41	11121A0441	09	ZERO NINE
42	11121A0442	07	ZERO SEVEN
43	11121A0443	10	ONE ZERO
44	11121A0444	A	← ABSENT →
45	11121A0445	06	ZERO SIX
46	11121A0446	05	ZERO FIVE
47	11121A0447	14	ONE FOUR
48	11121A0448	07	ZERO SEVEN
49	11121A0449	10	ONE ZERO
50	11121A0450	08	ZERO EIGHT
51	11121A0451	02	ZERO TWO
52	11121A0452	07	ZERO SEVEN
53	11121A0453	07	ZERO SEVEN
54	11121A0454	06	ZERO SIX
55	11121A0455	04	ZERO FOUR
56	11121A0456	08	ZERO EIGHT
57	11121A0457	06	ZERO SIX
58	11121A0458	05	ZERO FIVE
59	11121A0459	13	ONE THREE
60	11121A0460	10	ONE ZERO
61	11121A0461	08	ZERO EIGHT
62	11121A0462	06	ZERO SIX
63	11121A0463	A	← ABSENT →
64	11121A0464	06	ZERO SIX
65	11121A0465	07	ZERO SEVEN
66	11121A0466	09	ZERO NINE
67	11121A0467	10	ONE ZERO
68	11121A0468	10	ONE ZERO
69	11121A0469	A	← ABSENT →
70	11121A0470	06	ZERO SIX

Verified

Signature:

(iv) Remedial classes are organized for slow learners - *Schedule, List of students and attendance*

Remedial classes are organized for slow learners

Sample copy of Schedule, List of students and attendance:

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

Department of Electronics and Communication Engineering

ACADEMIC YEAR - 2015-2016

Remedial Classes Timetable

II B. Tech. – II Sem. (ECE A) Room No : **311**

Timing : **08:00AM - 9:40AM**

EFFECTIVE DAYS : **16/02/2016 to 02/03/2016**

Day \ Hour	08:00AM – 08:50AM	08:50AM – 09:40AM
Mon	**CS**	**ECA&D**
Tue	**AC**	**STLD**
Wed	**EMTL**	**S&S**
Thu	**ECA&D**	**PE**
Fri	**S&S**	**CS**
Sat	**STLD**	**EMTL**

14BT4HS02	**PE**	Professional Ethics	Mr.P.V.S.R.Bharadwaja Dr. N. Padmaja*
14BT40401	**AC**	Analog Communications	Mr.S.Thulasi Prasad Mr.G.Hemachandra*
14BT40402	**ECA&D**	Electronic Circuit Analysis and Design	Ms.K.Neelima Mr.C.Venkata Sudhakar*
14BT40403	**EMTL**	Electromagnetic Theory and Transmission Lines	Ms.K.Sudha Dr.A.B.Yadav*
14BT40404	**S&S**	Signals and Systems	Ms.D.Leela Rani Mr.G.Naresh*
14BT40405	**STLD**	Switching Theory and Logic Design	Mr.T.Ravi Sekhar Mr.K.Ramesh*
14BT50201	**CS**	Control Systems	Ms.B.Gowthami Ms.C.Maheswari*

HOD, ECE

CC: Principal, Director, Dean-Academics, Examination Section, NIVA Coordinator, Notice Board.

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)
Sree Sainath Nagar, Tirupati- 517 102

Department of Electronics and Communication Engineering

ACADEMIC YEAR - 2015-2016

Remedial Classes Timetable

II B. Tech. – II Sem. (ECE B) Room No : **330**
Timing : **08:00AM - 9:40AM**
EFFECTIVE DAYS : **16/02/2016 to 02/03/2016**

Day \ Hour	08:00AM - 08:50AM	08:50AM - 09:40AM
Mon	**EMTL**	**STLD**
Tue	**S&S**	**CS**
Wed	**ECA&D**	**AC**
Thu	**STLD**	**PE**
Fri	**ECA&D**	**EMTL**
Sat	**CS**	**S&S**

14BT4HS02	**PE**	Professional Ethics	Dr. N. Padmaja Ms.P.Geetha*
14BT40401	**AC**	Analog Communications	Mr.S.Thulasi Prasad Mr.G.Hemachandra*
14BT40402	**ECA&D**	Electronic Circuit Analysis and Design	Mr.C.Venkata Sudhakar Mr.Sai Raja Narendra*
14BT40403	**EMTL**	Electromagnetic Theory and Transmission Lines	Ms.K.Sudha Mr.Kaustubh Kumar Shukla*
14BT40404	**S&S**	Signals and Systems	Mr.G.Naresh Mr.T.Ravi Sekhar*
14BT40405	**STLD**	Switching Theory and Logic Design	Mr.K.Ramesh Mr.G.Naresh*
14BT50201	**CS**	Control Systems	Ms.C.Maheswari Mr.R.Maheswar Reddy*

HOD, ECE

CC: Principal, Director, Dean-Academics, Examination Section, NIVA Coordinator, Notice Board.

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)
Sree Sainath Nagar, Tirupati- 517 102

Department of Electronics and Communication Engineering

ACADEMIC YEAR - 2015-2016

Remedial Classes Timetable

II B. Tech. – II Sem. (ECE C) Room No : **331**
Timing : **08:00AM - 9:40AM**
EFFECTIVE DAYS : **16/02/2016 to 02/03/2016**

Day \ Hour	08:00AM – 08:50AM	08:50AM – 09:40AM
Mon	**S&S**	**AC**
Tue	**ECA&D**	**EMTL**
Wed	**PE**	**CS**
Thu	**STLD**	**S&S**
Fri	**CS**	**STLD**
Sat	**EMTL**	**ECA&D**

14BT4HS02	**PE**	Professional Ethics	Ms.P.Geetha Ms.V.R.Anitha*
14BT40401	**AC**	Analog Communications	Mr.G.Hemachandra Mr.Shaik Mahaboob Basha*
14BT40402	**ECA&D**	Electronic Circuit Analysis and Design	Mr.Sai Raja Narendra Mr.C.Venkata Sudhakar*
14BT40403	**EMTL**	Electromagnetic Theory and Transmission Lines	Dr.A.B.Yadav Ms.K.Sudha*
14BT40404	**S&S**	Signals and Systems	Mr.T.Ravi Sekhar Mr.P.K.Pradhan*
14BT40405	**STLD**	Switching Theory and Logic Design	Mr.G.Naresh Mr.C.Ravindra Murthy*
14BT50201	**CS**	Control Systems	Mr.R.Maheswar Reddy Ms.V.Navya*

HOD, ECE

CC: Principal, Director, Dean–Academics, Examination Section, NIVA Coordinator, Notice Board.

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)
Sree Sainath Nagar, Tirupati- 517 102

Department of Electronics and Communication Engineering

ACADEMIC YEAR - 2015-2016

Remedial Classes Timetable

II B. Tech. –II Sem. (ECE D) Room No : **332**
Timing : **08:00AM - 9:40AM**
EFFECTIVE DAYS : **16/02/2016 to 02/03/2016**

Day \ Hour	08:00AM – 08:50AM	08:50AM – 09:40AM
Mon	**EMTL**	**AC**
Tue	**ECA&D**	**S&S**
Wed	**STLD**	**CS**
Thu	**PE**	**EMTL**
Fri	**CS**	**ECA&D**
Sat	**S&S**	**STLD**

14BT4HS02	**PE**	Professional Ethics	Ms.V.R.Anitha Mr.P.V.S.R.Bharatwaja*
14BT40401	**AC**	Analog Communications	Mr.Shaik Mahaboob Basha Mr.S.Thulasi Prasad*
14BT40402	**ECA&D**	Electronic Circuit Analysis and Design	Mr.C.Venkata Sudhakar Ms.K.Neelima*
14BT40403	**EMTL**	Electromagnetic Theory and Transmission Lines	Mr.Kaustubh Kumar Shukla Ms.K.Sudha*
14BT40404	**S&S**	Signals and Systems	Mr.P.K.Pradhan Ms.D.Leela Rani*
14BT40405	**STLD**	Switching Theory and Logic Design	Mr.C.Ravindra Murthy Mr.T.Ravi Sekhar*
14BT50201	**CS**	Control Systems	Ms.V.Navya Ms.B.Gowthami*

HOD, ECE

CC: Principal, Director, Dean-Academics, Examination Section, NIVA Coordinator, Notice Board , HOD:EIE

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)
Sree Sainath Nagar, Tirupati- 517 102

Department of Electronics and Communication Engineering

II B.Tech II Sem ECE – A,B,C & D (2015-16)

Subject: EMTL

Attendance Sheet

Sl. No.	Roll Number	Student Name	17/02/2016	20/02/2016	24/02/2016	27/02/2016
1.	13121A04B4	NARAYANA VAISHNAVI	✓	✓	✓	✓
2.	14121A0405	AMBATIVENKATACHARAN	✓	A	✓	✓
3.	14121A0414	B S JYOTHSNA	✓	A	✓	✓
4.	14121A0417	BADABHAGNI PRASANTH	✓	✓	✓	✓
5.	14121A0433	CHINTALA YASWANTH	✓	✓	✓	✓
6.	14121A0442	DASARI SRAVANI	✓	✓	✓	✓
7.	14121A0445	DEVARAKONDA VASU	✓	✓	✓	✓
8.	14121A0450	EEDURU VIDYASAGAR	A	✓	✓	✓
9.	14121A0453	G CHANDRA SEKHAR NAIK	✓	✓	A	✓
10.	14121A0462	GUMMALLA KARTHIK REDDY	✓	A	✓	✓
Sl. No.	**Roll Number**	**Student Name**	**19/02/2016**	**22/02/2016**	**26/02/2016**	**29/02/2016**
11.	14121A0479	KALAVA VISHAL KUMAR	✓	✓	✓	✓
12.	14121A0485	KANIKARAM SAI PHANEENDRA	✓	✓	✓	✓
13.	14121A04A1	KOTHA NAVYA	✓	✓	✓	✓
14.	14121A04A7	LALMAHAMMADGARI MUNEER AHAMMAD	✓	✓	✓	✓
15.	14121A04A8	LINGALA GOUTHAM REDDY	✓	✓	A	✓
16.	14121A04B8	MARUPUDI RAMYA BHARATHI	✓	✓	✓	✓
17.	14121A04D4	NAMALA SAINATHA REDDY	✓	✓	✓	A
18.	14121A04D7	NUNE IZAZ	✓	✓	✓	✓
19.	14121A04E4	PACHAPALA RAVI	✓	A	A	✓
20.	14121A04H6	R KISHORE	✓	✓	✓	✓
Sl. No.	**Roll Number**	**Student Name**	**18/02/2016**	**22/02/2016**	**25/02/2016**	**29/02/2016**
21.	14121A04P5	YARRAGUNTLA VENKATNARYANA SUJAN BABU	✓	✓	✓	✓
22.	15125A0404	BHUPASAMUDRAM BHANU TEJA	✓	✓	✓	✓
23.	15125A0413	DUDDUKURU CHINA RAMANAMMA	✓	✓	✓	✓
24.	15125A0418	GUDIPI SUDHAKAR	✓	A	✓	✓
25.	15125A0420	GUNDLURU BHARGAVI	✓	✓	✓	✓
26.	15125A0431	MOPURI SUNEETHA	✓	✓	✓	✓
27.	15125A0437	PERUMANELLORE ASHOK	✓	A	A	✓
28.	15125A0443	SHAIK DADAGARI MOHAMMAD ZUBBAIR	✓	✓	✓	✓
29.	15125A0446	THALLA PRATHYUSH	A	✓	✓	✓
30.	15125A0451	YELLAREDDY RANJITH KUMAR REDDY	✓	✓	✓	A

HOD, ECE

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, A.Rangampet-517 102

Department of Electronics and Communication Engineering

Outcomes of Remedial Classes

Year & Branch: **II B.Tech., II-Semester, ECE-A, B, C & D**
Academic Year: **2015-16**

Based on the performance of the students in I-mid examination the remedial classes were scheduled and impact is analyzed:

S.NO.	ROLL NUMBER	STUDENT NAME	MID-I	(After Remediation) MID-II
		14BT4HS02		
1	14121A04G0	POBBATHI NITHIN KUMAR	9	13
2	14121A04M3	THUMALA BHANU PRAKASH	3	14
3	15125A0423	KAMPA NAGENDARA	9	13
4	15125A0433	P NARESH	11	19
5	15125A0450	VELPULA NAVEEN KUMAR	11	19
		14BT40401		
1	14121A0407	ANNAMPALLE NAVEEN	9	15
2	14121A0417	BADABHAGNI PRASANTH	7	4
3	14121A0440	DANDA VINOD KUMAR	2	5
4	14121A0444	DEVARAKONDA AKHILA	7	A
5	14121A0445	DEVARAKONDA VASU	8	15
6	14121A0450	EEDURU VIDYASAGAR	9	11
7	14121A0485	KANIKARAM SAI PHANEENDRA	7	4
8	14121A04D7	NUNE IZAZ	10	10
9	14121A04F0	PASULA SREENIVASA SUDHEER KUMAR	8	14
10	14121A04G1	POLARAPU NIRANJAN	6	8
11	14121A04G7	POTHUREDDY GANDAIAH SATEESH	7	12
12	14121A04H2	PULICALSREEKANTH	8	7
13	14121A04H6	R KISHORE	3	2
14	14121A04K0	SEELAM PRASANNA KUMAR REDDY	7	14
15	14121A04M0	THATIIGUTLA SATWIK KUMAR REDDY	6	12
16	14121A04M3	THUMALA BHANU PRAKASH	4	7
17	15125A0428	MADDURI SWATHI PRIYADARSINI	10	15
		14BT40402		
1	14121A0419	BASUTHKAR KIRAN RAO	5	15
2	14121A0440	DANDA VINOD KUMAR	3	15
3	14121A0442	DASARI SRAVANI	6	9
4	14121A0445	DEVARAKONDA VASU	7	19
5	14121A0453	G CHANDRA SEKHAR NAIK	2	A
6	14121A0465	HALVI VINOD KUMAR	6	15
7	14121A0470	K M VIGNESH	9	17
8	14121A04C0	MEDA REVANTH	8	16
9	14121A04D7	NUNE IZAZ	3	16
10	14121A04G0	POBBATHI NITHIN KUMAR	6	18

S.NO.	ROLL NUMBER	STUDENT NAME	MID-I	(After Remediation) MID-II
11	14121A04G1	POLARAPU NIRANJAN	5	14
12	14121A04H6	R KISHORE	5	4
13	14121A04K0	SEELAM PRASANNA KUMAR REDDY	6	19
14	14121A04K2	SHAIK AZHAR HUSSAIN	2	A
15	14121A04M0	THATIIGUTLA SATWIK KUMAR REDDY	10	16
16	14121A04M3	THUMALA BHANU PRAKASH	3	14
17	14121A04N0	UPPU RAVI TEJA	6	26
18	14121A04N1	UTTI RAJASREE	9	19
19	14121A04N2	V APOORVA REDDY	6	17
20	14121A04P0	VELLARU DURGA PRASAD	5	18
21	14121A04P5	YARRAGUNTLA VENKATNARYANA SUJAN BABU	9	15
22	15125A0418	GUDIPI SUDHAKAR	11	15
23	15125A0428	MADDURI SWATHI PRIYADARSINI	10	16
24	15125A0437	PERUMANELLORE ASHOK	6	18
25	15125A0443	SHAIK DADAGARI MOHAMMAD ZUBBAIR	6	14
26	15125A0444	SHAIK MOHAMMAD YOUNAS	10	15
		14BT40403		
1	13121A04B4	NARAYANA VAISHNAVI	10	A
2	14121A0405	AMBATIVENKATACHARAN	10	10
3	14121A0414	B S JYOTHSNA	8	10
4	14121A0417	BADABHAGNI PRASANTH	8	7
5	14121A0433	CHINTALA YASWANTH	2	1
6	14121A0442	DASARI SRAVANI	8	2
7	14121A0445	DEVARAKONDA VASU	9	9
8	14121A0450	EEDURU VIDYASAGAR	10	11
9	14121A0453	G CHANDRA SEKHAR NAIK	1	A
10	14121A0462	GUMMALLA KARTHIK REDDY	7	1
11	14121A0479	KALAVA VISHAL KUMAR	11	15
12	14121A0485	KANIKARAM SAI PHANEENDRA	11	1
13	14121A04A1	KOTHA NAVYA	7	17
14	14121A04A7	LALMAHAMMADGARI MUNEER AHAMMAD	8	27
15	14121A04A8	LINGALA GOUTHAM REDDY	8	12
16	14121A04B8	MARUPUDI RAMYA BHARATHI	5	14
17	14121A04D4	NAMALA SAINATHA REDDY	11	19
18	14121A04D7	NUNE IZAZ	4	1
19	14121A04E4	PACHAPALA RAVI	10	10
20	14121A04H6	R KISHORE	6	A
21	14121A04P5	YARRAGUNTLA VENKATNARYANA SUJAN BABU	10	18
22	15125A0404	BHUPASAMUDRAM BHANU TEJA	11	23
23	15125A0413	DUDDUKURU CHINA RAMANAMMA	7	17
24	15125A0418	GUDIPI SUDHAKAR	3	17
25	15125A0420	GUNDLURU BHARGAVI	10	18
26	15125A0431	MOPURI SUNEETHA	11	24
27	15125A0437	PERUMANELLORE ASHOK	8	16
28	15125A0443	SHAIK DADAGARI MOHAMMAD ZUBBAIR	7	15
29	15125A0446	THALLA PRATHYUSH	9	16
30	15125A0451	YELLAREDDY RANJITH KUMAR REDDY	6	26
		14BT40404		
1	14121A0433	CHINTALA YASWANTH	8	13

S.NO.	ROLL NUMBER	STUDENT NAME	MID-I	(After Remediation) MID-II
2	14121A0453	G CHANDRA SEKHAR NAIK	1	A
3	14121A04H6	R KISHORE	0	A
4	14121A04K2	SHAIK AZHAR HUSSAIN	3	A
		14BT40405		
1	14121A04G7	POTHUREDDY GANDAIAH SATEESH	8	16
2	14121A04H6	R KISHORE	1	A
3	14121A04M0	THATIIGUTLA SATWIK KUMAR REDDY	10	15
		14BT50201		
1	14121A0402	AKKYSETTY RUKMINI	10	15
2	14121A0444	DEVARAKONDA AKHILA	9	A
3	14121A0453	G CHANDRA SEKHAR NAIK	2	A
4	14121A04F2	PATNAMMADHUSAIPAVAN	11	15
5	14121A04G0	POBBATHI NITHIN KUMAR	5	14
6	14121A04G1	POLARAPU NIRANJAN	1	7
7	14121A04G7	POTHUREDDY GANDAIAH SATEESH	7	7
8	14121A04H4	R ASHA	11	18
9	14121A04H6	R KISHORE	2	A
10	14121A04J9	SATRASALA SAI TEJA	11	10
11	14121A04K2	SHAIK AZHAR HUSSAIN	0	A
12	14121A04M3	THUMALA BHANU PRAKASH	6	7
13	14121A04P5	YARRAGUNTLA VENKATNARYANA SUJAN BABU	11	19
14	15125A0443	SHAIK DADAGARI MOHAMMAD ZUBBAIR	1	17

Analysis of Remedial Outcomes:

Subject Code	Name of the Subject	No. of students Identified	No. of students benefited through Remedial
14BT4HS02	Professional Ethics	5	05
14BT40401	Analog Communications	17	11
14BT40402	Electronic Circuit Analysis and Design	26	23
14BT40403	Electromagnetic Theory and Transmission Lines	30	18
14BT40404	Signals and Systems	04	01
14BT40405	Switching Theory and Logic Design	03	02
14BT50201	Control Systems	14	08

HOD, ECE

(v) Reinforcement classes are given to those students who fail in the periodic summative tests - *Schedule, List of students and attendance*

Reinforcement classes are given to those students who fail in the periodic summative tests.

Sample copy of Schedule, List of students and attendance:

SREE VIDYANIKETHAN ENGINEERING COLLEGE
(Autonomous)
Sree Sainath Nagar, A.Rangampet – 517102

Department of Electronics and Communication Engineering

SVEC/ECE/Reinforcement Classes/2015-16 11-01- 2016

CIRCULAR

Reinforcement Classes for II B.Tech., I-Semester Subjects are scheduled during 18-31 January 2016.

Room No : **331** Timing: **8.00 AM to 10.00 AM**

Subject	Date	Name of the Faculty
Special Functions and Complex Analysis	18-1-2016	Dr. M. Sudheer Babu
	20-1-2016	Mr. B. Reddeppa
	22-1-2016	Dr. M. Sudheer Babu
	25-1-2016	Mr. K. Ramesh Babu
	28-1-2016	Mr. R. Mohan Ramana
	30-1-2016	Mr. B. Reddeppa
Network Analysis	19-1-2016	Dr. D. Leela Rani
	21-1-2016	Mr. T. Ravisekhar
	23-1-2016	Mr. T. V. S. Gowtham Prasad
	27-1-2016	Dr. D. Leela Rani
	29-1-2016	Mr. T. V. S. Gowtham Prasad
	31-1-2016	Mr. T. Ravisekhar

HOD, ECE

Cc: Principal, Dean (Academics), HOD-GEBH.

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, A. Rangampet – 517 102

Department of Electronics and Communication Engineering

Reinforcement Classes Attendance Sheet

II B. Tech I Semester – Special Functions and Complex Analysis

S. No.	Roll No.	Student Name	18/1	20/1	22/1	25/1	28/1	30/1
1	14121A0444	DEVARAKONDA AKHILA	✓	✓	✓	A	A	A
2	14121A0447	DUGGINENI KAILASHNATH YADAV	A	✓	✓	A	✓	A
3	14121A04D7	NUNE IZAZ	✓	A	A	✓	✓	✓
4	14121A04E8	PALLE VISHNUVARDHAN REDDY	✓	✓	A	✓	✓	✓
5	14121A04F2	PATNAM MADHUSAI PAVAN	✓	✓	A	✓	✓	✓
6	14121A04G1	POLARAPU NIRANJAN	✓	✓	✓	✓	✓	✓
7	14121A04G7	POTHUREDDYGANDAIAH SATEESH	A	A	✓	✓	✓	✓
8	14121A04H6	R KISHORE	A	✓	✓	✓	✓	A
9	14121A04J9	SATRASALA SAI TEJA	✓	✓	✓	✓	✓	✓
10	14121A04K0	SEELAM PRASANNA KUMAR REDDY	✓	✓	✓	✓	✓	✓
11	14121A04K2	SHAIK AZHAR HUSSIAN	✓	A	✓	✓	✓	✓
12	14121A04M0	THATIGUTLA SATWIK KUMAR REDDY	✓	✓	✓	✓	✓	✓
13	14121A04M3	THUMALA BHANU PRAKASH	A	✓	✓	✓	✓	✓
14	14121A04M6	TIRUVAIPATI BHARGAVNATH	✓	✓	A	✓	A	✓
15	14121A04P0	VELLARU DURGA PRASAD	✓	✓	A	✓	A	✓
16	14121A04P5	YARRAGUNTLA VENKATNARYANA SUJAN BABU	✓	A	A	✓	A	✓
17	13121A04B4	NARAYANA VAISHNAVI	✓	✓	✓	✓	✓	✓
18	15125A0401	ANNAM LAKSHMI NARAYANA	✓	✓	✓	✓	✓	✓
19	15125A0403	BANDI VAMSI KRISHNA	✓	✓	✓	✓	✓	✓
20	15125A0405	BODDU SIREESHA	✓	✓	✓	✓	A	✓
21	15125A0408	CHERUKURI VARAPRASAD	✓	✓	✓	✓	A	✓
22	15125A0412	DONAKONDA PRASANNA KUMAR REDDY	A	✓	✓	✓	✓	✓
23	15125A0413	DUDDUKURU CHINA RAMANAMMA	✓	A	✓	A	✓	✓
24	15125A0418	GUDIPI SUDHAKAR	✓	✓	✓	✓	A	✓
25	15125A0420	GUNDLURU BHARGAVI	✓	✓	✓	✓	✓	✓
26	15125A0422	ILLURU VINEELKUMAR REDDY	✓	✓	✓	A	✓	A
27	15125A0424	KODIDALA RAJKUMAR	✓	✓	✓	✓	✓	A
28	15125A0428	MADDURI SWATHI PRIYADARSINI	✓	✓	✓	✓	A	✓

S. No.	Roll No.	Student Name	18/1	20/1	22/1	25/1	28/1	30/1
29	15125A0429	MANDATI SUSHMITHA	✓	✓	✓	A	✓	✓
30	15125A0431	MOPURI SUNEETHA	✓	✓	✓	A	✓	A
31	15125A0433	P NARESH	A	✓	✓	A	A	✓
32	15125A0436	PALLIBOINA SARITHA	A	A	A	✓	✓	✓
33	15125A0437	PERUMANELLORE ASHOK	✓	✓	✓	✓	✓	✓
34	15125A0438	POLICHETTY NARESH	✓	A	✓	✓	✓	✓
35	15125A0439	P KRUPAKAR	✓	A	✓	A	✓	✓
36	15125A0443	SHAIK DADAGARI MOHAMMAD ZUBBAIR	✓	✓	✓	✓	A	A
37	15125A0444	SHAIK MOHAMMAD YOUNAS	A	✓	A	✓	✓	A
38	15125A0446	THALLA PRATHYUSH	✓	✓	✓	A	✓	✓
39	15125A0450	VELPULA NAVEEN KUMAR	✓	✓	✓	A	A	✓

HOD, ECE

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, A.Rangampet-517 102

Department of Electronics and Communication Engineering

Outcomes of Reinforcement Classes

Year & Branch: **II B.Tech ECE-A, B, C & D**
Semester: **I**
Academic Year: **2015-16**

Based on the failure rate of the students in Regular examinations the reinforcement classes were scheduled and impact is analyzed:

S. No.	Roll No.	Student Name	Marks Obtained	Marks Obtained(After Reinforcement)
		14BT3BS02		
1.	14121A0444	DEVARAKONDA AKHILA	31	44
2.	14121A0447	DUGGINENI KAILASHNATH YADAV	23	N
3.	14121A04D7	NUNE IZAZ	32	N
4.	14121A04E8	PALLE VISHNUVARDHAN REDDY	35	47
5.	14121A04F2	PATNAM MADHUSAI PAVAN	29	N
6.	14121A04G1	POLARAPU NIRANJAN	16	N
7.	14121A04G7	POTHUREDDYGANDAIAH SATEESH	23	N
8.	14121A04H6	R KISHORE	20	N
9.	14121A04J9	SATRASALA SAI TEJA	38	N
10.	14121A04K0	SEELAM PRASANNA KUMAR REDDY	25	42
11.	14121A04K2	SHAIK AZHAR HUSSIAN	20	N
12.	14121A04M0	THATIGUTLA SATWIK KUMAR REDDY	8	N
13.	14121A04M3	THUMALA BHANU PRAKASH	8	N
14.	14121A04M6	TIRUVAIPATI BHARGAVNATH	18	N
15.	14121A04P0	VELLARU DURGA PRASAD	22	47
16.	14121A04P5	YARRAGUNTLA VENKATNARYANA SUJAN BABU	9	N
17.	13121A04B4	NARAYANA VAISHNAVI	21	N
18.	15125A0401	ANNAM LAKSHMI NARAYANA	39	53
19.	15125A0403	BANDI VAMSI KRISHNA	36	47
20.	15125A0405	BODDU SIREESHA	18	41
21.	15125A0408	CHERUKURI VARAPRASAD	36	58
22.	15125A0412	DONAKONDA PRASANNA KUMAR REDDY	24	N
23.	15125A0413	DUDDUKURU CHINA RAMANAMMA	14	N
24.	15125A0418	GUDIPI SUDHAKAR	17	N
25.	15125A0420	GUNDLURU BHARGAVI	34	40
26.	15125A0422	ILLURU VINEELKUMAR REDDY	16	45
27.	15125A0424	KODIDALA RAJKUMAR	34	50
28.	15125A0428	MADDURI SWATHI PRIYADARSINI	35	N
29.	15125A0429	MANDATI SUSHMITHA	34	47
30.	15125A0431	MOPURI SUNEETHA	20	N

31.	15125A0433	P NARESH	13	N
32.	15125A0436	PALLIBOINA SARITHA	34	40
33.	15125A0437	PERUMANELLORE ASHOK	2	N
34.	15125A0438	POLICHETTY NARESH	30	42
35.	15125A0439	P KRUPAKAR	29	40
36.	15125A0443	SHAIK DADAGARI MOHAMMAD ZUBBAIR	2	N
37.	15125A0444	SHAIK MOHAMMAD YOUNAS	39	42
38.	15125A0446	THALLA PRATHYUSH	33	44
39.	15125A0450	VELPULA NAVEEN KUMAR	27	40
		14BT30232		
1.	14121A0402	AKKYSETTY RUKMINI	26	46
2.	14121A0405	AMBATI VENKATA CHARAN	38	43
3.	14121A0414	B S JYOTHSNA	33	53
4.	14121A0418	BALIJA GAYATHRI	37	54
5.	14121A0419	BASUTHKAR KIRAN RAO	30	N
6.	14121A0424	BODICHERLA MANISH	35	40
7.	14121A0436	CHOWDAM SAISRI	36	51
8.	14121A0442	DASARI SRAVANI	33	55
9.	14121A0446	DEVASATH SREE DIVYA	21	47
10.	14121A0447	DUGGINENI KAILASHNATH YADAV	5	N
11.	14121A0453	G CHANDRA SEKHAR NAIK	13	N
12.	14121A0485	KANIKARAM SAI PHANEENDRA	36	46
13.	14121A04B3	MADANA STANLEY	38	44
14.	14121A04B8	MARUPUDI RAMYA BHARATHI	33	45
15.	14121A04C0	MEDA REVANTH	32	45
16.	14121A04D3	NALLAPALLI MANVITHA CELESTINE	39	N
17.	14121A04D7	NUNE IZAZ	23	N
18.	14121A04F2	PATNAM MADHUSAI PAVAN	39	43
19.	14121A04G1	POLARAPU NIRANJAN	28	40
20.	14121A04G7	POTHUREDDYGANDAIAH SATEESH	25	N
21.	14121A04H2	PULICAL SREEKANTH	37	49
22.	14121A04H6	R KISHORE	23	48
23.	14121A04J6	SALMAN NAWAZ KHAN	36	N
24.	14121A04J9	SATRASALA SAI TEJA	37	N
25.	14121A04K0	SEELAM PRASANNA KUMAR REDDY	29	42
26.	14121A04K2	SHAIK AZHAR HUSSIAN	15	N
27.	14121A04L0	SILIVERU DIMPLE SUPRAJA	32	52
28.	14121A04M0	THATIGUTLA SATWIK KUMAR REDDY	31	N
29.	14121A04M3	THUMALA BHANU PRAKASH	11	N
30.	14121A04M6	TIRUVAIPATI BHARGAVNATH	18	N
31.	14121A04N1	UTTI RAJASREE	37	47
32.	14121A04P0	VELLARU DURGA PRASAD	27	42
33.	14121A04P5	YARRAGUNTLA VENKATNARYANA SUJAN BABU	34	N
34.	13121A04B4	NARAYANA VAISHNAVI	23	N
35.	15125A0405	BODDU SIREESHA	26	N
36.	15125A0412	DONAKONDA PRASANNA KUMAR REDDY	38	44
37.	15125A0413	DUDDUKURU CHINA RAMANAMMA	25	N
38.	15125A0418	GUDIPI SUDHAKAR	31	N

39.	15125A0423	KAMPA NAGENDARA	37	58
40.	15125A0428	MADDURI SWATHI PRIYADARSINI	29	46
41.	15125A0431	MOPURI SUNEETHA	23	N
42.	15125A0433	P NARESH	18	N
43.	15125A0436	PALLIBOINA SARITHA	39	46
44.	15125A0437	PERUMANELLORE ASHOK	28	N
45.	15125A0438	POLICHETTY NARESH	34	N
46.	15125A0443	SHAIK DADAGARI MOHAMMAD ZUBBAIR	32	N
47.	15125A0446	THALLA PRATHYUSH	35	48
48.	15125A0447	THEETLA VINAY KUMAR	25	N

Analysis of Reinforcement Outcomes:

Subject Code	Name of the Subject	No. of students Identified	No. of students benefited through Reinforcement
14BT3BS02	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS	39	18
14BT30232	NETWORK ANALYSIS	48	25

N: Not registered for the course

HOD, ECE

(vi) For advanced learners are advised to undertake literature survey and mini projects which subsequently help them in seminar and project courses - *Seminar schedules and List of student reports*

For advanced learners are advised to undertake literature survey and mini projects which subsequently help them in seminar and project courses.

Sample copy of ECE Department:

Category –Assignment

S. No.	Name of the faculty Mentor	Name of the subject(s)	Name of the Students	Title of the work	Class
1.	Dr. A. B. Yadav	Linea IC Applications	V Vandhana	Deriving and design of high order Active filter	III. B.Tech ECE
2.	Mr. R. Nagendra	Computer Networks	A Giridhar C Lithin C Mounika Challa raja	Simulation of Data Link Protocols	IV B.Tech ECE
3.	Mr. P. Madhu Kumar	Embedded and Real Time Systems	Amara Manasakumari	Traffic light controller – FSM and deployment	IV B.Tech ECE
4.	Mr. T. Krishna Murthy	Linear IC Applications	Pakyala Reshma Papasani Hari Priya R Naveen Tata Venkata Radhika Thalari Sravya	Applications of Op-Amp: Precision Current Mirror Simulated Inductor	III B.Tech ECE
5.	Mr. T. Ravikumar Naidu	DIP	IV B.T.ech, ECE	Simulation of Smoothening & Sharpening of a given image using MATLAB Simulation of Color image in various color models.	IV B.Tech ECE
6.	Mr. G. Guru Prasad	Computer Networks	Karnam Reddy Harithasree	Advantages of Ipv6 over Ipv4	IV B.Tech ECE
7.	Mr. Sai Raja Narendra	Digital Communications	U Eswar Sai Udatha Sivaniswecha Uppala Rishitha Uppu Raviteja	Assignment	III B.Tech ECE
8.		Analog Communications	II B.Tech ECE	Assignment	II B.Tech ECE

Category-Models

S. No.	Name of the faculty Mentor	Name of the subject(s)	Name of the Students	Title of the work	Class
1.	Dr. V. R. Anitha	MEMS	Gottigundala Sahithi J Deeraj Kumar G Surya Guduru Sai Mounika Gunda Narahari	Head Movement Controlled Wheel Chair using MEMS Accelerometer	IV B.Tech, ECE

2.	Dr. D. Leela Rani	Electronics	Chappidi Nikhileswar Reddy K V Monica Chittoor Muralidhar Sai Teja Gollalpalle Stephenraj	Periodically ON-OFF Mosquito Repellant	IV B.Tech, ECE
3.	Ms. K. Neelima	Embedded Systems	U Eswar Sai	Water Monitoring System for villages	III B.Tech ECE
4.	Ms. M. Bharathi	Embedded Systems	Gurka Hemasekhar Reddy	Automatic load controller with bidirectional visitors counter	IV B.Tech ECE
		Embedded Systems	Dwaracharla Phaneendra Reddy	IOT based Smart System	IV B.Tech ECE
5.	Ms. H. D. Praveena	Embedded Systems	I V Triveni K Sharanya	Unmanned Petrol Pump	III B.Tech, ECE
6.	Ms. K. Sudha	Electronics	Mareddy Sai Sree Manukonda Lasya	Smart energy saving system	III B.Tech ECE
7.	Mr. M. Naresh Babu	Communications	B Tharuni D Hareesh Kumar Aggarapu Sai Padma	IOT Based Air and Sound Pollution Monitoring System	IV B.Tech, ECE
8.	Mr. G. Naresh	Electronics	Shaik Mohammed Suhail Parangi Moulika	Automatic Detection of Unauthorised Parking Vehicle with SMS Notification	IV B.Tech, ECE
9.	Mr. P. V. Mahesh	Embedded systems	Thyuur Harsha Vardhan Konda Bhulakshmi Avula Subramanyam Leela Sri	IoT Based Liquid Level Monitoring System	IV B.Tech, ECE
10.	Mr. B. Mahesh	Embedded systems	Diddi Arunkumar K Sudhakar Gutti Reddy Arunkumar Reddy	Arduino Based Prepaid Billing System for Electricity through GSM	IV B.Tech, ECE
11.	Mr. M. Venkatanaresh	Embedded systems	Sompalli Mahitha T S Reddy Leela Sai	Automatic Railway Track Crack Fault Detection System	IV B.Tech, ECE
12.	Ms. V. Mounika Reddy	Electronic devices and circuits	B Praveen Kumar Reddy Devathi Mahesh	Application of Transistor:Design of Fire Alarm Circuit using transistors	III B.Tech, ECE

Category -Paper Presentation

S. No.	Name of the faculty Mentor	Name of the subject(s)	Name of the Students	Title of the work	Class
1.	Dr.P.Geetha	Nano Electronics	Mr.B.Vamsi Krishna Ms.Latha sree	CNTFET based Immuno sensor	III. B.Tech ECE
2.	Dr.P.Geetha	Nano Electronics	Ms. K.Supraja Ms. M.V.Padmini	Nano energy harvester	III. B.Tech ECE
3.	Dr. N. Vithyalakshmi	Image Processing Image Processing	Ms.P.Pooja Sai Ms.G.Pravalika	Loss less text data compression and decompression using novel simple byte coding technique	III. B.Tech ECE
4.	Mr. TVS Gowtham Prasad	Digital Signal Processing	Pooja Sai P R Asha R Naveen T Bhagya Sree T J Haritha	Applications and advantages of DSP Processors	III B.Tech ECE

Category -Mini-projects

S. No.	Name of the faculty Mentor	Name of the subject(s)	Name of the Students	Title of the work	Class
1.	Mr. K. Ramesh	Electronics	O Thippeswamy P Bhargav Theja	Panic Alarm	IV B.Tech, ECE
2.	Mr. G. Hemachandra	Electronics	Mudiam Supraja Murathoti Vinay Kotha Manish Kumar Reddy M S Angel Niharika	Cell Phone Detector Using Schottky Diode	IV B.Tech, ECE
3.	Ms. B. Gowthami	Analog IC Design	Anagallu Bhanurekha	Design of high speed And low power full adder in sub-threshold region	I M.Tech VLSI

Category –Simulation

S. No.	Name of the faculty Mentor	Name of the subject(s)	Name of the Students	Title of the work	Class
1.	Mr. Kaustubh Kumar Shukla	MEMS/NEMS	Kalyan Kumar M.Sunitha	Piezoelectric Based Device Modeling	III B.Tech ECE

(vii) For rural background students special classes in English communication, mathematics, computer programming and fundamental core courses are conducted to facilitate easy learning - *Schedule, List of students and attendance*

Special coaching sessions in English for rural background students:

Sample report:

Dr. P. Nageswara Rao, delivered a guest lecture to all the students of I B.Tech students on "Effective Communication Skills". The guest lecture was organised by Sree Vidyanikethan Engineering College specifically for first year B.Tech students for them to realize the significance of communication skills. The guest lecture by the eloquent expert was delivered on 21st November, 2016.

The lecture delved on nuances of effective communication and emphasized on mastering the skill of communication to be professional par excellence. The expert used simple language and disbursed lofty ideas worth emulating. The student-audience realized the importance of fundamentals like tenses and a keen interest towards this end was generated. A guest lecture of this grandeur was praiseworthy by the budding professionals.

Dr. P. Nageswararao delivering his lecture on 'Effective Communication Skills'

Students interacting with the expert

(viii) Special expert lectures on motivation by nationally renowned speakers for the first year students to facilitate them in goal setting and etiquette - *Reports of the events, students' attendance report and photographs*

Special expert lectures on motivation by nationally renowned speakers for the first year students to facilitate them in goal setting and etiquette.

Sample reports:

AN EVENING WITH SADHGURU

Sadhguru Jaggi Vasudev, the founder of Isha Foundation, a spiritual mystic gave an enlightening discourse amidst thousands of students, faculty, staff, earnest seekers of truth, and various print and electronic media in Sree Vidyanikethan International School on 4th January, 2016. Dr. M. Mohan Babu, Chairman of Sree Vidyanikethan Group of Educational Institutions felicitated Sadhguru and Miss Manchu Lakshmi Prasanna, actor-cum-producer expressed her gratitude for accepting the invitation and gracing the occasion.

Sadhguru's charismatic presence and his melodious voice charmed the audience resulting to bask in the perennial silence. His insightful discourse ranged from scientific explorations to the depths of the human consciousness. Some of the highlights of his talks and answers to the questions posed by the audience were:

- Technological innovations are human made and so they have the ultimate control over the machines to use it in a proper way.
- Our body is the sophisticated machine on the earth, everything is in one's self and he/she has to know to utilize it effectively.
- Humans make mistakes knowingly or by ignorance. To overcome the weaknesses, one has to practice meditation regularly and be in touch with one's self.
- If a person has the inclination to sharpen his senses and use the inherent tools appropriately, he/she can face the complexities of the world in a better way and emerge out as a successful being.
- It is not a magic or a miracle to achieve the greatest possibilities, but by sheer perseverance, if one focuses/gives his 100% on a particular thing at hand, he/she can definitely accomplish it.

The program came to an end with the Isha sounds and dance by the yoga guru, Jaggi Vasudev intoxicating the whole gamut of the gathering. The galaxy of the students, parents, faculty, staff and dignitaries present on and off the dais were blessed with his invincible spiritual vibe.

Sadhguru Jaggi Vasudev expounding the Law of the Universe

Students, Faculty, Staff and Earnest seekers of truth assembled to imbibe the

invaluable teachings of the God-Man

HOW TO SELL YOURSELF FOR A BETTER PRICE

Renowned writer and motivational speaker, Yandamoori Veerendranath inspired the I B.Tech students in his guest lecture on "How to Sell Yourself for a Better Price" on 30th August, 2017. His lectures in both the sessions addressed various issues pertaining to their academics and lifestyle.

The interactive sessions with his amusing charm entertained the students while driving home the lessons to be learnt. Mr. Yandamoori Veerendranath started his session with a thought provoking definition of success and differentiated 'watchers' from 'performers'. Students were encouraged to build up confidence, kick-start their innate potential and take up a positive approach to life.

His lecture included various methods to develop intelligence, quick reflex actions, expressing emotions appropriately and refurbishing enthusiasm towards life. All the first

B.Tech students were happy to be a part of the lecture and they are sure to put into practice the lessons learnt in the session.

Students participating in an activity on self-worth

The Student participants of the Guest Lecture

(ix) Fresher's day to introduce I year students with senior students - *Report of the event and photographs*

Fresher's Day 2015:

Freshers' Day of Sree Vidyanikethan Educational Institutions was celebrated on 1st September, 2015 in the presence of the Chief Guest, Dr. M. Mohan Babu, Hon"ble Chairman, and the Guests of Honour: Dr. Ali, Ms. Ramya Krishna, Mr. Naresh, Ms. Poorna, Mr. Varun Sandesh, Ms. Sonia, Mr. Krishna Bhagavan, Mr. Jeeva, and other popular film artists. Prof. T. Gopala Rao, Special Officer, Sree Vidyanikethan Educational Trust and other distinguished invitees also graced the occasion. The Freshers' Day celebrations reached a crescendo with exhilarating cultural items presented by the students.

Dr. M. Mohan Babu, Hon'ble Chairman,
Sree Vidyanikethan Educational Trust addressing the jubilant students

Ms. Ramya Krishna, popular film artiste enlightening the students

(x) Senior students taking anti ragging oath and written undertaking - *Template of anti ragging oath and photographs*

Report of anti ragging oath:

Anti-Ragging pledge by all students of SVET Institutions was administered by Dr. M. Mohan Babu, Hon"ble Chairman, SVET in connection with Anti-ragging Awareness Programme held on 26th August, 2015. In his brief address, Hon"ble Chairman advised the students to treat the fellow students with compassion, love and affection.

Chairman administering anti-ragging pledge

Scores of students taking the pledge

2.3.1

Student centric methods, such as experiential learning, participative learning and problem solving methodologies are used for enhancing learning experiences.

2. Evidences for QIM metric 2.3.1:

Experiential learning

(i) **Teaching courses with models, simulation and prototypes shall contribute to students experience in learning - *Teaching plan showing tools used***

(ii) **Concepts at the threshold and beyond of the syllabus are given as self learning topics to the students through the teaching plan - *Teaching plan showing self learning concepts***

(iii) **Seminars and technical association activities organized for the students to explore new paradigms in the domain and cross domain - *List of seminars and Technical association activities***

(iv) **Students are provided with Internships and Industrial Tours/visits as a part of Experiential learning - *List of Internships and Industrial Tours/Visits***

Participative Learning

(v) **Labs, seminar and project as a group work is designed to foster student peer and participative learning - *Team formation method***

(vi) **Participation of the student by giving feedback on teaching quality - *Feedback reports on teaching quality - analysis and impact***

(vii) **Student survey on Program Educational Objectives, Program Outcomes and Curriculum - *Survey reports- analysis and impact***

Courses with Problem Solving Skills

(viii) **Core, professional electives and a few of open electives is designed to develop the problem solving skills among the students - *List of courses with problem solving skills***

Summary of Activities under 2.3.1

Academic Year	Experiential/ Self Learning				Experiential/ Participative Learning	Participative Learning				Problem Solving Methodologies
	No. of Courses with Self learning Topics	*Student Seminars*	*Technical Association Activities for Self Learning*	*Models/ Simulations etc., used for Experiential Learning*	*Project Work Batches*	*Student Participations in Surveys*			*Student Feedback collected on teaching Quality for the offered courses*	*No. of Courses with Problem Solving skill as one Course Outcome*
						PEOs	*POs*	*Curriculum*		
2017-18	450	1537	53	411	334	1902	1902	2497	513	404
2016-17	445	507	50	367	294	1779	1779	2401	522	403
2015-16	379	1306	47	347	257	1398	1398	1765	452	356
2014-15	368	1068	36	314	222	1051	1051	1122	440	342
2013-14	359	939	36	280	189	667	667	772	423	315
2012-13	328	761	24	242	155	477	477	728	362	242

Experiential learning

(i) **Teaching courses with models, simulation and prototypes shall contribute to students experience in learning - *Teaching plan showing tools used***

(ii) **Concepts at the threshold and beyond of the syllabus are given as self learning topics to the students through the teaching plan - *Teaching plan showing self learning concepts***

(iii) **Seminars and technical association activities organized for the students to explore new paradigms in the domain and cross domain - *List of seminars and Technical association activities***

(iv) **Students are provided with Internships and Industrial Tours/visits as a part of Experiential learning - *List of Internships and Industrial Tours/Visits***

i) Teaching courses with models, simulation and prototypes shall contribute to students experience in learning

Sample copy of ECE Department

S. No.	Developed by(Name of the Faculty member)	Name of the Models, Simulations and Prototypes	Topic
1.	Dr. P. Venkata Ramanana	OPTsim & MODsys Simulation	Simulations of Eye Pattern/Eye Diagram for calculations of dispersions in Optical Fibers
2.	Dr. A. B. Yadav	Demonstration	MOSFET working with the tap and tank model where source represented source tank, drain as the sink and tap as gate
3.	Mr. S. Jaya Krishna	Circuit Simulator (A Java Applet which simulate any electronic circuit))	Network Analysis
4.	Ms. C. Maheswari	MAT Lab Programs through simulink	Generation of signals using Matlab simulink
5.	Mr. G. Guru Prasad	Simulation Model	Simulation of shortest path routing algorithm
6.	Mr. Sai Rajanarendra	Microwave components	Demonstration of Microwave Components
7.	Mr. P. V. Mahesh	OMAP L138 Emulator kit	Simulation of Digital signal processing programmes
8.	Mr. K.Ramesh	Circuit Simulator (A Java Applet which simulate any electronic circuit))	Network Analysis
9.	Dr. G. Arul Elango	Montecarlo simulation of Neyman Pearson criteria in GPS Satellite Signal acquisition under AWGN (Probability of detection vs probability of false alarm)	Neyman Person criteria –Binary decisions
10.	Mr. G. Hemachandra	EDA Tools	Recent Advancements in VLSI Technology and Design
11.	Dr. V. V. Satyanarayana Tallapragada	Simulation Model	Simulation of Distance Vector routing algorithm
12.	Ms. B. Gowthami	Prototype	IOT based garbage monitoring system for smart city
13.	Mr. V. M. S. N. Pavan Kumar Ch.	MAT Lab Simulink based Simulation	Design of LPF and HPF and analysis of amplifiers
		MAT Lab based Simulation	Simulation of Electronic circuits using Matlab
14.	Dr. D. Leela Rani	MAT Lab Program through simulink	Design of All pass filter
15.	Mr. B. Mahesh	PSPICE Software	Design of Filters using PSPICE
16.	Ms. G. Komala Yadav	RS232 Cable (Model)	Demonstration of RS232
17.	Dr. A. B. Yadav	Demonstration	Red light transmission through falling water
18.	Dr. N. Ashok Kumar	Electronic Measurement Components	Demonstration of Microwave Components
19.	Mr. T. Krishna Moorthy	Low power digital cell library	Demonstration of FPGA kit
20.	Mr. T. Ravi Kumar Naidu	MAT Lab Programs through simulink	Generation of signals using Matlab simulink
21.		OMAP L138 Emulator kit	Simulation of Digital signal processing programmes
22.	Mr. T. V. S. Gowtham Prasad	OMAP L138 Emulator kit	Simulation of Digital signal processing programmes
23.	Mr. P. V. S. R.	Simulated Model	Casode Amplifier

S. No.	Developed by(Name of the Faculty member)	Name of the Models, Simulations and Prototypes	Topic
24.	Bharadwaja	Simulated Model	Common Emitter Amplifier
25.	Ms. V. Mounika Reddy	Simulation model	Demonstration on simulink
26.	Ms. V. Meenakshi	Simulation model	Simulation of electronic circuits in multisim
27.	Mr. Argha Sarkar	Animation model for better understanding of OSI model and responsibilities	OSI model and responsibilities
28.	Dr. N. Ashok Kumar	Electronic Measurement Components	Demonstration of Microwave Components
29.	Mr. P. Madhu Kumar	Embedded Systems	Usage of Code Composer Studio with MSP430G2553 Launch pad for embedded system Design
30.	Dr. V. R. Anitha	Prototype	Prototype Metamaterial Antenna for Satellite Applications
31.	Mr. C. V. Sudhakar	Proto Type Model Presentation	DC Voltmeter, Ammeter & Multimeter
32.	Dr. P. Geetha	PDC Lab	Simulation of Analog Circuits
33.	Mr. K. K. Shukla	COMSOL Software	Gas sensor
34.	Dr. P. Nagarajan	Microwind EDA tool	Design and evaluation of digital cmos circuits
35.	Dr. N. Vithyalakshmi	Microwind EDA tool	Layout design for CMOS inverter

SREE VIDYANIKETHAN ENGINEERING COLLEGE
(Autonomous)
Sree Sainath Nagar, A. Rangampet – 517 102

Department of Electronics & Communication Engineering

Lesson Plan cum Diary 2016-'17

Name of the Subject: **Antennas & Propagation (14BT50401)**
Name of the faculty Member**: Ms. K.Sudha**
Class & Semester**: III B.Tech I Semester**
Section: **ECE- B&C**

S. No	Topic	No. of Periods required	Date(s) covered	No. of periods used	Book(s) followed	Remarks
UNIT-I: ANTENNA BASICS AND THIN LINEAR WIRE ANTENNAS						
1.	Introduction	1	1|7|16	1	T1	
2.	Basic Antenna Parameters- Patterns, Beam Area, Radiation Intensity, Beam Efficiency	1	5|7|16	1	T1	PPT
3.	Directivity-Gain-Resolution	1	7|7|16	1	T1	
4.	Tutorial - I	1	8|7|16	1		
5.	Antenna Apertures, Effective Height	1	12|7|16	1	T1	
6.	Antenna Field Zones, Polarization	1	13|7|16	1	T1	
7.	Radiation from Small Electric Dipole, Quarter Wave Monopole and Half Wave Dipole - Current Distributions, Field Components	1	14|7|16	1	T1	NPTEL
8.	Tutorial - II	1	15|7|16	1		
9.	Radiated Power, Radiation Resistance, Beam Width, Directivity, Effective Area and Effective Height	1	19|7|16	1	T1	
10	Natural current distributions, far fields and patterns of Thin Linear Center-fed Antennas of different lengths.	2	20|7|16 21|7|16	2	T1	
11	Tutorial - III	1	22|7|16	1		
12	Formative Test - I	1	26|7|16	1		
Total of periods required:		**13**	Total of periods used:			13
UNIT-II: ANTENNA ARRAYS						
13	Point sources - Definition, Patterns, arrays of 2 Isotropic sources- Different cases	2	27|7|16 28|7|16	2	T1	NPTEL
14	Tutorial - IV	1	29|7|16	1		
15	Principle of Pattern Multiplication, Uniform Linear Arrays – Broadside Arrays	2	3|8|16 4|8|16	2	T1	NPTEL
16	End Fire Arrays, EFA with Increased Directivity	1	5|8|16	1	T1	NPTEL
17	Tutorial - V	1	9|8|16	1		
18	Derivation of their characteristics	1	10|8|16	1	T1	

S. No	Topic	No. of Periods required	Date(s) covered	No. of periods used	Book(s) followed	Remarks
	and comparison					
19	BSA with Non-uniform Amplitude Distribution - General considerations and Bionomial Arrays, Illustrative Problems	2	11/8/16 12/8/16	2	T1	
20	Tutorial - VI	1	16/8/16	1		
21	Formative Test - II	1	17/8/16	1		
	Total of periods required:	**12**	Total of periods used:			12
	UNIT-III: VHF, UHF AND MICROWAVE ANTENNAS					
22	Arrays with Parasitic Elements, Yagi - Uda Arrays, Folded Dipoles & their characteristics	2	18/8/16 19/8/16	2	T1	PPT, NPTEL
23	Tutorial - VII	1	31/8/16	1		
24	Helical Antennas - Helical Geometry, Helix modes, Practical Design considerations for Monofilar Helical Antenna in Axial and Normal Modes	1	1/9/16	1	T1	PPT, NPTEL
25	Horn Antenna	1	2/9/16	1	T1	PPT, NPTEL
26	Tutorial - VIII	1	6/9/16	1		
27	Micro strip Antennas- Introduction, features, advantages and limitations	1	7/9/16	1	T1	PPT, NPTEL
28	Rectangular patch antennas – Geometry and parameters, characteristics of Micro strip antennas	1	8/9/16	1	T1	PPT, NPTEL e-journal [IEEE]
29	Impact of different parameters on characteristics	1	9/9/16	1	T1	
30	Tutorial – IX	1	14/9/16	1		
31	Reflector Antennas- Introduction, Flat Sheet and Corner Reflectors	1	15/9/16	1	T1	PPT, NPTEL
32	Paraboloidal Reflectors - geometry, pattern characteristics, Feed Methods, Reflector types	1	16/9/16	1	T1	PPT, NPTEL
33	Tutorial – X	1	20/9/16	1		
34	Formative Test-III	1	21/9/16	1		
	Total of periods required:	**14**	Total of periods used:			14
	UNIT –IV: ANTENNA MEASUREMENTS					
35	**Antenna Measurements:** Introduction, Concepts- Reciprocity, Near and Far Fields, Coordination system	2	22/9/16 23/9/16	2	T1	PPT
36	Tutorial – XI	1	24/9/16	1		
37	Sources of Errors, Patterns to be Measured, Pattern Measurement, Arrangement	1	27/9/16	1	T1	
38	Directivity Measurement, Gain Measurements (by comparison, Absolute and 3-Antenna Methods).	1	28/9/16	1	T1	PPT e-book

S. No	Topic	No. of Periods required	Date(s) covered	No. of periods used	Book(s) followed	Remarks
39	Formative Test-IV	1	29/9/16	1		
40	Tutorial – XII	1	30/9/16			
Total of periods required:		07	Total of periods used:			07
UNIT –V: WAVE PROPAGATION						
41	Ground wave propagation	1	4/10/16	1	R2	PPT
42	Space wave propagation - Introduction, field strength variation with distance and height, Effect of earth's curvature, Absorption	1	13/10/16	1	R2	PPT
43	Super refraction, M-curves and duct propagation, scattering phenomena	1	14/10/16	1	R2	e-Book.
44	Tutorial - XIII	1	18/10/16	1		
45	Tropospheric propagation, fading and path loss calculations	1	19/10/16	1	R2	
46	Sky wave propagation- Introduction, structure of Ionosphere	1	20/10/16	1	R2	PPT
47	Refraction and Reflection of sky waves by Ionosphere, Ray path, Critical frequency,	1	21/10/16	1	R2	
48	Tutorial XIV	1	22/10/16	1		
49	MUF, LUF, OF, Virtual height and Skip distance	1	25/10/16	1	R2	e-Book
50	Relation between MUF and Skip distance, Multi-HOP propagation	1	27/10/16	1	R2	
51	Formative - V	1	28/10/16	1		
Total of periods required:		11	Total of periods used:			11
Grand total of periods required		57	**Grand total of periods used:**			57

1/11/2016, 2/11/2016 & 3/11/2016, 4/11/2016 → Revision and previous year question papers discussion.

TEXT BOOKS:

T1. John D. Kraus and Ronald J. Marhefka and Ahmad S.Khan, *Antennas for all Applications*, TMH, 3rd Edition, 2006.

T2. E.C.Jordon and K.G.Balmain, *Electromagnetic Waves and Radiating Systems*, 2nd Edition, PHI, 2000.

REFERENCE BOOKS:

R1. C.A. Balanis, *Antenna Theory*, John Wiley & Sons, 2nd Edition 1982.
R2. E.V.D. Glazier and H.R.L. Lamont, *The Services Text Book of Radio, Transmission and Propagation*, Vol.5, Standard Publishers Distributors, Delhi.
R3. G.S.N.Raju, *Antennas and Wave Propagation*, Pearson Education India, 1st Edition, 2006.

Faculty Member **Head of the Department**

ii) Concepts at the threshold and beyond of the syllabus are given as self learning topics to the students through the teaching plan.

Sample Copy for the course Switching Theory and Logic Design

SREE VIDYANIKETHAN ENGINEERING COLLEGE
(Autonomous)
Sree Sainath Nagar, Tirupati – 517 102

Department of Electrical & Electronics Engineering

LESSON PLAN

Subject: Switching Theory And Logic Design
Code : 10BT40404
Class : II B.Tech. I-Semester EEE-B

Teacher : Mr .P. Venkatesh
Assistant Professor

Academic Year: 2013 - 2014

Total Hours :72

S.No	Topic	No. of Lecture Periods	Topics for self study
	Unit-I **Number System & Codes**		
1.1	Philosophy of number systems	1	ASCII Character Codes, Digital Logic Families
1.2	Complement representation of negative numbers	2	
1.3	Binary arithmetic	2	
1.4	Binary codes	2	
1.5	Error detecting & Error correcting codes	1	
1.6	Hamming Code	2	
	Total Hours	**10**	
	Unit-II **Boolean Algebra and Switching Functions**		
2.1	Fundamental postulates of Boolean Algebra	1	Logic gates-applications
2.2	Basic Theorems and properties	2	
2.3	Switching functions	1	
2.4	Canonical and standard forms, algebraic simplification	2	
2.5	Digital logic gates properties of X-OR gates, universal gates, multilevel NAND/NOR realizations	2	
	Total Hours	**08**	
	Unit-III **Minimization of Switching Functions**		
3.1	Map method, Prime implicants	2	Six variable K-Map
3.2	Don't care combinations, Minimal SOP & POS forms	2	
3.3	Tabular method	2	
3.4	Prime- Implicant chart, simplification rules	2	
	Total Hours	**08**	
	Unit-IV **Combinational Logic Design**		
4.1	Design using conventional logic gates, Binary Adders, Subtractors, Look ahead carry generator	3	Priority Encoders
4.2	Decimal Adder, BCD Adder ,Binary Multiplier	2	
4.3	Modular design using IC Chips – Magnitude Comparator	2	
4.4	Multiplexer- MUX Realization of switching functions, Demultiplexer ,parity bit generator	2	
4.5	Code converters, Hazards and Hazard free realizations	2	
	Total Hours	**11**	

S.No	Topic	No. of Lecture Periods	Topics for self study
	Unit-V **Programmable Logic Devices, Threshold Logic**		
5.1	Basic PLD's – ROM, PROM, PLA, PAL Realization of switching functions using PLD's	3	FPGA
5.2	Capabilities and limitations of threshold gate	1	
5.3	Synthesis of threshold functions	2	
5.4	Multi-gate synthesis	2	
	Total Hours	**08**	
	Unit-VI **Sequential Circuits-I**		
6.1	Classification of sequential circuits (synchronous Asynchronous, pulse mode, level mode with Examples)	3	FSM ,Conversion of Mealy and Moore Circuits
6.2	Basic flip flops, triggering & excitation tables	2	
6.3	Steps in synchronous sequential circuit design	2	
6.4	Design of synchronous counters - modulo – N ,ring , shift & Johnson counters	3	
6.5	Design of synchronous counters ,Serial binary adder, sequence detector	2	
	Total Hours	**12**	
	Unit-VII **Sequential Circuits-II**		
7.1	Finite state machine-capabilities and limitations	1	Minimal cover table from merger chart and graph methods.
7.2	Mealy and more models minimization of completely specified and incompletely specified sequential machines	2	
7.3	Partition techniques-with an example	2	
7.4	Merger chart and Merger graph methods	2	
	Total Hours	**07**	
	Unit-VIII **Algorithmic State Machines**		
8.1	Salient features of the ASM chart – simple Examples	1	ASM chart for electronics equipment.
8.2	System design using data path and control subsystems	2	
8.3	Control implementations	2	
8.4	Examples of weighing machine and Binary multiplier	3	
	Total Hours	**08**	

Text Books

Units – I – VI, VIII - 1. Digital Design-**Morris Mano,** Third Edition

Units – I, IV & VII - 1. Switching & Finite Automata Theory-**Zvi Kohavi,** Second Edition

References

Units – I to VIII

1. An Engineering Approach To Digital Design-Fletcher
2. Fundamentals of Logic Design-Charles H.Routh

T.N.Prasad

Head of the Department

Faculty Member

iii) Seminars and technical association activities organized for the students to explore new paradigms in the domain and cross domain

Sample Copy of ECE Department

a) Seminar

SREE VIDYANIKETHAN ENGINEERING COLLEGE
(Autonomous)
Sree Sainath Nagar, A. Rangampet – 517 102
Department of Electronics and Communication Engineering

SVEC/ECE/16/2013-14 28-04-2014

SEMINAR TITLES

III B. Tech. II- Sem, ECE-A, (Academic Year: 2013-14)

Sl. No.	Roll. No.	Name of the Student	Seminar Titles	Name of the Supervisor
1.	11121A0401	AAVULA YAMUNA	Pebble Bed Reactor	Prof. P.V. Ramana
2.	11121A0402	AMBITI CHINMAYI	Magnetic Random Access Memory	
3.	11121A0403	ARAVA ROJA	Digital Smell	
4.	11121A0404	AVVARU SRAVANTHI	Addiction Avoider using Embedded Systems	
5.	11121A0405	BACHHU CHANDANA ARCHITA	Accurate Microvolt Biomedical Measurements Using Oscilloscopes	
6.	11121A0406	BANDI SARANYA	Quadcopter Video Surveillance UAV	
7.	11121A0407	BATCHU SRAVANI	Securing Under Water Wireless Communication Networks	Dr. V. R. Anitha
8.	11121A0408	BAYANABOINA VENKATA SUBBAIAH	Design of Static Demultiplexing for Digital-TV	
9.	11121A0409	BEERAM APARNA	Humanoid Robotics	
10.	11121A0410	BENGULORE BHARATH KUMAR	Nuclear Batteries-Daintiest Dynamos	
11.	11121A0411	BHAGYASREE K S	Microcontroller Based Anesthesia Injector	
12.	11121A0412	BOGATHI JYOTHSNA	Micro Electronic Pill	
13.	11121A0413	BORUGULA ASHRAF	Black Box	Prof. R. S. Rao
14.	11121A0414	BUKKE PAVANKUMAR NAIK	3Dimensional Face Recognition	
15.	11121A0415	C JAYASRISAI	Pill Camera	
16.	11121A0416	C SIVA KESAVA MURTHY	ARM9 Based Real Time Vehicle Theft Identity and Control System	
17.	11121A0417	CHEEMALAPATI UDAYA PALLAVI	Phonesat	
18.	11121A0418	CHIGICHERLA SWARNALATHA	Cancer NANO Technology	
19.	11121A0419	D ANUSHA	Convergence of Electronics with Fabrics	Dr. N. Padmaja
20.	11121A0420	DASARAJU AKARSHA	Swarm Robotics	
21.	11121A0421	DEVARAYAPALLI RADHIKA	Sixth Sense Technology	
22.	11121A0422	DHAGUDU VINAY KUMAR	Sniffer Technology	
23.	11121A0423	EARLA ARUN KUMAR	Internet Protocol Television	
24.	11121A0424	EDALAPATI KUMAR	Speed Detection of Moving Vehicles using Speed Camera	
25.	11121A0425	G DHEERAJ	Mobile Processors 101	Ms. D. Leela Rani
26.	11121A0426	G SNEHA	HELIODISPLAY	
27.	11121A0427	G BAVAREDDY PRATHYUSHA	STRATELLITE	
28.	11121A0428	GANNAMANTHI CHENNA KESAVA	A Smart Camera for Traffic Surveillance	
29.	11121A0429	GM CHANDRA KISHORE	3D-Printing	
30.	11121A0430	GONTLA SAHITHYA LAKSHMI	Artificial Retina using Thin Film Transistor Driven by Wireless Power Supply	

Sl. No.	Roll. No.	Name of the Student	Seminar Titles	Name of the Supervisor
31.	11121A0431	GORREPATI USHASREE	TV Remote using as a Cordless Mouse for the Computer	Mr. R. Nagendra
32.	11121A0432	GOVINDU SAISREE	BIONIC EYES	
33.	11121A0433	GOVINDU VINOD KUMAR	Blue EYES Technology	
34.	11121A0434	GUNDLA SREE CHARAN	Submarine Communication	
35.	11121A0435	HARIJANA MANOHAR	Intelligent Wireless Video Camera	
36.	11121A0436	INAKOTA SAI ASLEASHA	Obstacles Avoiding ROBOT	
37.	11121A0437	JYOTHI ASHOK REDDY	MEMRISTOR	Mr. M. Naresh Babu
38.	11121A0438	K B NAGESWAR REDDY	WI-VI	
39.	11121A0439	K BHANU PRAKASH	Mobile Operated SPY ROBOT	
40.	11121A0440	K RAJESH	Wireless Electricity	
41.	11121A0441	KANCHAM KESUVULU REDDY	BAC Start Engine	
42.	11121A0442	KASI SAI SEKHAR	Earthquake and Tsunami Alert System by Iridium Satellite System	
43.	11121A0443	KUMMARA SRAVANTHI	NANO Defense	Mr. P. Madhu Kumar
44.	11121A0444	KUNDARAPU SUBBARYUDU	Biometric Voting System	
45.	11121A0445	KUPPACHI PAVAN KUMAR	Voice Interactive Computer Systems	
46.	11121A0446	L ADITYA REDDY	Electro Mechanical Human Computer Interaction	
47.	11121A0447	L GUNADEEP	On-Line Electric Vehicle	
48.	11121A0448	L SUNEENDRA KUMAR REDDY	Number Plate Recognition	
49.	11121A0449	M BHAVATEJA REDDY	Wireless Charging of Mobile Phones using Microwaves	Mr. K. Ramesh
50.	11121A0450	M SANKAR	3D TRI-GATE Transistors	
51.	11121A0451	M V SENTHIL KUMAR	Microcontroller Based Heart Rate Monitoring System	
52.	11121A0452	MAJJARI PRANEETH	Smart Grid Technology	
53.	11121A0453	MALAPATI ROJA	Solar Mobile Charging	
54.	11121A0454	MALEYMALPURAM MOUNIKA	DNA Computing	
55.	11121A0455	MARAY VENKATA SUBBAIAH	Border Security using WINS (Wireless Integrated Network Sensors)	Mr. A. Nagaraju
56.	11121A0456	MARRI PAVANI	Laser Imaging Detection and Ranging	
57.	11121A0457	MARUJOLLA MANIKANTA	Free Space Laser Communications	
58.	11121A0458	MARUPURI REVANTH CHOWDARY	On Chip Optical Communication using Graphene	
59.	11121A0459	MUDDA RAKESHKUMAR REDDY	ANTI Collision System	
60.	11121A0460	MUJAHID SHEIK	Wavelet-based Feature Extraction for DNA Microarray Classification	
61.	11121A0461	MULE ABHI ROOP	Detection of Concealed Weapons using Image Processing	Ms. G. Madhavi Latha
62.	11121A0462	MULLANGI BHARGAVI	Digital Watermarking	
63.	11121A0463	MUMMINENI LAKSHMI DURGA	Swarm Intelligence and Swarm Robotics	
64.	11121A0464	MUTTINENI MUNIKISHORE	OVONIC Unified Memory	
65.	11121A0465	N DINESH REDDY	Vehicular Communication	
66.	11121A0466	N SIREESHA	Fire Fighting ROBOT	
67.	11121A0467	N SUKESH	Transparent Electronics	Mr. M. Sivasubramanayam
68.	11121A0468	N V RADHA	Electroculographic Guidance of a Wheel Chair using EYE Movement Codification	
69.	11121A0469	P BHANUSHREE	General Packet Radio Service (GPRS)	
70.	11121A0470	P H SUMANA	ZIGBEE Technology	

HOD, ECE

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Department of Electronics and Communication Engineering

SVEC/ECE/16/2013-14 28-04-2014

SEMINAR TITLES

III B. Tech. II- Sem, ECE-B, (Academic Year: 2013-14)

Sl. No.	Roll. No.	Name of the Student	Seminar Titles	Name of the Supervisor
1.	11121A0471	P HIMA BINDU	Motion Sensors	Ms. H. D. Praveena
2.	11121A0472	PADMANABHAM VENKATESH	Electronic Fuel Injection System	
3.	11121A0473	PALANI LAKSHMI PRASANNA	High Performance Memory Systems Using 3D ICS	
4.	11121A0474	PALLI SAMYUKTHA	3D Holographic Projection Technology	
5.	11121A0475	PAPIREDDY BHAVANA	Design and Construction of Automatic Solar Tracker	
6.	11121A0477	PATURU CHATHURA	iBOT	
7.	11121A0478	PITTA VENKATA VAMSI	EM BOMB	Mr. T. Krishna Murthy
8.	11121A0479	POLAVARAM DHAMINI	Silent Sound Technology	
9.	11121A0480	POLU SUDHEER KUMAR REDDY	Automatic Control Of Unmanned Rail Gate	
10.	11121A0481	POLUKA SRILATHA	Self Destructing Electronics	
11.	11121A0482	POLURU VISMAYA	Multi Sensor System for Obstacle Detection in Train Applications	
12.	11121A0483	PUTHA LAKSHMI BALAJI	Securing Under Water Wireless Communication Networks	
13.	11121A0484	R V SRINIVASAN	Graphene-Key to our Future	Mr. T. Ravikumar Naidu
14.	11121A0485	RAMAVATH SHILPA	NANO Solar Cells	
15.	11121A0486	RAMIREDDY ANUDEEP REDDY	Railway Communications	
16.	11121A0487	RAMIREDDY DEVANATHA REDDY	Clockless Chips	
17.	11121A0488	RAPALLI VANI	Brain Computer Interface	
18.	11121A0489	RASHMI N	Smart Card	
19.	11121A0490	RS SANDEEP	Google Glasses	Ms. K. Sudha
20.	11121A0491	S CHETAN	Carrier Aggregation In LTE-Advanced	
21.	11121A0492	S KOUSAR TAJ	FEMTOCELL Technology	
22.	11121A0494	SANDHYA N	Pendrive to Pendrive Data Transfer using ARM	
23.	11121A0495	SANGEETHAM HEMANTH KUMAR	WITRICITY	
24.	11121A0496	SHAIK ABDUL NADEEM	ATM with an EYE	
25.	11121A0497	SHAIK MOHAMMAD FARUKH	Mobile Phone Cloning	Mr. C. Venkata Sudakar
26.	11121A0498	SINGANAMALLA LALITHA DEVI	Solar Tree	
27.	11121A0499	S SREEKRISHNA YADAV	Quantum Computing	
28.	11121A04A0	SIVAKAVI NAVEEN KUMAR	Brain GATE	
29.	11121A04A1	SOMA RAMYA VANI	HONEYPOTS	
30.	11121A04A2	SYED NAZEER AHMED	Space Teleportation Using Quatanum Teleportation	
31.	11121A04A3	T CHAITHANYA DILEEP	Sensors and its Applications	Ms. P. Padmaja
32.	11121A04A4	T HARSHITHA	Sniffers to Detect Lost Mobiles	
33.	11121A04A5	T S RAJESH	3D Printing	
34.	11121A04A6	TAKKOLI AMULYA REDDY	3D Internet	
35.	11121A04A7	T ANANTHA KALYAN REDDY	Solar Powered Electric Aircraft	
36.	11121A04A8	T DEEPAK PAVAN KUMAR	Power Generation From Speed Breakers	

Sl. No.	Roll. No.	Name of the Student	Seminar Titles	Name of the Supervisor
37.	11121A04A9	T RAJEEVKUMAR REDDY	Heterogenous System Architecture	Mr. T. Ravisekhar
38.	11121A04B0	THOTLAPALEPU ROYAL MONICA	Stealth Technology	
39.	11121A04B1	THUPAKULA CHANDU	Green Gadgets	
40.	11121A04B2	T MADHUSUDHAN REDDY	MEMS Technology	
41.	11121A04B3	TIRUVEEDHULA SARANYA	Agriculture Robots	
42.	11121A04B4	UNDELA SETHUVARDHAN REDDY	Digital Signature	
43.	11121A04B5	V DARSHAN	Imouse: An Integrated Mobile Surveillance and Wireless Sensor System	Mr. G. Guru Prasad
44.	11121A04B6	VELURI NAVEEN	Human Robot Interaction using Gesture Identification	
45.	11121A04B7	VANGA VENKATA APARNA	Hyperspectral Imaging	
46.	11121A04B8	VINAY KUMAR B S	Fractal Antennas	
47.	11121A04B9	VULAVA POORNACHANDRA RAO	Tongue Controlled Wheel Chair	
48.	11121A04C0	Y DIG VIJAY KUMAR	IGZO Technology	
49.	12125A0401	AVULA VEMALAKRISHNA	Bio-Metric Security System	Mr. K. V. Rajendra Prasad
50.	12125A0402	BATHALA POORNIMA	An Embedded System for Automated Adaptive Traffic Signalling	
51.	12125A0403	BOLLAPU ANILKUMAR GOUD	Signal Jammer	
52.	12125A0404	BUKYA SIVA SANKAR NAIK	Density Based Traffic Signal System	
53.	12125A0405	C SIVA SANKARA PRASAD	Microcontroller Based Automatic Railway GATE Control	
54.	12125A0406	DUTHALURU VEERAIAH	Solar Sailing	
55.	12125A0407	GADE NAVEEN KUMAR	Aeronautical Communication	Mr. G. Naresh
56.	12125A0408	GALIVETTI PUNYAVATHI	Mobile Train Radio Communication	
57.	12125A0409	GOPINI GOPI CHAND	Ambient Backscatter	
58.	12125A0410	I PHANEENDRA NATH	3D Password	
59.	12125A0411	KALICHERLA HIMABINDU	Heliodisplay	
60.	12125A0412	M LAKSHMI KUMARI	A Vehicle to Vehicle Communication Protocol for Co-Operative Collision Warning	
61.	12125A0413	MAHENDRAKAR REVATHI	GI-FI Technology	Mr. T.V.S. Gowtham Prasad
62.	12125A0414	N JAYA SAI SRINIVAS	Wireless Charging of Mobile Phones Using Microwaves	
63.	12125A0415	N NAVEENA DURGA BHARGAVI	Telehealth Systems	
64.	12125A0416	POREDDY LAKSHMI REDDY	HAWK EYE Technology	
65.	12125A0417	RAMAVATH NIRMALA BAI	Medical Mirror	
66.	12125A0418	S SALIHA	LEAP Motion	
67.	12125A0419	SAM JABADURAI D	Attendance Maintaining Using RFID	Mr. A. Nagaraju
68.	12125A0420	ULCHALA SAMULU	Google Wave	
69.	12125A0421	VADDI HAREESH	Intervehicle Communication	
70.	12125A0422	VANGAPATTU KIRAN KUMAR	Internet of Things (IoT)	
71.	12125A0423	VUTUKURU VENKATA GOWRI	Mobile High Definition Link	
72.	12125A0424	H N SUNITHA	Robotic Surgery	
73.	10121A0407	ANIL KAIVALYA NEMANI	A DSP Based On-Line UPS	Mr. K.V. Koteswara Rao
74.	10121A0414	BADDELA VENKATESH	mobile high definition link(mhl)	
75.	10121A0475	MOOGATI SHABARI	robotics in surgery	

HOD, ECE

b) Summary of Co-Curricular activities under ACME (ECE Student Technical Association for the Academic Year 2016-`17

Sl No	**Name of the Event**	**Dates Organized**	**No. of Students Participated**
1.	Paper Presentations	09-08-2016 To 11-08-2016	94
2.	Block Out	12-08-2016	144
3.	Debate	13-08-2016	84
4.	Paper Presentations	13-09-2016 to 15-09-2016	58
5.	Expert Lecture on IOT	28-10-2016	600
6.	Foundo Hunt	27-01-2017	52
7.	ABC In Ardiuno	10-02-2017 to 15-02-2017	196
8.	Guest Lecture on "Career Guidance" by Dinesh Gutha	27-04-2017	194
9.	Group Discussions	10-02-2017 and 11-02-2017	272
10.	Mock Exam	12-02-2017	20
11.	Mock Interviews	13-02-2017 to 15-02-2017	125
12.	Add on Course on Layouts	14-03-2017 to 15-03-2017	118
13.	ABC In Ardiuno	04-04-2017 and 05-04-2017	154
14.	Smart Student	Through Out the Year	15

iV) Students are provided with Internships and Industrial Tours/ visits as a part of experiential learning

Internships during 2015-16

Name of the Industry	Duration	Name of the Student
My Home Constructions Pvt. Ltd., Hyderabad	25-05-2015 To 23-06-2015	B. KONETI REDDY
		K. SAIBHANU
		M. RAMU
		N. KARTHIK
		N. N.SUBBA REDDY
		E. KRISHNA SAI
		M. HARISH
		M. AKHIL
		N R TRINADH
		N SHARADA
STUP Consultants Pvt. Ltd., Bangalore	25-05-2015 To 23-06-2015	GNK CHAITANYA
		K SUMANTH
		K M AKHIB
		K SREEKATH REDDY
TGP and GNSS Project, Irrigation & CAD Dept., Govt. of AP, Tirupati	25-05-2015 To 23-06-2015	T ANUSHA
		Y V SAIGOVARDHAN
		P VISHNU PRIYA
		POOLA LOKESH
		S P KUMAR
BSCPL Infrastructure Ltd., Hyderabad	25-05-2015 To 23-06-2015	A J SAIKARTHIKEYA
		A V NAVEEN
		DINAKAR P B
		J GOPINATH
		K V DUSHYANTH
		B LOKESH
		B TEJA SREE
		H L ANUSHA
		K PRAVEEN KUMAR
		NARRAGIRISH
Shapoorji Pallonji and Company Limited, Bangalore	25-05-2015 To 23-06-2015	B SWETHA
		D GUNASEKHAR
		K MANASA
		M SRIKANTH
		O TEJASWI
RDS Projects, Kerala	25-05-2015 To 23-06-2015	A AFTAN SYED
		B PRANEETH REDDY
		GOLLA RAMESH

		M SAMANTH REDDY
		M BAJIBABU
Sandeep Constructions, Bangalore	25-05-2015 To 23-06-2015	B HASWANTH
		B LEELAVATHI
		L K SAGARSINGH
		P.C. VAISHNAVI
		G SHAHEEN
Arya Constructions, Bangalore	25-05-2015 To 23-06-2015	P VISHNUVARDHAN REDDY
		S SHALINI
		U VAMSI KRISHNA
		V SUPRIYA
Ultra tech Cements Pvt. Ltd., Tirupati.	25-05-2015 To 23-06-2015	B.N. KALAPANA
		B DEVARANI
		C HIMAKAR
		O SWATHI
		P. SREE DURGA
SDSC-ISRO, Sriharikota	23-05-2016 To 30-06-2016	P. VENKATA RAMANA
		S KARTHEEK
		T SAI PAVAN KUMAR
SDSC-ISRO, Sriharikota	23-05-2016 To 30-06-2016	P. VENKATA RAMANA
		S KARTHEEK
		T SAI PAVAN KUMAR
AMAZON, Development Center India, Pvt Ltd., Hyderabad	29-02-2016 To 19-08-2016	C.SAI KIRAN
Binate Technologies Pvt. Ltd, Tirupathi	28-12-2015 To 11-06-2016	YARADODDI HARITHA
		VEMULA DURGA MAHESH BABU
		T.LOKESH
Amphenol Advanced Sensors Hyderabad	28-12-2015 To 11-06-2016	A.VISWANATH
Reliance JIO, Hyderabad	10-11-2015 To 09-06-2016	BEZAWADA LEKHYA
Suffix Tree Technologies, Bangalore	09-11-2015 To 24-06-2016	KALAVA GUNTA LAKSHMI
		NAGAPATLA THEJA
VEDA IIT, Hyderabad	03-11-2015 To 30-06-2016	ARAVA PRAKASH
Vikram Sarabahai Space Centre (VSSC), Tiruvanantapuram, Kerala	03-11-2015 To 30-06-2016	MALISETTY MAMATHA
NELCAST Ltd., Nellore	25-05-2015 To 29-05-2015	JALADI SAI KUMAR
		BALA KARTHEEK
		MUPPALA BHANU PRAKASH
		GANTEM KRUPA ABHILASH
		NADIMGOLLA REDDY PRASAD
Coca-Cola, Sri Kalahasti	03-06-2015 To 09-06-2015	DAVALA KIRANMAYI
		JANGAM RAMESH
		G MUNISEKHAR
		BUKKAPATNAM VISHNUVARDHAN
Bharati Cement Corporation Pvt. Ltd.,	25-05-2015 To 30-06-	GANGELI MAMATHA

Yerraguntla	2015	BANDELA PAVANKUMAR
		KARNATI SIVA KESHAVA REDDY
		K ESWAR
Avend Technologies, Bengaluru	01-06-2015 To 06-06-2015	RACHAMALLA VIKRANTH
		PATTI RAJESHREDDY
		PEDDINTI VENKATESH
		NETLA PARTHA SARADI REDDY
RABS Engg works, Chennai.	01-06-2015 to 06-06-2015	N. SRI CHARAN
IBM	01-10-2015 To 31-12-2015	MONIKA KEERTHI
		BANDI SAI PRIYADARSHINI
		MANASA H
Paradigm Creatives, Hyderabad	16-05-2015 To 30-06-2015	MONIKA KEERTHI
Rebaca Technologies Pvt. Ltd., Bangalore	21-01-2015 To 20-06-2015	SAKAMURI DILLI BABU
Regional Telecom Training Center, BSNL, Gachibowli, Hyderabad – 500032	17-05-2015 To 31-06-2015	BANDARU SRI HARSHA
		BUSETTY MUNINDRA
		M N KARTHIK CHALEMPALEM
		MUNIKUMAR E
		VISHNUVARDHAN PAGADALA
		RAGHAVA ESWAEABHISHEK
Grow-Z Software Solutions, Bangalore	17-05-2015 To 31-06-2015	NIKIITHA P
TECHWORLD SOLUTIONS 5th Floor,Sarada Complex, Saifabad, Hyderabad – 500 029, Telangana, India Phone No: 040 – 64586123 E-Mail : info@techworldsolutions.in	6 Months (January-June, 2016)	NAGELLA SRAVANI
Leiyo informatics Private Limited 8-3-229/D/1/24, Sravanthi Nagar, Near Chiranjeevi Blood Bank, Jubilee Hills, Road No:10, HyderabadTelangana, India	6 Months (January-June, 2016)	SURYA PRAKASH REDDY
Tridentz Technologies SulochanaTowers ,1st Floor No 1-8-617/2, Prakash Nagar, Begumpet, Hyderabad, Telangana-500016 INDIA +91 40 66441666,+91 40 66442666 info@tridentztechnologies.com	6 Months (December-May, 2016)	K VENKATEWSARLU
	6 Months (December-May, 2016)	KURUVA MALLIKHARJUNA REDDY
Koushik Web Solutions Pvt. Ltd., 7-1-212/A/69, ShivBagh Colony, Ameerpet, Hyderabad, Telangana - 500038 Ph: 040-23754499. Email: info@koushikwebsolutions.com Website: www.koushikwebsolutions.com	6 Months (December-May, 2016)	P CHIRANJEEVI

Industrial Tours/Visits during 2012-`13 to 2016-`17

Name of the Industry	Duration (From-To)	Name of the Student/Branch
Rayalaseema Thermal Power Project (RTPP), Kadapa	01-06-2016 to 01-06-2016	MUKKARA PRIYA VANDANA
	01-06-2016 to 01-06-2016	KONASAMUDRAM REVANTH
	01-06-2016 to 01-06-2016	N R SREEDHAR
	01-06-2016 to 01-06-2016	KURUGODU DINAKAR
	01-06-2016 to 01-06-2016	MADISETTY MANOJ KUMAR
National Atmospheric Research Laboratory, Gadanki	09-02-2016 to 09-02-2016	III EIE-B
Satish Dhawan Space Centre SHAR, Sriharikota	10-26-2016 to 10-26-2016	III EIE- A
Vizag Steel Plant, Vizag	13-06-2015 to 13-06-2015	PELLELLA MUNIHEMANTH
	13-06-2015 to 13-06-2015	SHAIK BELLARI NEHA AFREEN
	13-06-2015 to 13-06-2015	V H SRI MUDHITHA
	13-06-2015 to 13-06-2015	V M SURENDRANATHA REDDY
	13-06-2015 to 13-06-2015	V VENKATESH
Ravands, Chandragiri	25-06-2015 to 25-06-2015	ALLAM SUNIL KUMAR
	25-06-2015 to 25-06-2015	THUMMALA SANDHYA
	25-06-2015 to 25-06-2015	KURUVA VANNURSWAMY
	25-06-2015 to 25-06-2015	PEDDAPOTULA PRAVEENKUMAR
	25-06-2015 to 25-06-2015	YERRAPALLI RAJASHEKAR
DRDL, Hyderabad	22-06-2015 to 22-06-2015	PUTTUR UMAMAHESWAR
	22-06-2015 to 22-06-2015	VINNAKOTA SURYA MADHURI
	22-06-2015 to 22-06-2015	VARIDHIREDDY SIVAKUMAR REDDY
	22-06-2015 to 22-06-2015	MEENUGA YESHWANT
Garuda Polymers Pvt. Ltd., Tirupati	26-09-2014 to 26-09-2014	CHAVVA HARSHA VARDHAN REDDY
	26-09-2014 to 26-09-2014	GANTALA SANDEEP KUMAR
	26-09-2014 to 26-09-2014	V KISHORE REDDY
	26-09-2014 to 26-09-2014	J ANILKUMAR REDDY
	26-09-2014 to 26-09-2014	V CHANDU
	23-09-2014 to 23-09-2014	KAMASETTY VENUGOPAL
	23-09-2014 to 23-09-2014	P R THEJASWINI
	23-09-2014 to 23-09-2014	KONDURU VAMSI KRISHNA
	23-09-2014 to 23-09-2014	B SUBBA RAO
	23-09-2014 to 23-09-2014	KALLURU MOHAMMAD SHAKEER
Satish Dhawan Space Centre SHAR, Sriharikota	29-09-2014 to 29-09-2014	III EIE Students
	22-11-2013 to 22-11-2013	IV EIE Students
Bharathi Cement Corporation Private Limited	21-11-2013 to 21-11-2013	IV EIE Students

Participative Learning

(i) **Labs, seminar and project as a group work is designed to foster student peer and participative learning - *Team formation method***

(ii) **Participation of the student by giving feedback on teaching quality - *Feedback reports on teaching quality - analysis and impact***

(iii) **Student survey on Program Educational Objectives, Program Outcomes and Curriculum - *Survey reports- analysis and impact***

i) Labs, Seminar and Project as a group work is designed to foster student peer and participative learning

- **Practical Courses:** Each student batch/team (Max. of 3) in lab courses comprise of Two Advanced Learners and One Slow Learner.

- **Mini Project & Project Courses:** Each student batch/team (Max. of 4) in these courses comprise of Two Advanced Learner and Two Slow Learners.

SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)
Sree Sainath Nagar, A. Rangampet – 517 102
Department of Information Technology

IV B.Tech II Semester (IT-A), Project Work (2014-15)

April 22, 2015

S.No.	Roll No.	Name of the Student	Title of the Project
	A01: Mr. V. Lokanadham Naidu		
1	11121A1224	E Sukanya	Hashing Technique using Color Vector Angles and Discrete Wavelet Transform for Image Integrity
2	11121A1260	Mounika Sabbani	
3	11121A1252	Kummetha Vepakshi Reddy	
4	11121A1205	Amineni Uday Kumar	
	A02: Mr. K. Khaja Baseer		
1	11121A1219	Chelluri Sharmila	Modeling Quality Attributes and Quality Aware Product Configuration using QAKB
2	11121A1238	K Jyothinadh	
3	11121A1232	Guddeti Manasa	
4	11121A1209	Baligari Vignan	
	A03: Dr. K. Ramani		
1	11121A1227	Gaddam Lakshmi Swathi	Authentication through Voice Biometrics
2	11121A1222	Darimadugu V Susmita	
3	11121A1254	M Raveendra	
4	11121A1226	G Leelavinodh	
	A04: Mr. P. Srinivasa Reddi		
1	11121A1215	C M Mehtaj	Lossless Color Image Compression Using Prediction Methods
2	11121A1253	Kundanagurthi Prathyusha	
3	11121A1242	K Sandeep	
4	11121A1212	Bhagath Naveen Naik	
	A05: Dr. L. Venkateswara Reddy		
1	11121A1245	Kakarla Yogitha	Students Performance Prediction System using Multi Agent Data Mining
2	11121A1248	Kongara Bandhavi	
3	11121A1230	Golla Sai Vinay	
4	11121A1225	G Dinesh Kumar	
	A06: Mr. G. M. Chanakya		
1	11121A1241	K Pooja	A Vehicular Networking Perspective on Estimating Vehicle Collision Probability at Intersections
2	11121A1264	N Sruthi	
3	11121A1223	Doniparthi Umamaheswar Rao	
4	11121A1234	Ishan Chinta Reddy	
	A07: Mr. G. Uma Mahesh		
1	11121A1231	Gopi Rekha	An Efficient Protocol for Warning Message Dissemination to a Desired Number of Vehicles in VANET
2	11121A1221	D Jeevana Priya	
3	11121A1229	Ganta Sowmya	
4	11121A1202	A Ramya	
	A08: Mr. O. Obulesu		
1	11121A1240	K Nandini	Semantic Web-Page Recommendation System based on Domain Knowledge
2	11121A1250	Kota Sairam	
3	11121A1244	K Usha Rani	
4	11121A1228	Gaddam Prasanth	

S.No.	Roll No.	Name of the Student	Title of the Project
	A09: Mr. S. Sreenivasa Chakravarthi		
1	11121A1243	K Somalatha	An Efficient way of Classifying and Clustering Documents based SMTP
2	11121A1201	A Manasa	
3	11121A1247	Konduru Shanmuka Sai Sankar Raju	
4	11121A1210	Bandaru Subhramanya Nikhil Vasista	
	A10: Mr. P. Bhasha		
1	11121A1266	Nagari Muni Saikumar	Facilitating Document Annotation using Content and Querying Value
2	11121A1233	Gunda Vijaykumar	
3	11121A1256	Malem Sai Saranya	
4	11121A1206	Autukuri Mounikachowdary	
	A11: Ms. V. Jyothsna		
1	11121A1268	Nagisetti Sai Pavani	Performance Evaluation of Intrusion Detection System using EDADT Algorithm
2	11121A1203	A Sai Krishna Monesh	
3	11121A1213	Bodireddigari Spandana	
4	11121A1217	C Thippeswamy	
	A12: Ms. E. Sandhya		
1	11121A1249	Kontham Mounika	Hypothesis of Unsupervised Web Data Extraction using Shared Patterns
2	11121A1269	Naguru Vyshnavi	
3	11121A1265	N Umesh	
4	11121A1207	B Amarnath	
	A13: Mr. Mahendra M		
1	11121A1236	Jambala Naga Vineela	Finding the Position of Jammers in Wireless Networks by Error Minimizing Framework
2	11121A1211	Bellamkonda Vamsi	
3	11121A1259	Megavath Balaji Naik	
4	11121A1235	Iytha Sai Chandrisha	
	A14: Mr. Shaik Munwar		
1	11121A1214	Bysani Narasimha Kumar	Heuristic based Anonymization for Relational Data
2	11121A1257	Maram Sreeja	
3	11121A1251	Kottakota Bhanusri	
4	11121A1218	Challa Madhu Babu	
	A15: Mr. C. Kishore		
1	11121A1263	Muppirala Sandeep	Relational Association Rule Mining for Predicting Software Defects
2	11121A1216	C Shilpa	
3	11121A1262	Mukku Ramesh	
4	11121A1220	Chinnagireddy Sushmitha	
	A16: Mr. A. Srinivasulu		
1	11121A1270	Nangineni Gadilingappa Manasa	Efficient Prediction of Complex Queries over Multiple Databases
2	11121A1204	Aluru Kalpana	
3	11121A1255	M Vishnu Priya	
4	11121A1208	Bakkireddy Vinod Kumar Reddy	

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SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet – 517 102

Department of Information Technology

IV B.Tech II Semester (IT-B), Project Work (2014-15)

April 21, 2015

S.No.	Roll No.	Name of the Student	Title of the Project
	B01: Mr. P. Bhasha		
1	11121A1299	T Swetha	A Cost Effective Model for Vehicle Tracking System
2	11121A12B5	Vidavaluru Anusha	
3	12125A1217	T Amala	
	B02: Dr. L. Venkateswara Reddy		
1	11121A1279	Pathakamuri Praveena	Scalable Keyword Search on Large RDF Data
2	11121A1288	S Divya Sree	
3	11121A12B4	Vennamuddala Sandhya Sravani	
	B03: Mr. V. Lokanadham Naidu		
1	11121A12B7	Y G Vijayasravanthi	Robust Image Hashing using Ring-Based Entropies for Image Authentication
2	11121A12A6	V K Ramya	
3	11121A12A4	Tiruvayipati L Narasimha Kartheek	
4	12125A1215	S Krishna Kumar	
	B04: Mr. P. Srinivasa Reddi		
1	11121A12B6	Vupputuru Hasmitha	A Novel Scheme of Compressing Encrypted Images
2	11121A12B9	Avula Eswar Prasad Naik	
3	10121A1270	Malisetty Sravani	
4	11121A1278	Panchala Vinod Kumar	
	B05: Dr. K. Ramani		
1	11121A1287	Rasineni Keerthi	Secure Bank Transactions using Face Recognition
2	11121A12A9	Varadaraju Ujjwala	
3	11121A1281	Patnam Rajesh	
4	11121A1289	Sagalamarry Sandeep	
	B06: Ms. K. Nirmala		
1	11121A1272	Nedimusili Haritha	FRIENDBOOK: A Lifestyle-Based Friend Recommendation System for Social Networks
2	11121A12A2	Tanguturi Sreevani	
3	10121A12A2	S Prathima	
4	11121A1290	Sailekya D	
	B07: Mr. G. Uma Mahesh		
1	11121A12B8	Yennamreddi Reddy Harshitha	Protecting Service based Vehicular Networks using Intrusion Detection Mechanism
2	11121A1296	Sripathi Revathi	
3	12125A1206	K Bhagyasri	
4	11121A12A3	Thallapaka Sai Nandini	
	B08: Ms. C. Silpa		
1	11121A1277	Pamisetty Ramyasruthi	Discovering Emerging Topics in Social Streams via Link Anomoly Detection
2	12125A1203	Darsi Vidyasagar	
3	12125A1202	D Alekhya	
4	11121A1275	Padapalli Uday Chandu	
	B09: Mr. M. Thrilok Reddy		
1	12125A1201	Chanathlaa Gayathri	Online Placement and Training System
2	12125A1207	K Kushal Kumar	
3	12125A1210	Malepati Prathibha	

4	11121A1274	P Maruthi Rao	
S.No.	**Roll No.**	**Name of the Student**	**Title of the Project**
	B10: Ms. B. Minny Pricilla		
1	11121A1276	Pamisetty Rama Mohan	Web-Based Traffic Sentimental Analysis based on Opinion Mining
2	11121A1291	Shaik Abdul Quadeer	
3	11121A1292	Shaik Saliq Ul Haq	
4	11121A1283	Pidikiti Bhanu Thej	
	B11: Mr. Mastan Mohammed Meera D		
1	11121A12A0	Tadiparthi Amani	Protecting Privacy on Location-Based Personal Identification
2	12125A1213	R Anusha	
3	12125A1208	Karamalla Mubarak	
4	11121A12A7	V Nikhil	
	B12: Ms. E. Sandhya		
1	11121A1298	T Srujana Kumar	Uncommon Weighted Itemset Mining using Frequent Pattern Growth
2	11121A12A8	Vanjavakam Radha	
3	11121A1294	Siripuram Sindhuja	
4	11121A12A5	V Harsath Kumar	
	B13: Mr. B. Bhaskar Kumar Rao		
1	11121A12B2	Veeramachineni Rajeswari	Efficient Multicast Routing Algorithm Based on Tree and Mesh Structure in MANETS
2	12125A1204	Gottipati Ramcharan	
3	12125A1205	J Muni Partha	
4	12125A1211	Nadiminti Venkataswamy Kavitha	
	B14: Mr. O. Obulesu		
1	11121A12A1	Talapaka Sreevalli	A Hybrid Recommendation Strategy for Travel Package System
2	11121A1295	Sompalli Rupika	
3	11121A1286	R D Thejesh	
4	10121A1252	K Hariprasad Reddy	
	B15: Mr. N. Jaswanth		
1	12125A1212	Pandluri Madhavi	Personalized Recommendation for Social Networks using Probabilistic Matrix Pactorization
2	11121A1282	Perugu Lokeswar	
3	11121A1271	Narasimha Prathima	
4	12125A1214	Rajesh Kumar Soma	
	B16: Mr. Shaik Munwar		
1	11121A1285	Puduri Anju	Profile based Privacy Protection in Personal Web Search
2	11121A1284	Posam Lokeswara Reddy	
3	11121A12C0	T Naveen Kumar	
4	10121A1224	C Tharun Kumar	
	B17: Mr. A. Srinivasulu		
1	11121A12B3	Vemula Maneesha	Finding Ratings of Feedback Comments using Extended LLDA Algorithm
2	11121A1297	Sulake Vasudha	
3	12125A1209	M Suresh	

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ii) Participation of the student by giving feedback on teaching quality.

Faculty Feedback parameters

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)
SREE SAINATH NAGAR, A.RANGAMPET, TIRUPATI - 517102
2017-2018 Mid Semester Feedback - III B.Tech I Sem ECE

MR. M. VENKATA NARESH	ELECTRONICS AND COMMUNICATION ENGINEERING

Pulse and Digital Circuits

1. Knowledge Base of the Teacher	85
2. Communication Skills	86
3. Interest generated by the Teacher	84.67
4. Sincerity/Commitment of the Teacher	84.33
5. Ability to explain the relevance and applications of the subject	83
6. Ability to combine content with other courses	85
7. Accessibility of the Teacher in and out of the class	83
8. Ability to design quiz/test/assignment/examination and project to evaluate student understanding of the course	83
9. Provision of sufficient time for interacting and clearing doubts	82.67
10. Overall Rating	85

Cummulative Percentage	84.167	% of Respondents	75.949367088608

PRINCIPAL

Student Feedback on Faculty – Analysis – Department of ECE:

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)
SREE SAINATH NAGAR, A.RANGAMPET, TIRUPATI - 517102
2016 - 17 Mid Semester Feedback - III B.Tech - I Sem ECE-Section - A

Course Code and Name of the Course	Faculty ID and Name of the Faculty	Cummulative %
14BT5HS10-Student Development Activity	SVECECE62-MR. K. RAMESH	65.688
14BT50422-PDC and IC Lab	SVECECE89-P.V.S.R.BHARADWAJA	68.134
14BT50422-PDC and IC Lab	SVECECE50-DR. N. PADMAJA	69.791
14BT50422-PDC and IC Lab	SVECECE94-MR. M. VENKATA NARESH	70.586
14BT50422-PDC and IC Lab	SVECECE89-P.V.S.R.BHARADWAJA	72.086
14BT5HS10-Student Development Activity	SVECECE94-MR. M. VENKATA NARESH	72.114
14BT50422-PDC and IC Lab	SVECECE50-DR. N. PADMAJA	72.25
14BT50422-PDC and IC Lab	SVECECE94-MR. M. VENKATA NARESH	72.384
14BT5HS10-Student Development Activity	SVECECE06-V.R.ANITHA	72.485
14BT4HS01-Business Communication and Presentation Skills	SVECGEBH110-MS. B. ANITHA	72.753
14BT50404- Linear IC Applications	SVECEEE08-SHAIK MAHABOOB BASHA	74.304
14BT50405-Pulse and Digital Circuits	SVECECE94-MR. M. VENKATA NARESH	75.316
14BT5HS01-Managerial Economics and Principles of Accountancy	SVECGEBH26- G. SUBRAMANYAM	76.668
14BT50403-Digital IC Applications	SVECECE38-MR. M. NARESH BABU	77.999
14BT50402-Digital Communications	SVECECE96-M.NAGA NAVEEN KUMAR	78.267
14BT50401-Antennas and Propagation	SVECECE06-V.R.ANITHA	79.09

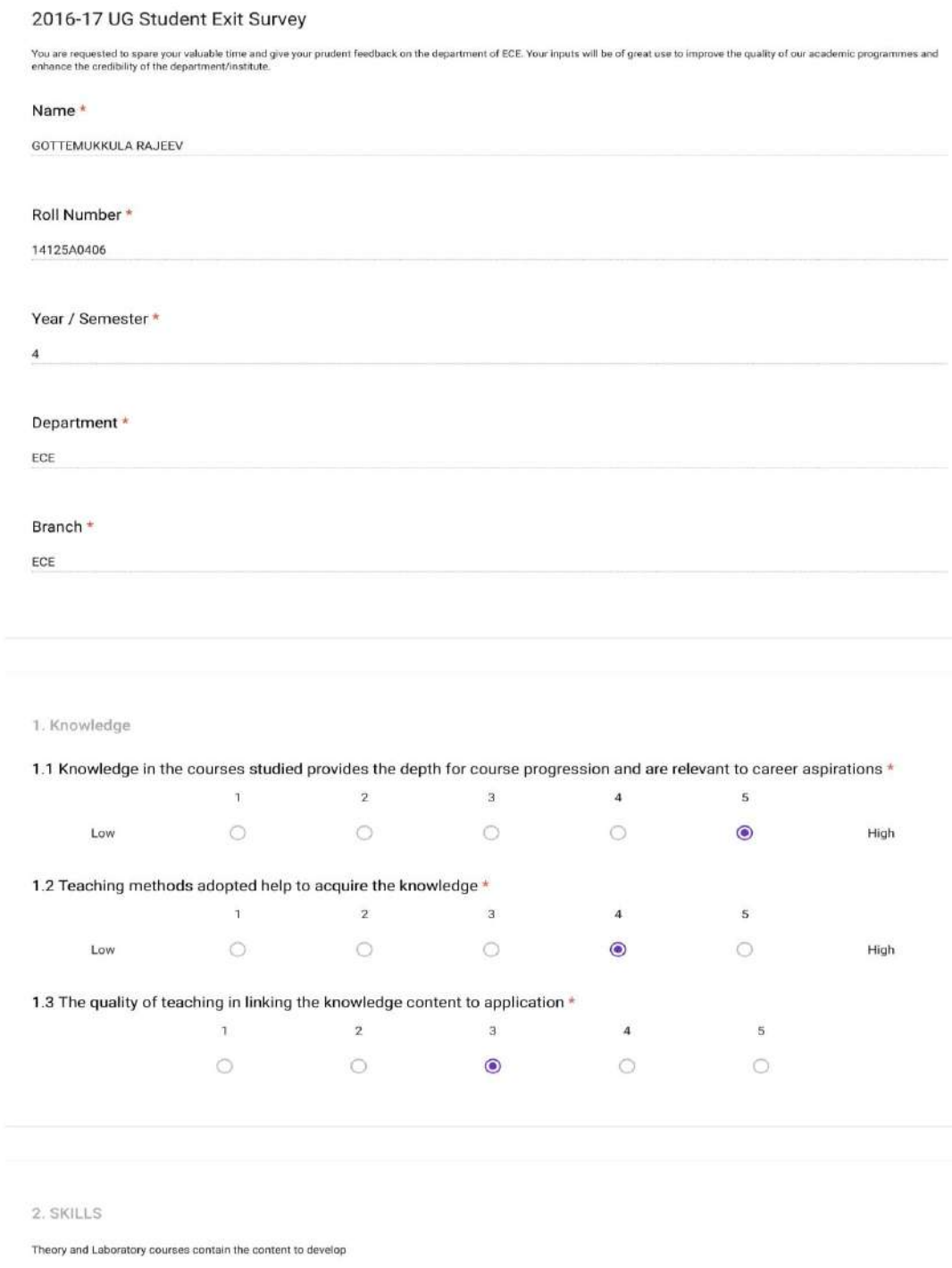
PRINCIPAL

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)
SREE SAINATH NAGAR, A.RANGAMPET, TIRUPATI - 517102
2016 - 17 Mid Semester Feedback - III B.,Tech - I Sem ECE-Section - B

Course Code and Name of the Course	Faculty ID and Name of the Faculty	Cummulative %
14BT50403-Digital IC Applications	SVECECE51-G. NARESH	49.953
14BT5HS10-Student Development Activity	SVECECE51-G. NARESH	51.395
14BT50402-Digital Communications	SVECECE91-MS. P. VENKATA SAMEERA	52.001
14BT5HS10-Student Development Activity	SVECECE91-MS. P. VENKATA SAMEERA	53.487
14BT50422-PDC and IC Lab	SVECECE11-MS. M. BHARATHI	54.924
14BT50422-PDC and IC Lab	SVECECE11-MS. M. BHARATHI	56.334
14BT50422-PDC and IC Lab	SVECECE84-P GEETHA	57.484
14BT50422-PDC and IC Lab	SVECECE84-P GEETHA	59.36
14BT50422-PDC and IC Lab	SVECECE13-MR. P. MADHU KUMAR	62.329
14BT5HS01-Managerial Economics and Principles of Accountancy	SVIM83-V SUBHAMATHI	63.235
14BT50422-PDC and IC Lab	SVECECE13-MR. P. MADHU KUMAR	63.385
14BT5HS10-Student Development Activity	SVECECE32-MS. K. SUDHA	71.116
14BT50404- Linear IC Applications	SVECECE87-ANIRUDDH BAHADUR YADAV	72.434
14BT5HS10-Student Development Activity	SVECECE50-DR. N. PADMAJA	72.756
14BT50405-Pulse and Digital Circuits	SVECECE50-DR. N. PADMAJA	74.904
14BT50401-Antennas and Propagation	SVECECE32-MS. K. SUDHA	75.582
14BT4HS01-Business Communication and Presentation Skills	SVECGEBH27-MS. G. M. MADHAVI	77.138

PRINCIPAL

iii) Student survey on Program Educational Objectives, Program Outcomes



2016-17 UG Student Exit Survey

You are requested to spare your valuable time and give your prudent feedback on the department of ECE. Your inputs will be of great use to improve the quality of our academic programmes and enhance the credibility of the department/institute.

Name *

GOTTEMUKKULA RAJEEV

Roll Number *

14125A0406

Year / Semester *

4

Department *

ECE

Branch *

ECE

1. Knowledge

1.1 Knowledge in the courses studied provides the depth for course progression and are relevant to career aspirations *

	1	2	3	4	5	
Low	○	○	○	○	◉	High

1.2 Teaching methods adopted help to acquire the knowledge *

	1	2	3	4	5	
Low	○	○	○	◉	○	High

1.3 The quality of teaching in linking the knowledge content to application *

1	2	3	4	5
○	○	◉	○	○

2. SKILLS

Theory and Laboratory courses contain the content to develop

2.1. skills to Analyze problems and cases in the course / program *

	1	2	3	4	5	
Low	○	◉	○	○	○	High

2.2 Design and development of systems and processes *

	1	2	3	4	5	
Low	○	○	◉	○	○	High

2.3 Problem solving skills in the domain *

	1	2	3	4	5	
Low	○	○	○	◉	○	High

2.4 Skills in devising experiment protocols/reports and communicate well with the domain experts *

	1	2	3	4	5	
Low	○	○	○	○	◉	High

3. APPLICATION

3.1 Ability to apply new tools and software relevant to your laboratory sessions or in project work. *

	1	2	3	4	5	
Low	○	◉	○	○	○	High

3.2 Ability to write case studies relevant to the course domain. *

	1	2	3	4	5	
Low	○	○	◉	○	○	High

4. ATTITUDE

4.1 Ability to work individually and in a team in a lab session and executing a project *

	1	2	3	4	5	
Low	○	○	○	◉	○	High

4.2 Course content prepares you to plan solutions for societal needs *

	1	2	3	4	5	
Low	○	○	○	◉	○	High

4.3 Course content help you understand and create eco- friendly solutions *

	1	2	3	4	5	
Low	○	○	○	○	◉	High

4.4 Awareness to ethical code and practice *

	1	2	3	4	5	
Low	○	○	◉	○	○	High

4.5 Courses/Program stimulates you to further acquire skills and knowledge in the domain *

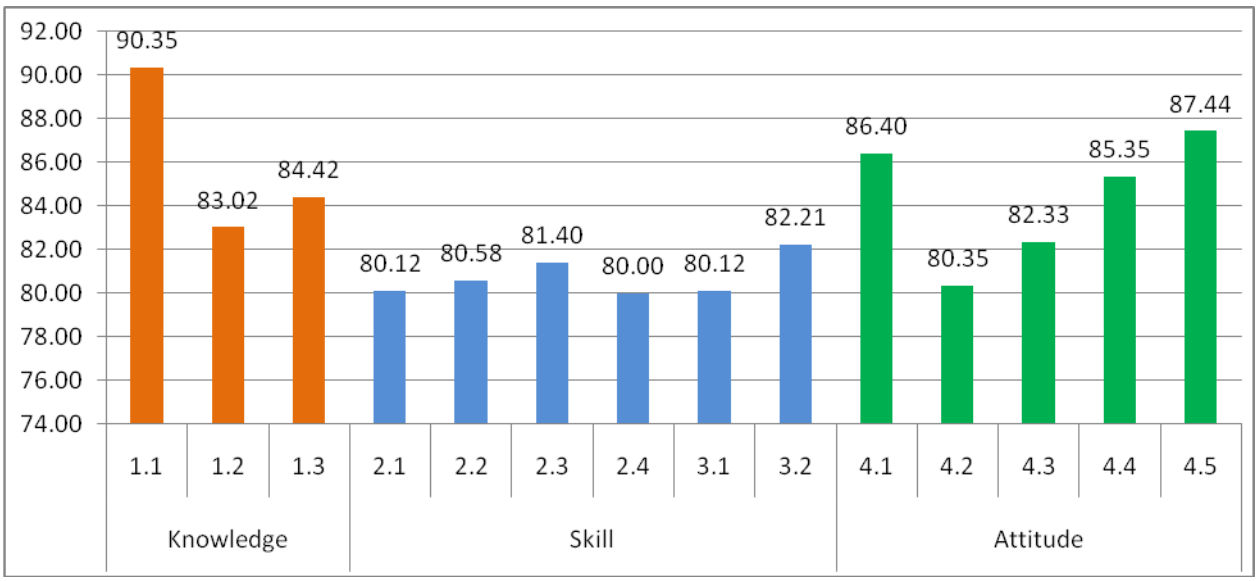
	1	2	3	4	5	
Low	○	○	○	◉	○	High

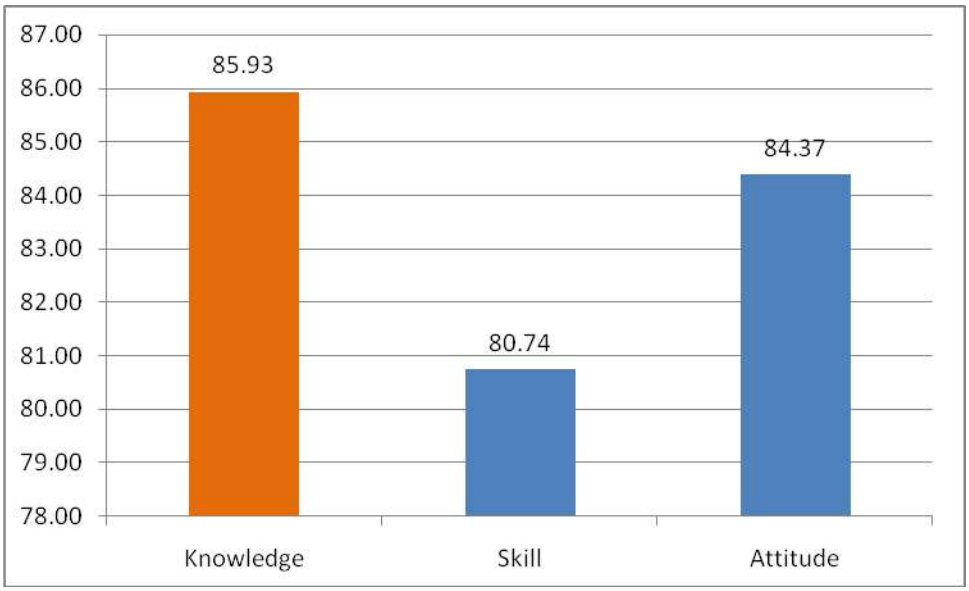
Suggestions for change of syllabus in the existing courses and inclusion of new courses/ technologies/ tools etc to be included in the curriculum

Introduce advanced subjects in latest technologies like communication design

Thanks for your valuable time !! Your suggestions will surely help us to enhance our curriculum !!

Analysis of Student Exit Survey 2016-17





No. of Respondents:	172
Total No. of Students:	200
% Response:	86

iV) Student survey on Curriculum

Survey form for the course STLD

Switching Theory and Logic Design Course Exit Survey form (II B. Tech., I-Semester, ECE)

SREE VIDYANIKETHAN ENGINEERING COLLEGE(Autonomous)
Sree Sainath Nagar, Tirupathi – 517102

Department of Electronics and Communication Engineering

Dear Student,

Name *

B. Sri midhusha

Roll No. *

16121a0423

Mail ID *

srimidhusha23@gmail.com

1. The curriculum designed, provided sufficient knowledge on the concepts of the domain. *

- (●) Strongly Agree
- ( ) Agree
- ( ) Disagree
- ( ) Not relevant

2. The curriculum is helpful in analyzing the concepts within the syllabus and also beyond the syllabus. *

- (●) Strongly Agree
- ( ) Agree
- ( ) Disagree
- ( ) Not relevant

3. The curriculum is helpful in designing the Combinational and Sequential circuits to meet the required specifications. *

- (●) Strongly Agree
- ( ) Agree
- ( ) Disagree
- ( ) Not relevant

4. The curriculum inculcates skill and ability to evaluate and analyze the behavior and performance of various Digital circuits and systems *

- (●) Strongly Agree
- ( ) Agree
- ( ) Disagree
- ( ) Not relevant

5. The course renders scope to provide possible solutions pertaining to digital electronics. *

- (●) Strongly Agree
- ( ) Agree
- ( ) Disagree
- ( ) Not relevant

6. The concepts in the course taught optimized designs useful for society than I expected. *

- (●) Strongly Agree
- ( ) Agree
- ( ) Disagree
- ( ) Not relevant

7. In this course overall, I have learned more than I expected. *

- (●) Strongly Agree
- ( ) Agree
- ( ) Disagree
- ( ) Not relevant

8. This course made me aware of its applications in real world. *

- (●) Strongly Agree
- ( ) Agree
- ( ) Disagree
- ( ) Not relevant

9. I recommend this course for my juniors *

- (●) As it is
- ( ) With little modifications
- ( ) With total revamp
- ( ) Unable to recommend

Courses with Problem Solving Skills

(viii) Core, professional electives and a few of open electives is designed to develop the problem solving skills among the students - *List of courses with problem solving skills*

Core, Professional electives and a few of open electives is designed to develop the problem solving skills among the students

Sample copy of B.Tech. ECE (SVEC-16)

List of Courses having Problem Solving Outcome

S. No.	Course Code	Course Title
I Year - I Semester		
1.	16BT1BS02	Engineering Physics
2.	16BT1BS03	Matrices and Numerical Methods
3.	16BT1BS04	Multi-variable calculus and Differential equations
4.	16BT10241	Network Analysis
5.	16BT10501	Programming in C
6.	16BT10531	Programming in C Lab
I Year - II Semester		
7.	16BT2BS01	Transformation Techniques and Partial Differential Equations
8.	16BT20401	Electronic Devices and Circuits
9.	16BT20541	Foundations of Data Structures
10.	16BT10331	Computer Aided Engineering Drawing
11.	16BT20551	Foundations of Data structures Lab
II Year - I Semester		
12.	16BT3BS02	Special Functions and Complex Analysis
13.	16BT30401	Electronic Circuit Analysis and Design
14.	16BT30402	Signals and Systems
15.	16BT30403	Switching Theory and Logic Design
16.	16BT30241	Electrical Technology
17.	16BT30431	Basic Electronics and Digital Design Lab
18.	16BT30432	Signal and Systems Lab
II Year - II Semester		
19.	16BT40401	Analog Communications
20.	16BT40402	Digital IC Applications
21.	16BT40403	Electromagnetic Theory and Transmission Lines
22.	16BT40404	Linear IC Applications
23.	16BT40405	Probability and Stochastic Process
24.	16BT40406	Pulse and Digital Circuits

25.	16BT40431	Analog Communications Lab
26.	16BT40432	Electronic Circuit Analysis and Design Lab
III Year - I Semester		
27.	16BT50201	Control Systems
28.	16BT50401	Digital Communications
Interdisciplinary Elective-1		
29.	16BT50404	Electronic Measurements and Instrumentation
30.	16BT50501	Computer Networks
31.	16BT30501	Computer Organization
32.	16BT50432	Microprocessors and Microcontrollers Lab
III Year - II Semester		
33.	16BT60401	Antennas and Waveguides
34.	16BT60402	Digital Signal Processing
Interdisciplinary Elective-2		
35.	16BT40502	Database Management Systems
36.	16BT71205	Cryptography and Network Security
37.	16BT61241	Wireless Sensor Networks
Program Elective-1		
38.	16BT60404	Image Processing
39.	16BT60405	Radar Engineering
40.	16BT60406	Telecommunication Switching Systems
Program Elective-2		
41.	16BT60407	Digital CMOS IC Design
42.	16BT60408	Information Theory and Coding
43.	16BT60409	Light Wave Communications
44.	16BT60410	Nanoelectronics
45.	16BT60431	Digital Communications Lab
46.	16BT60432	Digital Signal Processing Lab
IV Year - I Semester		
47.	16BT70401	Cellular and Mobile Communications
48.	16BT70402	Embedded Systems
49.	16BT70403	Microwave Engineering
Program Elective-3		
50.	16BT70404	Advanced Digital Signal Processing
51.	16BT70405	Mixed Signal Design

52.	16BT70406	Satellite Communications
53.	16BT70407	Wireless Communication and Networks
Program Elective-4		
54.	16BT70408	Low Power CMOS VLSI Design
55.	16BT70409	RF Engineering
56.	16BT70411	Spread Spectrum Communication
57.	16BT70431	Antennas and Microwave Engineering Lab
58.	16BT70432	Embedded Systems Lab
59.	16BT70433	Comprehensive Assessment
60.	16BT80431	Project Work
Open Elective		
61.	16BT6HS01	Banking and Insurance
62.	16BT6HS03	Cost Accounting and Financial Management
63.	16BT60112	Building Maintenance and Repair
64.	16BT60310	Managing Innovation and Entrepreneurship
65.	16BT60505	Engineering System Analysis and Design
66.	16BT71011	Micro-Electro-Mechanical Systems
67.	16BT61505	Bio-informatics

2.6.1

Program outcomes, program specific outcomes and course outcomes for all Programs offered by the institution are stated and displayed on website and communicated to teachers and students

3. Evidences for QlM metric 2.6.1:

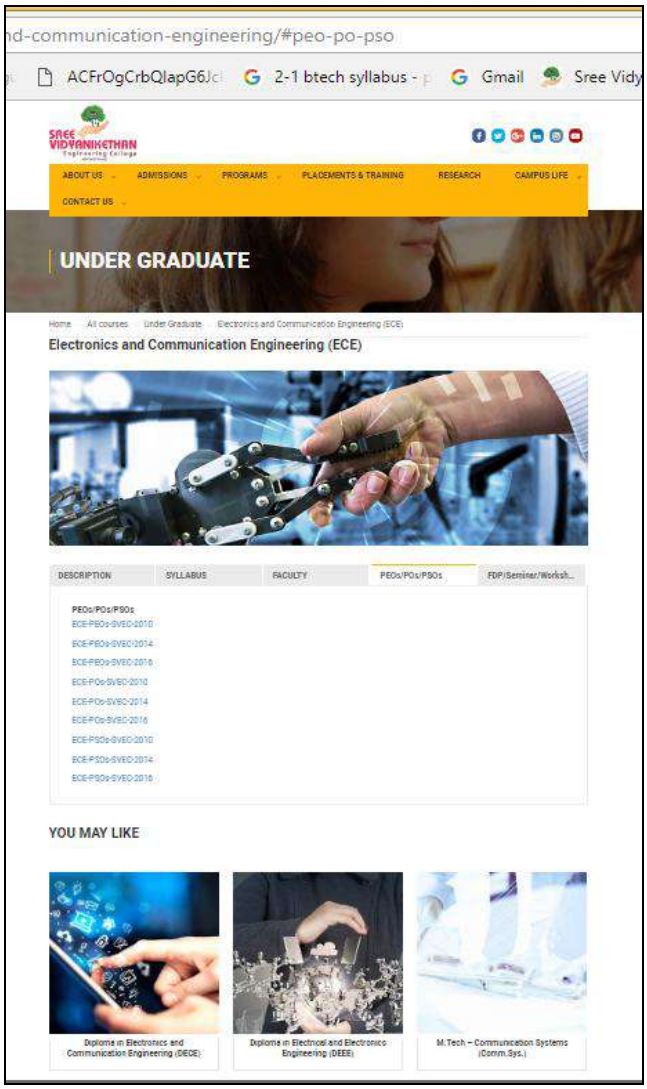
(i) Program outcomes, Program specific outcomes and course outcomes statements of all the programs are posted on the College Website and Department URL respectively - *Screen Shots and URL of Institution & Department depicting the above*

(ii) Method of Dissemination to students and members of faculty

Students: Through display boards, Department notice boards, laboratories, syllabus books, progress reports, seminar halls and brochures.

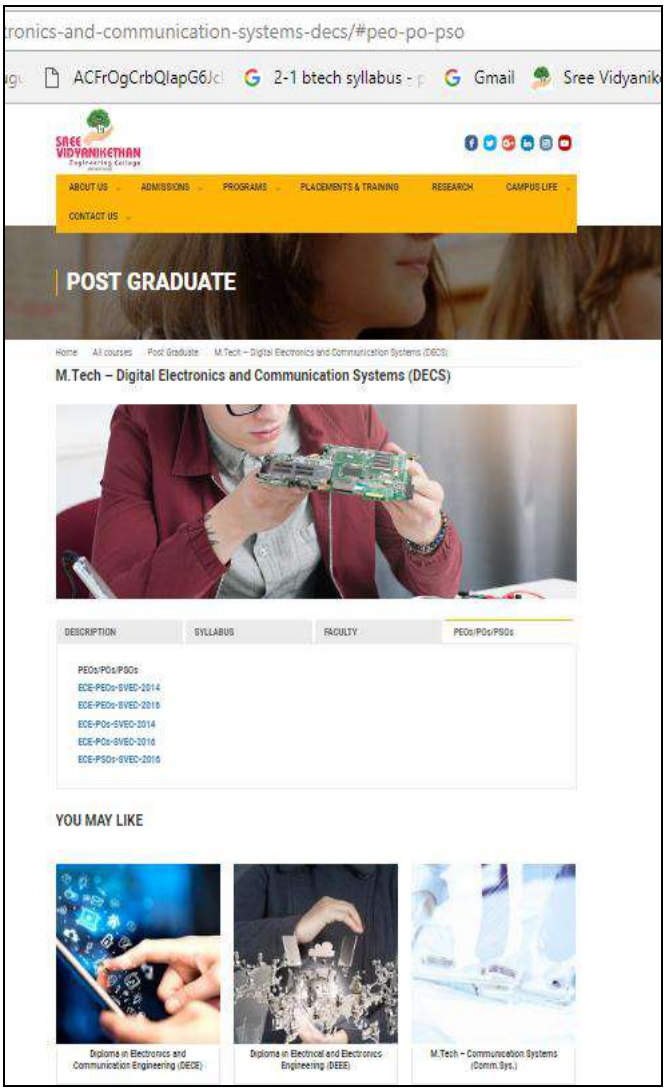
Teachers: Display boards, syllabus books, faculty course files, Department presentations and documents. - *Photographs of the Infrastructure, syllabus books and reports*

Method of Dissemination
➢ College Website ➢ Notice Boards ➢ Laboratories ➢ Syllabus Books ➢ Progress Reports ➢ Seminar halls and Brochures ➢ Statutory Compliances and Proposals ➢ Presentation Documents ➢ Formal communication ➢ Other Promotional Materials



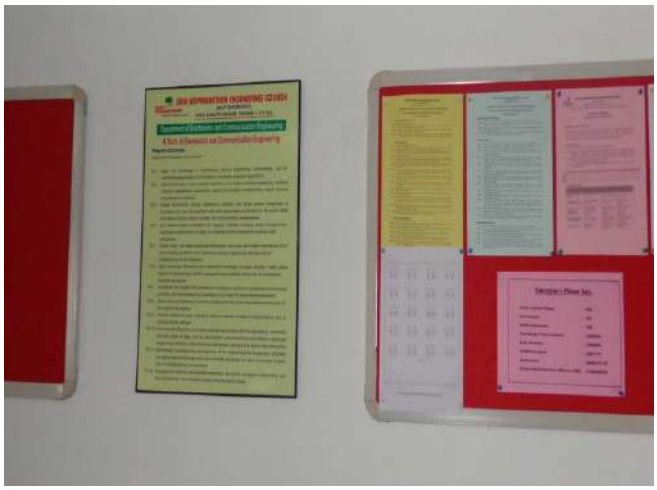
http://svec.education/courses/b-tech-electronics-and-communication-engineering/

Displayed in College Website

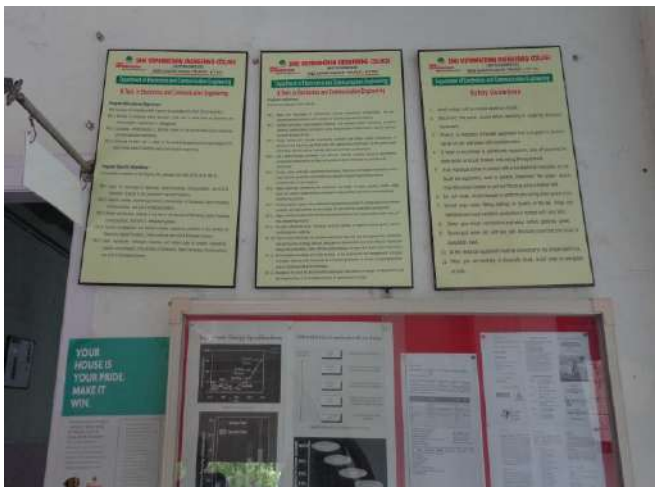


http://svec.education/courses/m-tech-digital-electronics-and-communication-systems-decs/#peo-po-pso

Displayed in College Website



Lab Notice boards

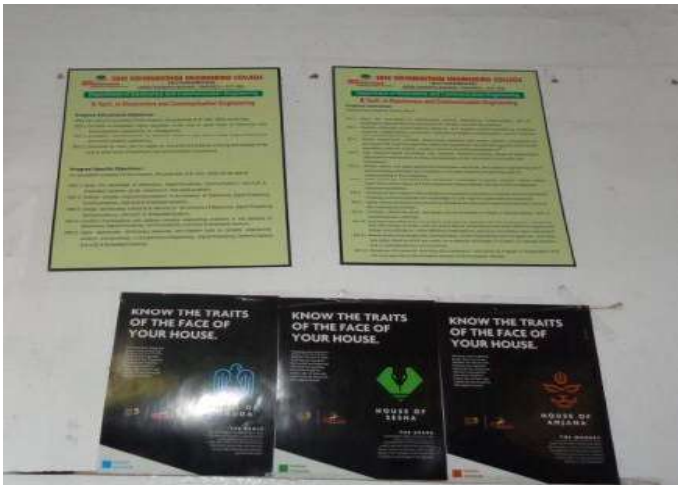


Notice boards

HOD Room

Class Rooms

Faculty Room



Corridor

Seminar Hall

Smart Class Room

Workshops

Auditorium

Principal Room

Progress Report

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, Tirupati – 517 102

STUDENT PROGRESS REPORT

To
S. Subbaiah,
D.No: 2-61, Mangalavandlapalli (V)
Sidhout (M), K.
Kadapa (Dt) – 516237

Roll. No. : 14121A0436

Name : S. Sudha Rani

Branch : ECE

Semester : III-I

Dear Parent / Guardian,

The details of August 2015 attendance, Assignment- I & Mid- I marks of your Son/Daughter Studying in III B. Tech I-Sem, ECE are furnished below for your information.

Attendance & Marks

Subject Name	Conducted Classes	Attended Classes	Assignment- I 10 Marks	Mid- I 20 Marks
Computer Architecture and Organization	36	32	10	14
Analog Communications	38	36	9	16
Antennas and Wave Propagation	34	32	5	12
Linear IC Applications	32	30	9	16
Digital IC Applications	38	35	6	14
Managerial Economics and Principles of Accountancy	32	30	8	17
Analog Communications Lab	27	27	-	-
Pulse and Digital Circuits Lab	24+1(P)	24+1	-	-
Total	262	247	47	89
Percentage	94.27		78	74

HOD, ECE

As per the SVEC (Autonomous) regulations, a student has to put in a minimum of 75% attendance in aggregate. A student securing less than 65% attendance will be detained.

S. Sudha Rani — S. Surendra

Signature of the Counselor — Signature of the Student — Signature of the Parent

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, Tirupati – 517 102

Department of Electronics and Communication Engineering

SVEC-10

B. Tech. (Electronics and Communication Engineering)

Program Educational Objectives:

After a few years of graduation,

PEO1: Graduates of the program will pursue higher education in the core and allied areas of Electronics and Communication Engineering.

PEO2: Graduates of the program will have successful technical careers in the fields related to Electronics and Communication Engineering.

PEO3: Graduates of the program will continue to learn and to adapt in a world of constantly evolving technology in the fields pertaining to Electronics and Communication Engineering.

Program Outcomes:

Engineering Graduates will be able to:

PO1: Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.

PO2: Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.

PO3: Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.

PO4: Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.

PO5: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.

PO6: Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.

PO7: Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.

PO8: Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.

PO9: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.

PO10: Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.

PO11: Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.

PO12: Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

Program Specific Outcomes:

Engineering Graduates will be able to:

PSO1: Apply the knowledge of Electronics, Signal Processing, Communications, and VLSI & Embedded Systems to the solutions of real world problems.

PSO2: Analyze complex engineering problems in the domains of Electronics, Signal Processing, Communications, and VLSI & Embedded Systems.

PSO3: Design and Develop solutions in real time in the domains of Electronics, Signal Processing, Communications, and VLSI & Embedded Systems.

PSO4: Conduct investigations and address complex engineering problems in the domains of Electronics, Signal Processing, Communications, and VLSI & Embedded Systems.

PSO5: Apply appropriate techniques, resources, and modern tools to complex engineering systems and processes in the domains of Electronics, Signal Processing, Communications, and VLSI & Embedded Systems.

Syllabus Book

VISION

To be one of the Nation's premier Engineering Colleges by achieving the highest order of excellence in Teaching and Research.

MISSION

- To foster intellectual curiosity, pursuit and dissemination of knowledge.
- To explore students' potential through academic freedom and integrity.
- To promote technical mastery and nurture skilled professionals to face competition in ever increasing complex world.

QUALITY POLICY

Sree Vidyanikethan Engineering College strives to establish a system of Quality Assurance to continuously address, monitor and evaluate the quality of education offered to students, thus promoting effective teaching processes for the benefit of students and making the College a Centre of Excellence for Engineering and Technological studies.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VISION

To be a center of excellence in Electronics and Communication Engineering through teaching and research producing high quality engineering professionals with values and ethics to meet local and global demands.

MISSION

- The Department of Electronics and Communication Engineering is established with the cause of creating competent professionals to work in multicultural and multidisciplinary environments.
- Imparting knowledge through contemporary curriculum and striving for development of students with diverse background.
- Inspiring students and faculty members for innovative research through constant interaction with research organizations and industry to meet societal needs.
- Developing skills for enhancing employability of students through comprehensive training process.
- Imbibing ethics and values in students for effective engineering practice.

PROGRAM EDUCATIONAL OBJECTIVES

After few years of completion of the Program, the graduates of B. Tech. (ECE) would have

1. Enrolled or completed higher education in the core or allied areas of electronics and communication engineering or management.
2. Successful entrepreneurial or technical career in the core or allied areas of electronics and communication engineering.
3. Continued to learn and to adapt to the world of constantly evolving technologies in the core or allied areas of electronics and communication engineering.

PROGRAM OUTCOMES

On successful completion of the Program, the graduates of B. Tech. (ECE) will be able to:

1. Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
2. Identify, formulate, research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
3. Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
4. Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
5. Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
6. Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
7. Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
8. Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
9. Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
10. Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
11. Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
12. Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

PROGRAM SPECIFIC OUTCOMES

On successful completion of the Program, the graduates of B. Tech. (ECE) will be able to:

1. Apply the knowledge of Electronics, Signal Processing, Communications, and VLSI & Embedded Systems to the solutions of real world problems.
2. Analyze, Design and Develop solutions in real time in the domains of Electronics, Signal Processing, Communications, and VLSI & Embedded Systems.
3. Conduct investigations and address complex engineering problems in the domains of Electronics, Signal Processing, Communications, and VLSI & Embedded Systems.
4. Apply appropriate techniques, resources, and modern tools to complex engineering systems and processes in the domains of Electronics, Signal Processing, Communications, and VLSI & Embedded Systems.