

About The College



Sree Vidyanikethan Engineering College was established in the year 1996 by Sree Vidyanikethan Educational Trust (SVET) under the stewardship of Padma Shri Dr. M. Mohan Babu, a renowned film artiste and former Member of Rajya Sabha with the noble objective of promoting Technical Education in the backward region of Rayalaseema. The College is situated 15 km from the temple-town of Tirupati on the Tirupati-Madanapalli National Highway (NH-205). For Every 10 minutes there is a bus from Tirupati bus station to Madanapalli.

The College is approved by All India Council for Technical Education, New Delhi and is affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapur. University Grants Commission (UGC), New Delhi has conferred Autonomy to the College for a period of six years with effect from the academic year 2010-2011. The College has been accredited by NAAC with 'A' Grade. Also the Institution is included in the list of colleges under 2(f) and 12(B) of UGC for funding from Govt. agencies.

The college is endowed with excellent infrastructure facilities for teaching, learning and research. Exemplary academic achievements of the College include consistent University gold medals and awesome student placements in top class industries.

Course offered:

The college is presently running 9 UG Programmes and 8 PG Programmes.

UG Programmes are: ECE, EEE, EIE, EConE, CSE, IT, CSSE Civil and Mechanical Engineering.

PG Programmes are: CS, SE, DECS, VLSI, EPS, CNIS, Com. Sys. and Bio-Technology.

Dept of Computer Science and Systems Engineering

The Department was established in the year 1999 with a modest intake of 40. The intake was increased to 60 in the year 2002 and 120 in the year 2012. The Department has well qualified and experience faculty. In every Academic Year about 80% of eligible students are placed in different organizations both in national and multi national companies. As part of continuing education, the Department organizes various Guest Lectures, Seminars and Workshops by inviting people from Academia and Industry. Department has its distinguished Alumni in National, Multinational, Government and Public sector units.

Objective of Work Shop

High Performance Computing presents new research field relating to high performance parallel and distributed systems used in both academia and industry. It has been the center of new discoveries in systems such as clusters, grids, clouds, parallel and multicore computers. Over all the applications of high performance computing are based on special type of planning which includes system selections, acceptance test, bench marks and may others. The current data growth rate signifies that high-performance computing will be coming to every industry sector sooner or later. This workshop will cover both hardware and software aspects of High Performance Computing.

Outcome of the workshop

1. Exposure to knowledge in advanced subject area.
2. It helps in designing B.Tech or M.Tech Curriculum.
3. It helps for setting up new laboratory in the area of High Performance Computing.
4. Interaction with subject experts and exchange of new ideas.

Contents of FDP

- ❖ Fundamentals of HPC-Parallel Computing
- ❖ Requirements, Issues and Services of HPC
- ❖ Fundamentals of Processor Architecture for HPC
- ❖ Power Architecture Overview and its current generation processor features
- ❖ Parallel Programming with MPI
- ❖ Introduction to Multicore Architectures
- ❖ Parallel programming with OpenMP
- ❖ Parallel Algorithms for Multicore Architectures
- ❖ Resource Management, Scheduling, Load Balancing and System Availability
- ❖ Green HPC
- ❖ HPC & Cloud

Eligibility

Faculty of CSE, IT, CSSE in all AICTE Approved Engineering Colleges and polytechnics.

Resource Persons

1. Dr. V.C.V. Rao, C-DAC, Pune.
2. Dr. Chapram Sudhakar, NIT, Warangal
3. Dr. M. Rajasekhar Babu, VIT, Vellore.
4. Dr. Amit Parashuram Kesarkar, NARL, Gadanki.

Registration

Participants are requested to fill up the registration form and mail to the co-ordinator.

General Information

- ❖ The number of participants is limited to 40.
- ❖ High Tea, snacks and lunch will be served as free of cost.
- ❖ Certificates will be issued to all the participants.
- ❖ No registration Fee

Important Dates

Last date for Registration : **23-08- 2013**

Confirmation to participants : **24-08-2013**

Workshop Dates : **30-31 , August 2013**

For further details:

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Sree Vidyanikethan Engineering College,
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Chief Patron
Padma shri Dr.M. Mohan Babu
Chairman, SVET

Patron
Dr. P. C. Krishnamachary
Principal, SVEC

Adviser
Dr.D.V.S. Bhagavanulu
Director

Convenor
Prof. C. Madhusudhana Rao
Head, Department of CSSE

Co-ordinator, TEQIP-II
Mr.A.K.Damodaram
Dept. of ME

Co-ordinator
Ms T. Kavitha
Dept. of CSSE

Organizing Committee

Ms. K. Lalitha, Asst. Professor(S.L)
Mr. A. Chandra, Asst. Professor
Ms. P. Dhanalakshmi, Asst. Professor
Ms. C. Sushama, Asst. Professor
Mr. S. Shiva Prakash, Asst. Professor
Mr. M. Ramu, Asst. Professor
Mr. B.D. Kalyan Kumar, Asst. Professor
Ms. C. Bhuvana, Asst. Professor
Mr. G. Amarnath, Asst. Professor
Mr. B. Narendra, Asst. Professor
Mr. P. Surya Chandra, Asst. Professor
Mr. Y. Ravi Kumar, Asst. Professor
Ms K. Padmaja, Asst. Professor

REGISTRATION FORM

Faculty Development Programme
On
High Performance Computing

30 - 31, August 2013

Under TEQIP-II

1.Name : _____

2.Designation : _____

3. Qualification : _____

4. Department : _____

5. Organization : _____

6. Phone No. : _____

7. Email : _____

8.Address for Communication

Place:

Date:

Signature of Applicant

*Signature of the Sponsoring
Authority with seal*

(Use Xerox copies if Necessary)

Faculty Development Programme **On** **High Performance Computing**

30 - 31, August 2013

Under TEQIP-II



Organized by

DEPARTMENT OF COMPUTER SCIENCE AND SYSTEMS ENGINEERING
SREE VIDYANIKETHAN ENGINEERING COLLEGE
(Autonomous)

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