Department of Electronics and Communication Engineering

## SVEC-14

# M. Tech. (VLSI)

### Program Outcomes:

On completion of the Programme, the students will be able to

**PO1:** Acquire in-depth knowledge in the specialized domain of Very Large Scale Analog, Digital and Mixed Signal Integrated Circuits.

**PO2:** Analyze complex engineering problems critically for conducting research in very large and ultra large scale integration technology.

**PO3:** Solve engineering problems to arrive at feasible and optimal solutions in the core areas of Very Large Scale Integration with consideration to societal and environmental factors.

**PO4:** Apply appropriate research methodologies and techniques to contribute for development of scientific and technological knowledge in Very Large Scale Integration and allied areas.

**PO5:** Use appropriate techniques, resources and modern tools to complex engineering activities in the field of Very Large Scale Integration.

**PO6:** Contribute to collaborative-multidisciplinary scientific research, demonstrate capacity for self-management, teamwork and decision making.

**PO7:** Manage projects as a member in a team and leader with the understanding of engineering and management principles with consideration to economic and financial factors.

**PO8:** Communicate effectively in professional and personal domains through verbal and written forms.

**PO9:** Engage in life-long learning to improve knowledge and competence in the world of rapid technological changes.

**PO10:** Follow ethical code of conduct in professional activities for sustainable development of society.

**PO11:** Adapt to reflective self learning for enhanced personal development.