SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)
Sree Sainath Nagar, Tirupati – 517 102

Department of Electronics and Communication Engineering

SVEC-14

M. Tech. (VLSI)

Program Educational Objectives:

PEO1: To impart advanced knowledge in the domains of Very Large Scale Analog, Digital and Mixed Signal Integrated Circuits.

PEO2: To develop analytical, problem solving, design and development skills in Very Large Scale Integration.

PEO3: To apply the knowledge and skills of Very Large Scale Integration for the solutions to the real world engineering problems.

PEO4: To provide opportunity to work with a strong sense of professionalism and code of engineering practice.